



Wall Industries, Inc.

# POL Series

Non-Isolated Point of Load Products  
16A Output Current Rating

- High efficiency - 95% @ 3.3V full load
- SMD & SIP packages
- Small size and low profile: 1.3" x 0.53" x 0.305" (SMD) 2.0" x 0.50" x 0.287" (SIP)
- Output voltage programmable from 0.75Vdc to 3.3Vdc via external resistor
- Delivers up to 16A of output current
- No minimum load required
- Low output ripple and noise
- Fixed switching frequency (300KHz)
- Lead free Directive compatible
- Remote ON/OFF
- Input under-voltage lockout
- Output overcurrent protection
- Over temperature protection
- Cost - efficient open frame design
- ISO 9001 certified manufacturing facilities
- UL 60950-1 and TUV(EN60950-1) and CB



## TECHNICAL SPECIFICATIONS

All specs are typical at nominal input, full load and 25°C unless otherwise noted

### OUTPUT SPECIFICATIONS

Output current			16A max
Voltage accuracy	Full load and $V_{in,min}$	$\pm 2\%V_{o,set}$	
Minimum load			0%
Line regulation	$V_{in}=V_{o,set}+0.5V$ to $V_{in,max}$ at Full Load	$\pm 0.3\%V_{o,set,typ}$	
Load regulation	0% to 100% FL	$\pm 0.4\%V_{o,set,typ}$	
Ripple and noise (Note1)	20MHz bandwidth	15mVrms,max 50mVp-p,max	
Temperature coefficient			$\pm 0.4\%$ , typ
Dynamic load response (Note1)	$\Delta I_o / \Delta t = 2.5A/uS$ , $V_{in,nom}$	Peak deviation	300mV, typ
	Load change step (50% to 100% or 100% to 50% of $I_{o,max}$ )	Setting time ( $V_o < 10\%$ peak deviation)	25uS, typ
Dynamic load response (Note2)	$\Delta I_o / \Delta t = 2.5A/uS$ , $V_{in,nom}$	Peak deviation	150mV, typ
	Load change step (50% to 100% or 100% to 50% of $I_{o,max}$ )	Setting time ( $V_o < 10\%$ peak deviation)	100uS, typ
Output current limit			220%, typ
Output short-circuit current	Hiccup, automatic recovery		
External load capacitance	ESR $\geq 1m\Omega$	1000uF,max	
	ESR $\geq 10m\Omega$	5000uF,max	
Output voltage overshoot-startup	$V_{in}=2.4\sim 5.5V$ , F.L.	1% $V_{o,set}$	
Voltage adjustability (see fig.1)	(Note3)	0.7525V ~ 3.63V	

### INPUT SPECIFICATIONS

Input voltage range	$V_{o,set} < V_{in} - 0.5V$	2.4 – 5.5VDC
Maximum input current	$V_{in}=2.4$ to 5.5V; $I_o=I_{o,max}$	16000mA
Input filter (Note 4)	C filter	
Input no load current ( $V_{in}=5V$ , $I_o=0$ , module enabled)	$V_{o,set}=0.75Vdc$	25mA,typ
	$V_{o,set}=3.3Vdc$	40mA,typ
Input stand-by current ( $V_{in}=5V$ , $I_o=0$ , module disabled)	1.5mA,typ	
Input undervoltage lockout	Start-up voltage	2.2V,typ
	Shutdown voltage	2.0V,typ
Input reflected ripple	5~20MHz, 1uH source impedance	100mA <sub>p-p</sub>

### GENERAL SPECIFICATIONS

Efficiency	See table	
Isolation voltage	None	
Switching frequency	300KHz, typ	
Sfety standards	IEC60950-1, UL60950-1, EN60950-1	
Dimensions	(SMD)	1.30 X 0.53 X 0.305 Inch (33.0 X 13.5 X 7.75 mm)
	(SIP)	2.0 X 0.50 X 0.287 Inch (50.8 X 12.7 X 7.3 mm)
Weight	6.0g(0.22oz)	
MTBF (Note 5)	1.428*10 <sup>7</sup> hrs	

### ENVIRONMENTAL SPECIFICATIONS

Operating temperature range	-40°C ~ +85°C
Storage temperature range	-55°C ~ +125°C
Thermal shock	MIL-STD-810D
Over temperature protection	125°C, typ

### FEATURE SPECIFICATIONS

Remote ON/OFF(Note 6)		
(Positive logic)	ON = $V_{in,max}$ , $I_{IN}=10\mu A,max.$ OFF = $0V < V_r < 0.3V$ , $I_{IN}=1mA,max.$	
(Negative logic)	ON = $0V < V_r < 0.3V$ , $I_{IN}=1mA,max.$ OFF = $1.5V < V_r < V_{in,max}$ , $I_{IN}=10\mu A, max$	
Remote sense range	0.5V,max	
Rise time	Time for $V_o$ to rise from 10% to 90% of $V_{o,set}$	6msec,max
Turn-on delay time	Case 1 (Note7)	1msec,typ
	Case 2 (Note8)	1msec,typ

Model Name	ON/OFF Logic	Package	Input Voltage	Output Voltage	Output Current	Efficiency (%) 5Vin, 3.3Vdc@16A
POLS16-05T	Negative	SMD	2.4 ~ 5.5Vdc	0.75 ~ 3.3Vdc	16A	95%
POLS16-05T-P	Positive					
POLT16-05T	Negative	SIP	2.4 ~ 5.5Vdc	0.75 ~ 3.3Vdc	16A	95%
POLT16-05T-P	Positive					

Note

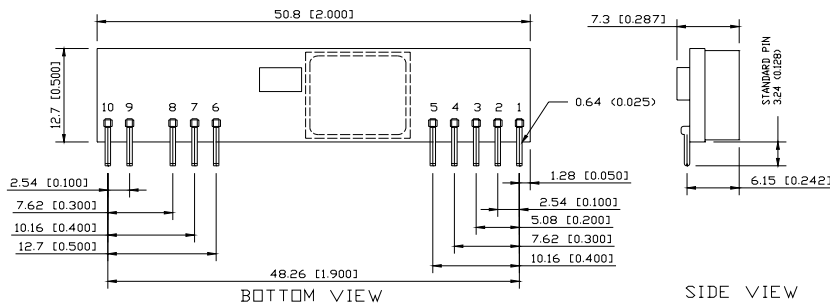
- External with C<sub>out</sub> = 1uF ceramic // 10uF tantalum capacitors.
- External with C<sub>out</sub> = 2x150uF polymer capacitors.
- Output voltage programmable from 0.75V to 3.3V by connecting a single resistor (shown as R<sub>trim</sub> in Table 1) between the TRIM and GND pins of the module. To calculate the value of the resistor **R<sub>trim</sub>** for a particular output voltage **V<sub>o</sub>**, use the following equation:

$$R_{trim} = \left[ \frac{21070}{V_o - 0.7525} - 5110 \right] \Omega$$

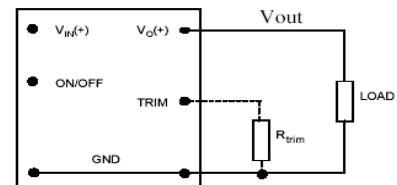
- To minimize input voltage ripple, Low-ESR polymer and ceramic capacitors are recommended at the input of the module. We suggest external C<sub>in</sub> = 2x150uF polymer capacitors and 2x47uF ceramic capacitors.
- BELLCORE TR-NWT-000332. Case I: 50% Stress, Temperature at 25°C. (Ground fixed and controlled environment).
- Device code with suffix “-P” – Positive logic (On/Off is open collector/drain logic input; Signal referenced to GND).  
Device code with no suffix – Negative logic (On/Off pin is open collector/drain logic input with external pull-up resistor; signal referenced to GND).
- Case 1 :On/Off input is set to logic low (module on) and then input power is applied (delay from instant at which Vin=Vin,min until Vo=10% of Vo,set).
- Case 2 :Input power is applied for at least one second and then the On/Off input is set to logic low (delay from instant at which Von/off=0.3V until Vo=10% of Vo,set).

**CAUTION:** This power module is not internally fused. An input line fuse must always be used.

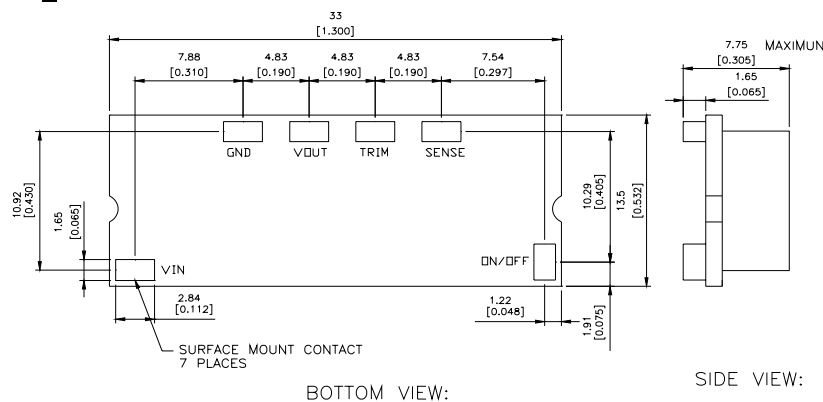
**POLT16-05T**



POLT16-05T	
PIN	FUNCTION
1	V <sub>o</sub>
2	V <sub>o</sub>
3	+SENSE
4	V <sub>o</sub>
5	GND
6	GND
7	V <sub>in</sub>
8	V <sub>in</sub>
9	Trim
10	ON/OFF



**POLS16-05T**



Dimensions are in millimeter and (inches).  
Tolerances : x.x mm ±0.5mm (x.xx in ±0.02in) [unless otherwise indicated],  
x.xx mm ±0.25mm (x.xxx in ±0.01in).

Figure1

Table 1	
V <sub>o,set</sub> (V)	R <sub>trim</sub> (KΩ)
0.7525	Open
1.2	41.973
1.5	23.077
1.8	15.004
2.5	6.974
3.3	3.160