

Vishay Semiconductors

Schottky Rectifier, 1.0 A



SMB	
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PRODUCT SUMMARY				
Package	DO-214AA (SMB)			
I _{F(AV)}	1 A			
V _R	90 V, 100 V			
V _F at I _F	0.78 V			
I _{RM}	1 mA at 125 °C			
T _J max.	175 °C			
Diode variation	Single die			
E _{AS}	1.0 mJ			

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\text{C}$
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-MBRS190TRPbF, VS-MBRS1100TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	А		
V _{RRM}		90/100	V		
I _{FSM}	t _p = 5 μs sine	870	А		
V _F	1.0 Apk, T _J = 125 °C	0.63	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRS190TRPbF	VS-MBRS1100TRPbF	UNITS	
Maximum DC reverse voltage	V _R	90	100	V	
Maximum working peak reverse voltage	V _{RWM}	90	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 147 °C, rectangular waveform 1.0			
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	870	А
	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	50	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH 1.0		1.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical 0.5		А	

For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS VALUES		UNITS		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.78	V	
See fig. 1	¥FM (*)		T _J = 125 °C	0.62	v	
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated V _B	0.5	mA	
See fig. 2	IRM ("	IRM (*/	$T_J = 125 \text{ °C}$	v _R = naleu v _R	1.0	ША
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C 42 pF		pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0 nH		nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μs			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C AN
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approvimate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	V19/	V10

Notes

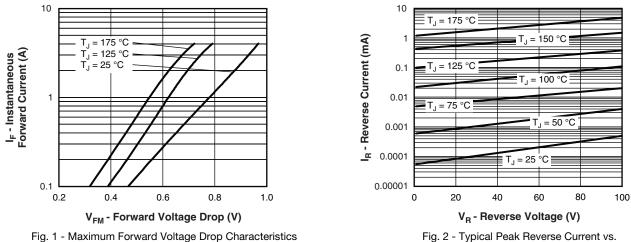
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

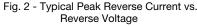
(2) Mounted 1" square PCB



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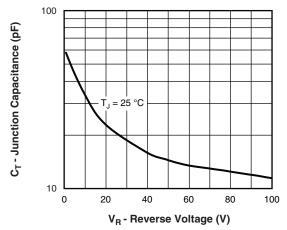


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

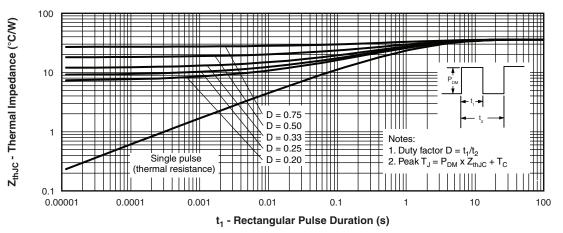
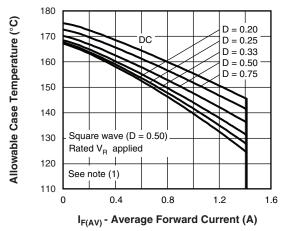


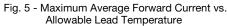
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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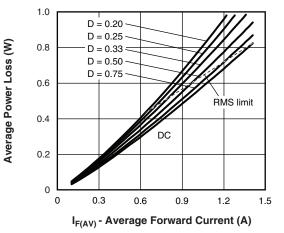


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

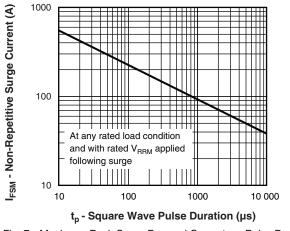


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

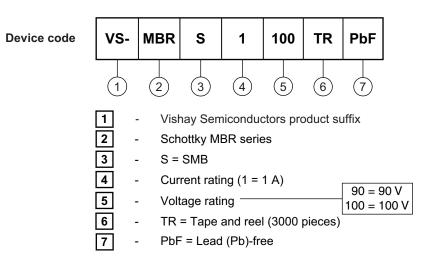
- (1)
- $\begin{array}{l} \mbox{Formula used: } T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6)}; \\ Pd_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 D)}; \mbox{ } I_R \mbox{ at } V_{R1} = 80 \ \% \mbox{ rated } V_R \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95017			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

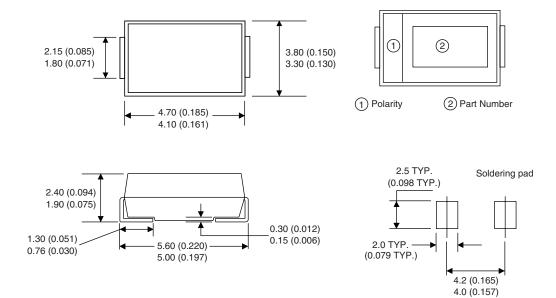


Outline Dimensions

Vishay High Power Products

SMB

DIMENSIONS in millimeters (inches)





Vishay

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