

Surge chip resistor size 1206

SRC01 5%; 2%

FEATURES

- Reduced size of final equipment
- Low assembly costs
- Higher component and equipment reliability
- Excellent performance at high frequency.

APPLICATIONS

- Power supply in small sized equipment
- Telecommunication
- Medical and Military
- Automotive industry.

DESCRIPTION

The resistors are constructed on a high grade ceramic body (aluminium oxide). Internal metal electrodes are added at each end and connected by a resistive paste which is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance, by laser cutting of this resistive layer.

The resistive layer is covered with a protective coating and printed with the resistance value. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a lead/tin alloy.

QUICK REFERENCE DATA

DESCRIPTION	VALUE
Resistance range	1 Ω to 100 k Ω
Resistance tolerance	$\pm 5\%$; $\pm 2\%$ E24 series
Temperature coefficient	$\leq \pm 200 \times 10^{-6}/K$
Absolute maximum dissipation at $T_{amb} = 70^{\circ}C$	0.25 W
Maximum permissible voltage	200 V (DC or RMS)
Climatic category (IEC 60068)	55/155/56
Basic specification	IEC 60115-8

ORDERING INFORMATION

Table 1 Ordering code indicating resistor type and packaging

TYPE	RESISTANCE RANGE	TOL. (%)	ORDERING CODE 2350 550
			PAPER TAPE ON REEL
			5000 units
SRC01	1 Ω to 100 k Ω	5	10 ...
		2	11 ...

Ordering code (12NC)

- The resistors have a 12-digit ordering code starting with 2350 550.
- The subsequent 2 digits indicate the resistor type and packaging; see Table 1.
- The remaining 3 digits indicate the resistance value:
 - The first 2 digits indicate the resistance value.
 - The last digit indicates the resistance decade in accordance with Table 2.

Table 2 Last digit of 12 NC

RESISTANCE	LAST DIGIT
1 to 9.76 Ω	8
10 to 97.6 Ω	9
100 to 976 Ω	1
1 to 9.76 k Ω	2
10 to 97.6 k Ω	3
100 k Ω	4

ORDERING EXAMPLE

The ordering code of a SRC01 resistor, value 750 Ω with a tolerance of 5%, supplied on paper tape of 5000 units per reel is:
2350 550 10751.

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FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of $\pm 5\%$ or 2% . The values of the E24 series are in accordance with "IEC publication 60063".

Limiting values

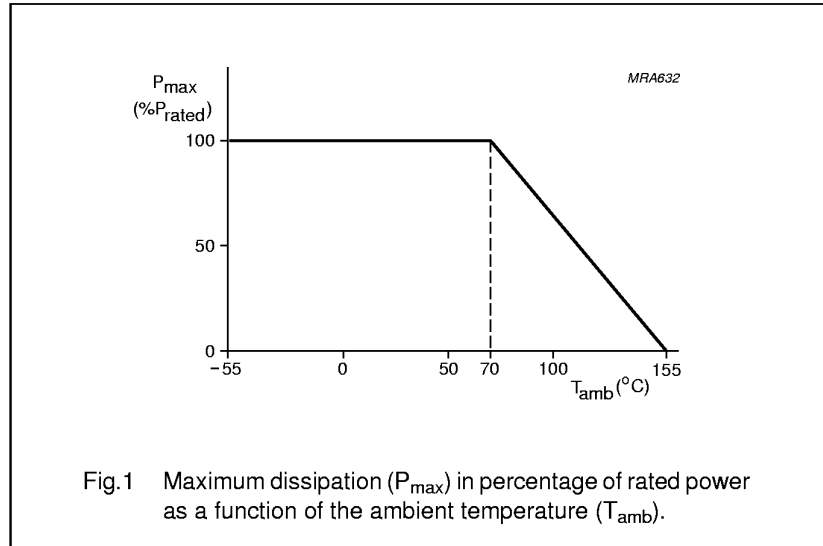
TYPE	LIMITING VOLTAGE ⁽¹⁾ (V)	LIMITING POWER (W)
SRC01	200	0.25

Note

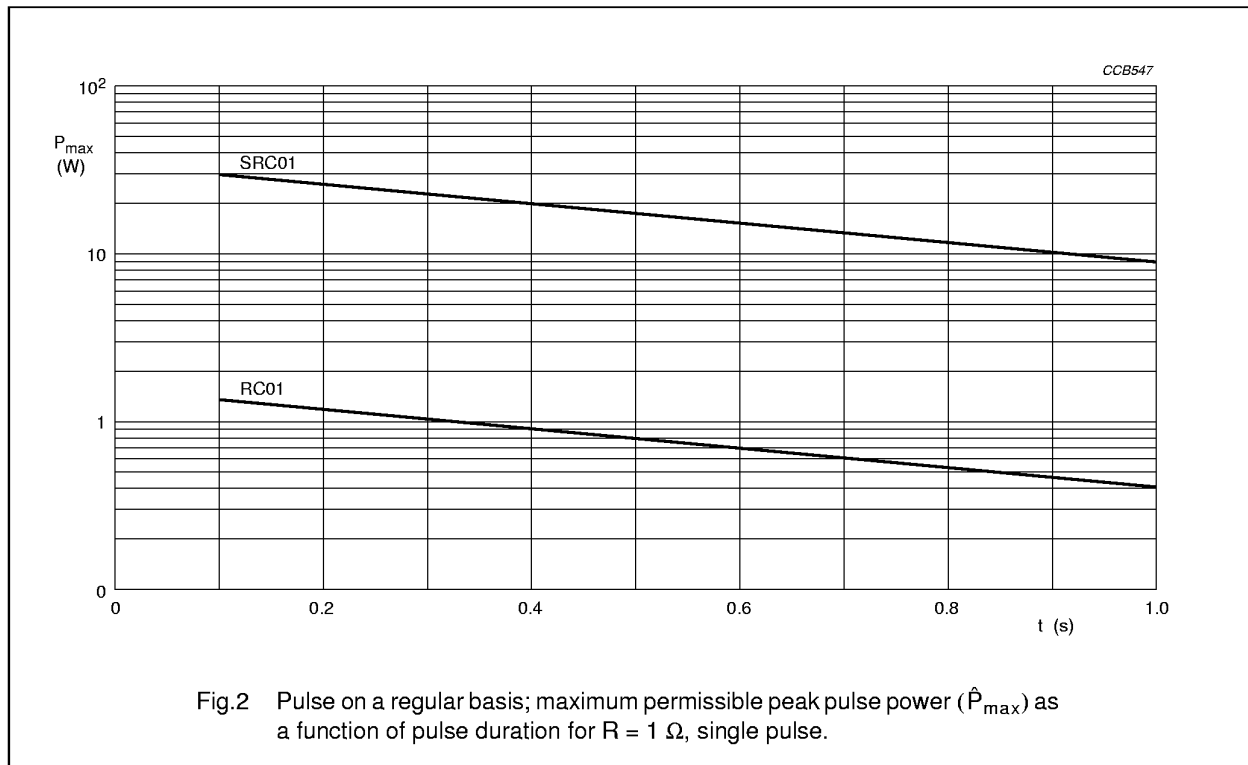
1. This is the maximum voltage that may be continuously applied to the resistor element, see "IEC publication 60115-8".

DERATING

The power that the resistor can dissipate depends on the operating temperature; see Fig.1.



PULSE LOADING CAPABILITIES



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MECHANICAL DATA

Mass per 100 units

TYPE	MASS (g)
SRC01	1.0

Marking

All resistors are marked with a 3-digit code on the protective coat to designate the nominal resistance value.

3-DIGIT MARKING

For values up to 1 k Ω the R is used as a decimal point. For values of 1 k Ω or greater the first 2 digits apply to the resistance value and the third indicates the number of zeros to follow.

Example

MARKING	RESISTANCE
10R	10 Ω
102	1 k Ω
104	100 k Ω

PACKAGE MARKING

The packaging is also marked and includes resistance value, tolerance, catalogue number, quantity, production period, batch number and source code.

Outlines

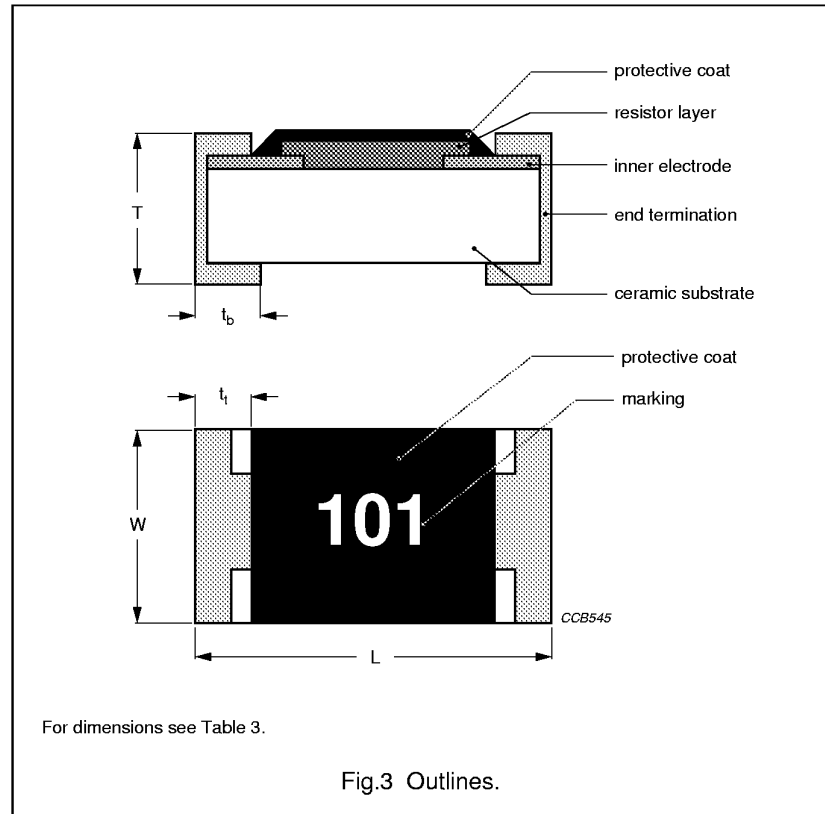


Table 3 Chip resistor type and relevant physical dimensions; see Fig.3

TYPE	L (mm)	W (mm)	T (mm)	t _t (mm)	t _b (mm)
SRC01	3.20 +0.10/-0.20	1.60 ±0.15	0.55 ±0.10	0.45 ±0.25	0.50 ±0.25

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TESTS AND REQUIREMENTS

Essentially all tests are carried out in accordance with the schedule of "IEC publication 60115-8", category 55/155/56 (rated temperature range -55 to +155 °C; damp heat, long term, 56 days). The testing also covers the requirements specified by EIA and EIAJ.

The tests are carried out in accordance with IEC publication 60068, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to "IEC 60068-1", subclause 5.3.

Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C

Relative humidity: 45% to 75%

Air pressure: 86 kPa to 106 kPa.

In Table 4 the tests and requirements are listed with reference to the relevant clauses of "IEC publications 60115-8 and 60068", a short description of the test procedure is also given. In some instances deviations from the IEC recommendations were necessary for our method of specifying.

All soldering tests are performed with mildly activated flux.

Table 4 Test procedures and requirements

IEC 60115-8 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
Tests in accordance with the schedule of IEC publication 60115-8				
4.4.1		visual examination		no holes; clean surface; no visible damage
4.5		resistance	applied voltage (+0/-10%): 0.1 V	$R - R_{nom}$: max. $\pm 5\%$
4.18	20 (Tb)	resistance to soldering heat	unmounted chips; 10 ± 1 s; 260 ± 5 °C	no visible damage $\Delta R/R$ max.: $\pm(1\% + 0.05 \Omega)$
4.29	45 (Xa)	component solvent resistance	isopropyl alcohol; H ₂ O	no visible damage
4.17	20 (Ta)	solderability	unmounted chips completely immersed for 2 ± 0.5 s in a solder bath at 235 ± 2 °C	good tinning ($\geq 95\%$ covered); no damage
4.17	20 (Ta)	solderability (after ageing)	8 hours steam or 16 hours at 155 °C; unmounted chips completely immersed for 2 ± 0.5 s in a solder bath at 235 ± 2 °C	good tinning ($\geq 95\%$ covered); no damage
4.7		voltage proof on insulation	200 V (RMS) during 1 minute	no breakdown or flashover
4.13		short time overload	room temperature; dissipation $6.25 \times P_n$; 5 s (voltage not more than $2 \times V_{max}$)	$\Delta R/R$ max.: $\pm(2\% + 0.1 \Omega)$
4.33	(JIS) C 5200	bending	resistors mounted on a 90 mm glass epoxy resin printed-circuit board; bending: 5 mm	no visible damage $\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$
4.19	14 (Na)	rapid change of temperature	30 minutes at LCT and 30 minutes at UCT; 5 cycles	no visible damage $\Delta R/R$ max.: $\pm(1.5\% + 0.05 \Omega)$
4.6.1.1		insulation resistance	100 V (DC) after 1 minute	R_{ins} min.: 1000 M Ω
4.24.2	3 (Ca)	damp heat (steady state)	56 days; 40 ± 2 °C; 93 +2/-3% RH; loaded with $0.01 P_n$	R_{ins} min.: 1000 M Ω $\Delta R/R$ max.: $\pm(1.5\% + 0.05 \Omega)$

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4.25.1		endurance	1 000 +48/-0 hours; 70 ±2 °C; loaded with P _n or V _{max} ; 1.5 hours on and 0.5 hours off	ΔR/R max.: ±(1% +0.05 Ω)
4.23.2	27 (Ba)	endurance at upper category temperature	1 000 +48/-0 hours; 155 °C; no load	no visible damage ΔR/R max.: ±(1% +0.05 Ω)
4.8.4.2		temperature coefficient	at 20/LCT/20 °C and 20/UCT/20 °C	≤±200 × 10 ⁻⁶ /K
Other applicable tests				
		leaching	unmounted chips 60 ±1 s; 260 ±5 °C	good tinning; no leaching
	(JIS) C 5202 7.5	resistance to damp heat (steady state)	1 000 +48/-0 hours; 40 ±2 °C; 93 +2/-3% RH; loaded with P _n or V _{max} ; 1.5 hours on and 0.5 hours off	ΔR/R max.: ±3%