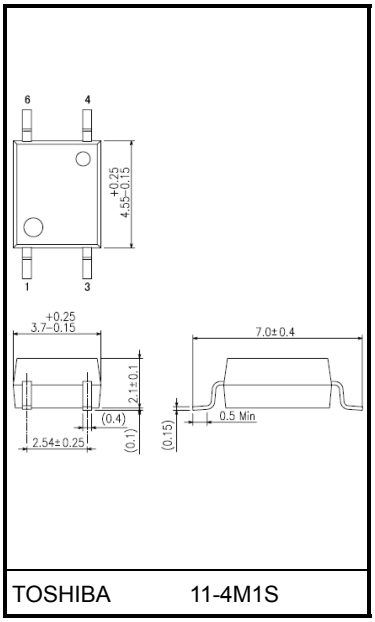
TOSHIBA Photocoupler GaAs Ired & Photo−Transistor

TLP185

Office Machine
Programmable Controllers
AC Adapter
I/O Interface Board

Unit: mm



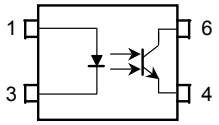
TOSHIBA 11-4M1S

Weight: 0.08 g (Typ.)

The TOSHIBA mini flat coupler TLP185 is a small outline coupler, suitable for surface mount assembly.
TLP185 consist of a photo transistor optically coupled to a gallium arsenide infrared emitting diode. Since TLP185 is smaller than DIP package, it's suitable for high-density surface mounting applications such as programmable controllers

- Collector−emitter voltage: 80V (min)
- Current transfer ratio: 50% (min)
  Rank GB: 100% (min)
- Isolation voltage: 3750Vrms (min)
- Operation Temperature:-55 to 110 ˚C
- Safety Standards
  UL approved: UL1577, File No. E67349
  cUL approved: CSA Component Acceptance Service No. 5A
  File No.E67349
- BSI approved :BS EN60065:2002, Certificate No. 9020
  BS EN60950-1:2006, Certificate No. 9021
- Option (V4) type
  VDE approved: EN60747-5-2, Certificate No. 40009347
  (Note): When a EN60747-5-2 approved type is needed,
  Please designate "Option(V4)"
- Construction mechanical rating
  Creepage distance : 5.0 mm(min)
  Clearance : 5.0 mm(min)
  Insulation thickness : 0.4 mm(min)

Pin Configuration(top view)



1: Anode
3: Cathode
4: Emitter
6: Collector

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Current Transfer Ratio

Type	Classification Note1	Current Transfer Ratio (%) (I_C / I_F) I_F = 5mA, V_{CE} = 5V, Ta = 25°C		Marking Of Classification
		Min	Max	
TLP185	Blank	50	400	Blank
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank GB	100	400	GB
	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	B

(Note1): Ex Rank GB: TLP185 (GB,E

(Note) Application, type name for certification test, please use standard product type name, i, e.
TLP185(GB,E: TLP185

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Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
LED	Forward current		I_F	50	mA
	Forward current derating (Ta ≥ 90°C)		ΔI_F / °C	-1.5	mA / °C
	Pulse forward current	(Note2)	I_{FP}	1	A
	Reverse voltage		V_R	5	V
	Junction temperature		T_j	125	°C
Detector	Collector−emitter voltage		V_{CEO}	80	V
	Emitter−collector voltage		V_{ECO}	7	V
	Collector current		I_C	50	mA
	Collector power dissipation		P_C	150	mW
	Collector power dissipation derating	(Ta ≥ 25°C)	ΔP_C / °C	-1.5	mW / °C
	Junction temperature		T_j	125	°C
Operating temperature range			T_{opr}	−55 to 110	°C
Storage temperature range			T_{stg}	−55 to 125	°C
Lead soldering temperature			T_{sol}	260 (10s)	°C
Total package power dissipation			P_T	200	mW
Total package power dissipation derating (Ta ≥ 25°C)			ΔP_T / °C	-2.0	mW / °C
Isolation voltage (AC, 1min., R.H. ≤ 60%)		(Note 3)	BV_S	3750	V_{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Pulse width ≤ 100 μs,f=100 Hz

Note 3: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

Recommended Operating Conditions (Note)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V_{CC}	—	5	48	V
Forward current	I_F	—	16	20	mA
Collector current	I_C	—	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V_F	I_F = 10 mA	1.1	1.25	1.4	V
	Reverse current	I_R	V_R = 5 V	—	—	5	μA
	Capacitance	C_T	V = 0, f = 1 MHz	—	30	—	pF
Detector	Collector−emitter breakdown voltage	$V_{(BR)\ CEO}$	I_C = 0.5 mA	80	—	—	V
	Emitter−collector breakdown voltage	$V_{(BR)\ ECO}$	I_E = 0.1 mA	7	—	—	V
	Collector dark current	I_{CEO}	V_{CE} = 48 V	—	0.01	0.08	μA
			V_{CE} = 48 V, Ta = 85°C	—	2	50	μA
	Capacitance (collector to emitter)	C_{CE}	V = 0, f = 1 MHz	—	10	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	MIn	Typ.	Max	Unit
Current transfer ratio	I_C / I_F	I_F = 5 mA, V_{CE} = 5 V	50	—	400	%
		Rank GB	100	—	400	
Saturated CTR	I_C / $I_{F\ (sat)}$	IF = 1 mA, V_{CE} = 0.4 V	—	60	—	%
		Rank GB	30	—	—	
Collector−emitter saturation voltage	$V_{CE\ (sat)}$	I_C = 2.4 mA, I_F = 8 mA	—	—	0.3	V
		I_C = 0.2 mA, I_F = 1 mA	—	0.2	—	
		Rank GB	—	—	0.3	
Off−state collector current	$I_{C\ (off)}$	V_F = 0.7V, V_{CE} = 48 V	—	1	10	μA

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance (input to output)	C_S	V_S = 0V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R_S	V_S = 500 V, R.H. ≤ 60%	1×10^{12}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 1 minute	3750	—	—	V_{rms}
		AC, 1 second, in oil	—	10000	—	
		DC, 1 minute, in oil	—	10000	—	V_{dc}

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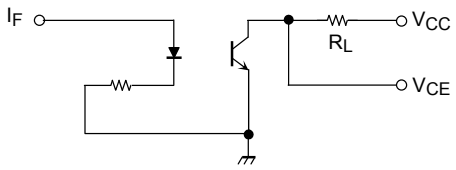
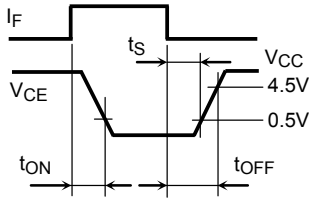
Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	t_r	V_{CC} = 10 V, I_C = 2 mA R_L = 100Ω	—	2	—	μs
Fall time	t_f		—	3	—	
Turn−on time	t_{on}		—	3	—	
Turn−off time	t_{off}		—	3	—	
Turn−on time	t_{ON}	R_L = 1.9 kΩ (Fig.1) V_{CC} = 5 V, I_F = 16 mA	—	2	—	μs
Storage time	t_s		—	25	—	
Turn−off time	t_{OFF}		—	40	—	

Fig. 1 Switching time test circuit





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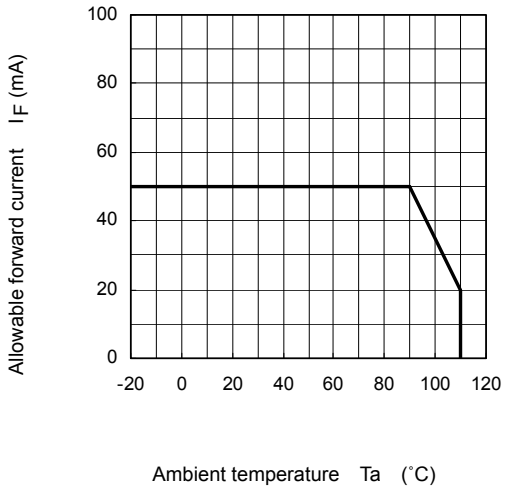
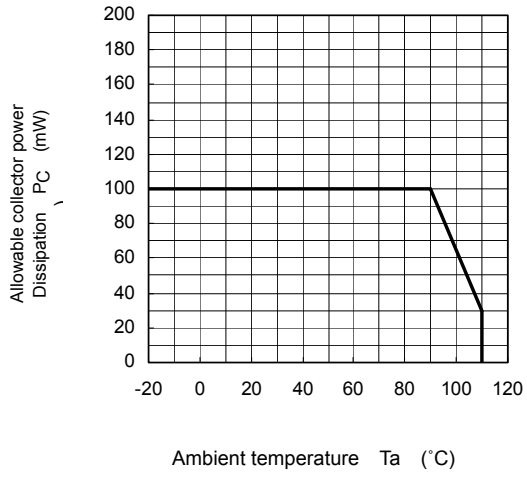
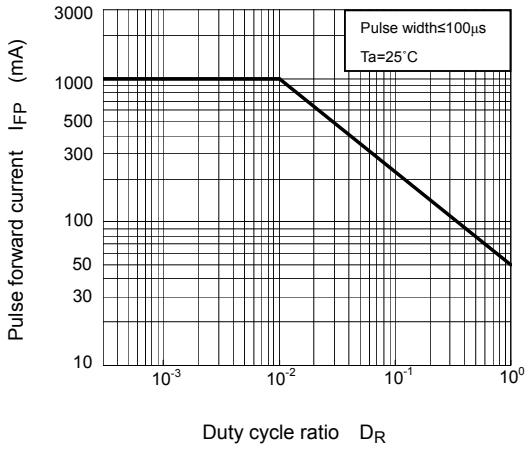
I_F - Ta



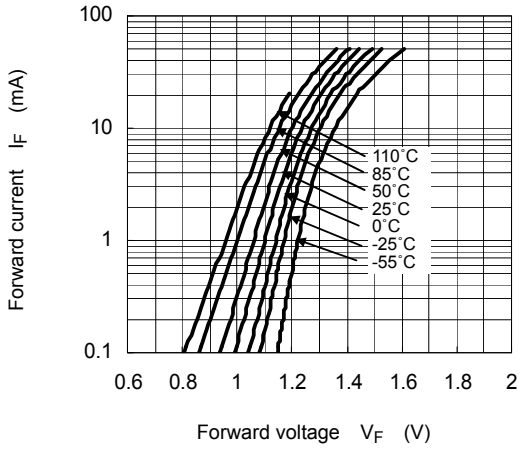
P_C - Ta



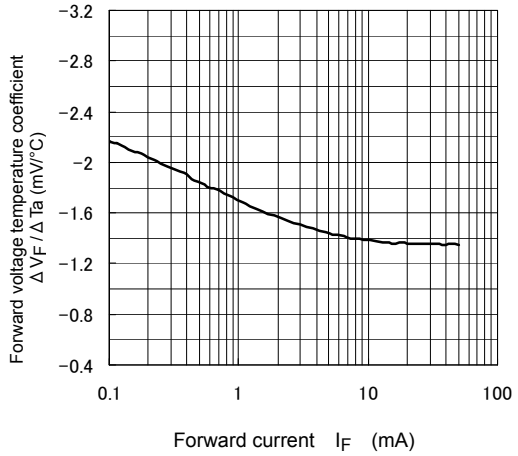
I_{FP} - D_R



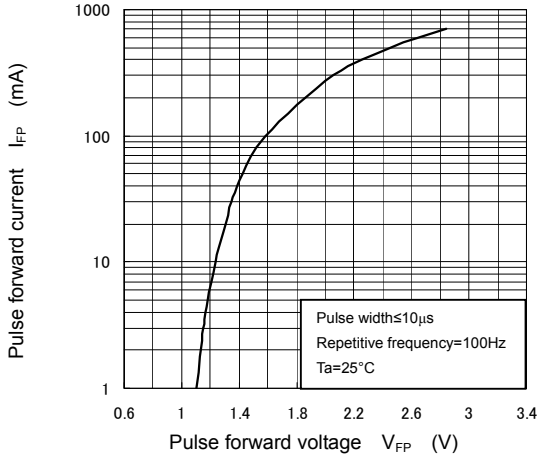
I_F - V_F



$\Delta V_F / \Delta Ta$ - I_F



I_{FP} – V_{FP}



*The above graphs show typical characteristic.

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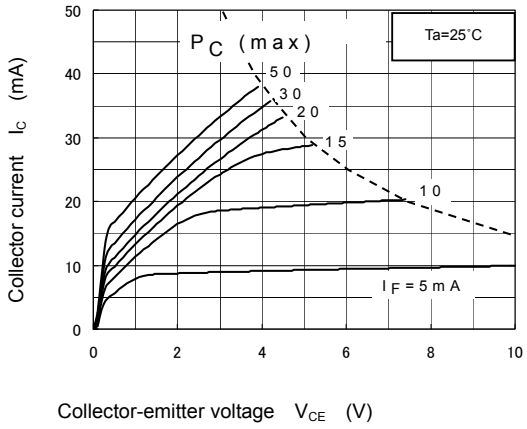
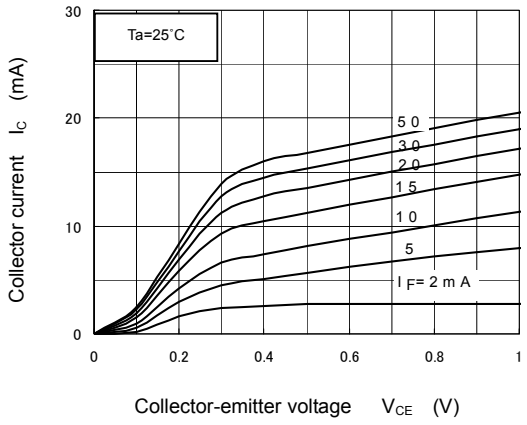
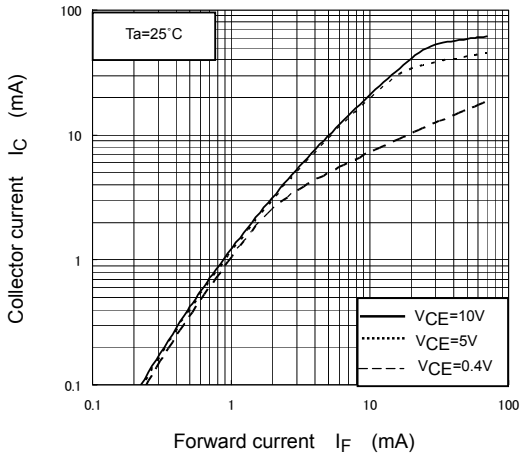
I_C-V_{CE}



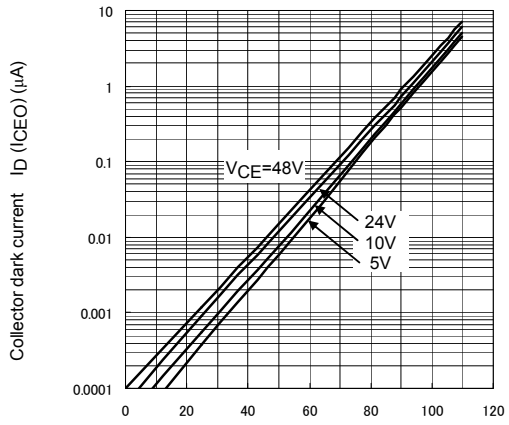
I_C-V_{CE}



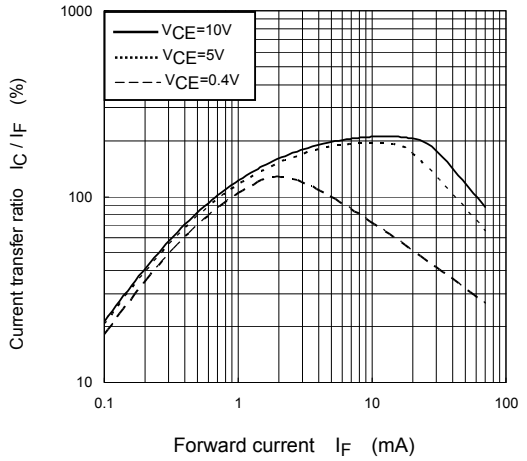
I_C-I_F



ICEO-Ta



I_C / I_F -I_F



*The above graphs show typical characteristic.

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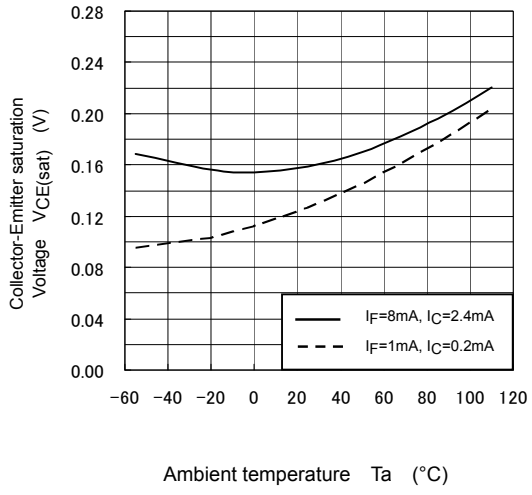
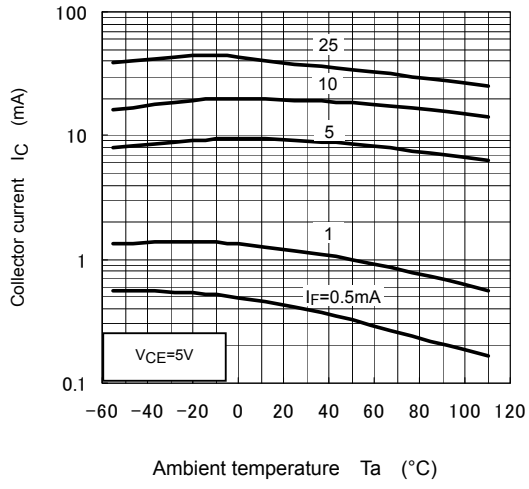
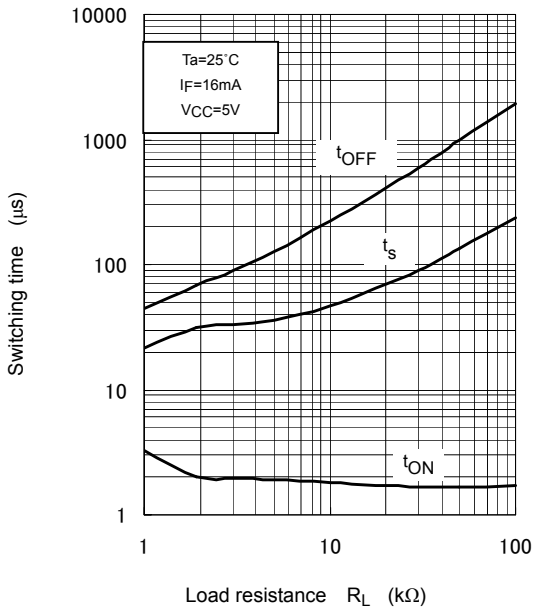
$V_{CE(sat)}$ - Ta



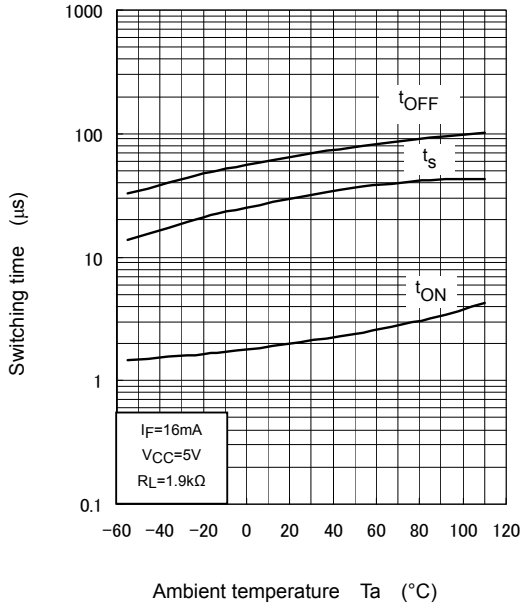
I_C - Ta



Switching time - R_L



Switching time - Ta



*The above graphs show typical characteristic.

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Soldering and Storage

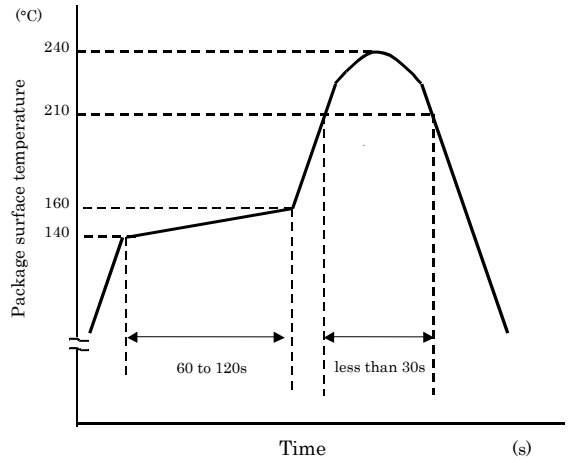
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

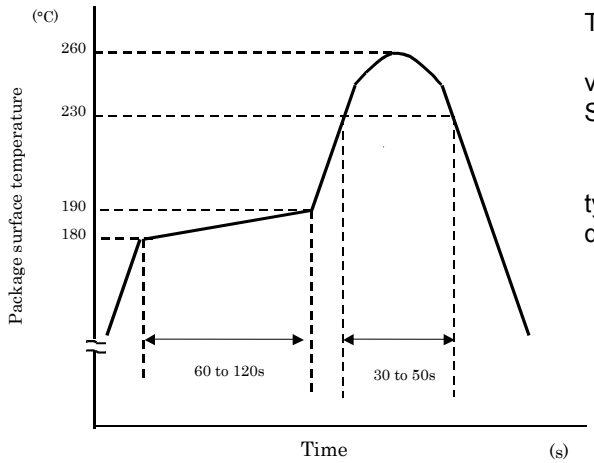
1) Using solder reflow

·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

Please preheat it at 150°C between 60 and 120 seconds.

Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

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2. Storage

1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
2) Follow the precautions printed on the packing label of the device for transportation and storage.
3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
6) When restoring devices after removal from their packing, use anti-static containers.
7) Do not allow loads to be applied directly to devices while they are in storage.
8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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