

C5750X7R2E105K230KE

--- Multilayer Ceramic Chip Capacitor (MLCC) ---



Application & Main Feature

Commercial Grade (Soft Termination)

Series

C5750 [EIA CC2220]



Dimensions

L 5.70mm +0.5,-0.4mm
 W 5.00mm +/-0.4mm
 T 2.30mm +0.3,-0.2mm
 B 0.20mm Min.
 G

Temperature Characteristic

X7R (-55 to 125 degC +/-15%)

Rated Voltage

2E (250Vdc)

Capacitance

1uF

Capacitance Tolerance

K (+/-10%)

Dissipation Factor

3% Max.

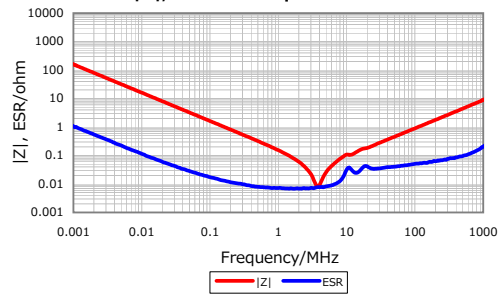
Insulation Resistance

500Mohm Min.

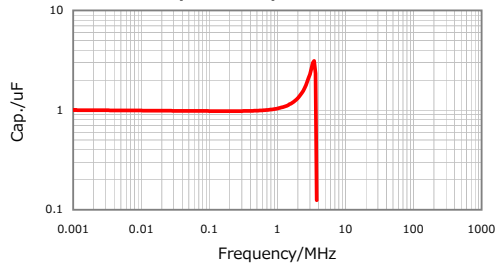
AEC-Q200

Not Applicable

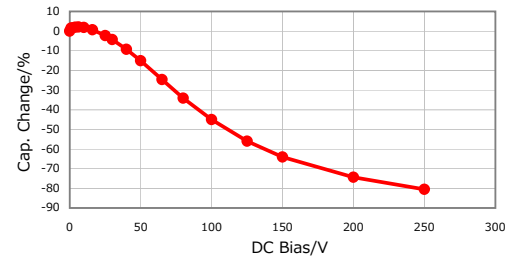
|Z|, ESR vs. Freq. Characteristics



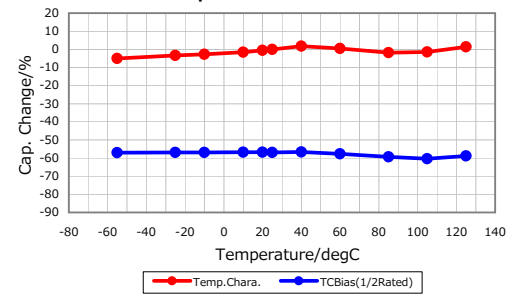
Cap. vs. Freq. Characteristic



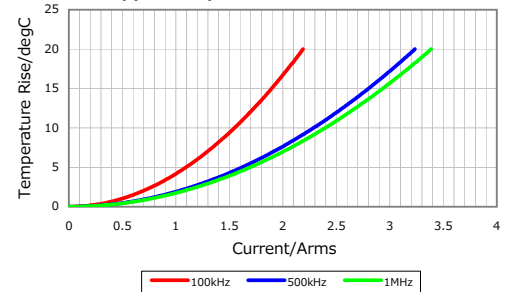
DC Bias Characteristic



Temperature Characteristic



Ripple Temperature Rise Characteristic



Characterization Sheet



⚠ All specifications are subject to change without notice.

December 22, 2012