

# C2012X7R1E105M125AB

--- Multilayer Ceramic Chip Capacitor (MLCC) ---

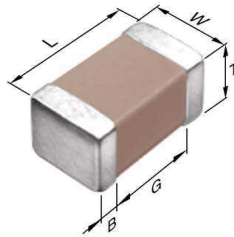


## Application & Main Feature

Commercial Grade ( General (Up to 50V) )

## Series

C2012 [EIA CC0805]



## Dimensions

L 2.00mm +/-0.2mm  
 W 1.25mm +/-0.2mm  
 T 1.25mm +/-0.2mm  
 B 0.20mm Min.  
 G 0.50mm Min.

## Temperature Characteristic

X7R (-55 to 125 degC +/-15%)

## Rated Voltage

1E (25Vdc)

## Capacitance

1uF

## Capacitance Tolerance

M (+/-20%)

## Dissipation Factor

5% Max.

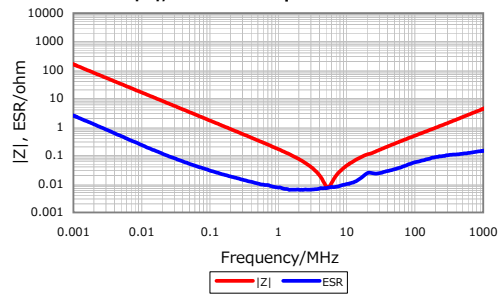
## Insulation Resistance

500Mohm Min.

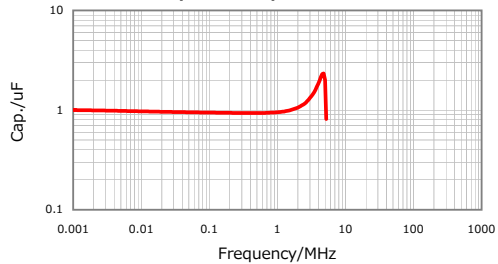
## AEC-Q200

Not Applicable

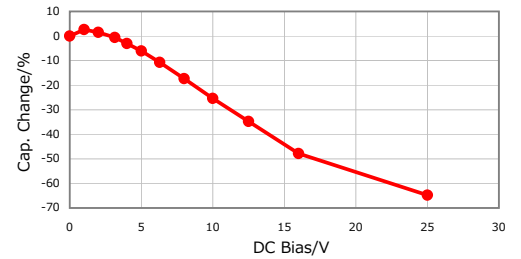
**|Z|, ESR vs. Freq. Characteristics**



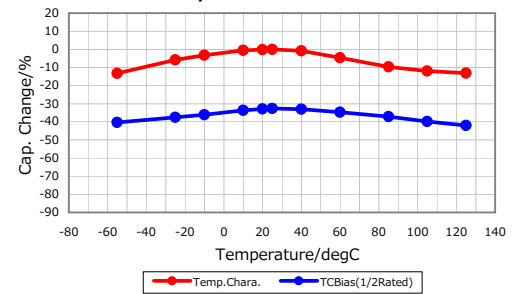
**Cap. vs. Freq. Characteristic**



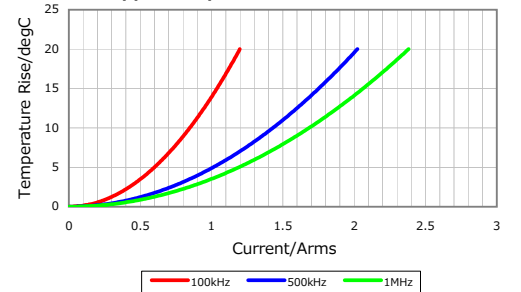
**DC Bias Characteristic**



**Temperature Characteristic**



**Ripple Temperature Rise Characteristic**



Characterization Sheet



⚠ All specifications are subject to change without notice.

December 22, 2012