

C1005X5R1H683K050BB

--- Multilayer Ceramic Chip Capacitor (MLCC) ---



Application & Main Feature

Commercial Grade (General (Up to 50V))

Series

C1005 [EIA CC0402]



Dimensions

L 1.00mm +/-0.05mm
 W 0.50mm +/-0.05mm
 T 0.50mm +/-0.05mm
 B 0.10mm Min.
 G 0.30mm Min.

Temperature Characteristic

X5R (-55 to 85 degC +/-15%)

Rated Voltage

1H (50Vdc)

Capacitance

68nF

Capacitance Tolerance

K (+/-10%)

Dissipation Factor

5% Max.

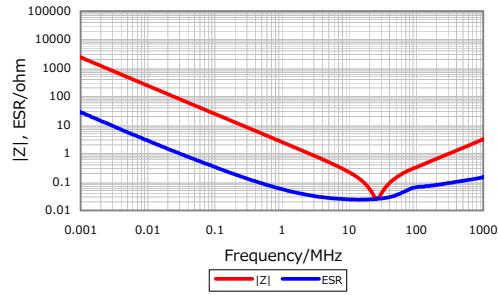
Insulation Resistance

7.35Gohm Min.

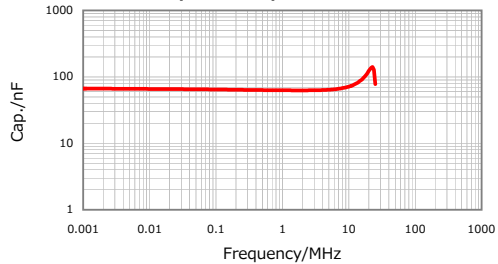
AEC-Q200

Not Applicable

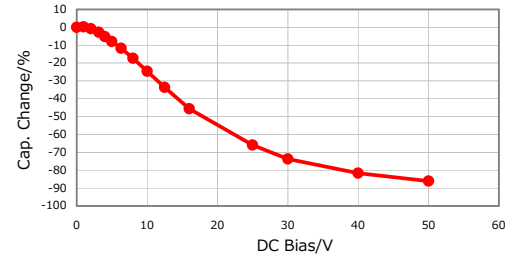
|Z|, ESR vs. Freq. Characteristics



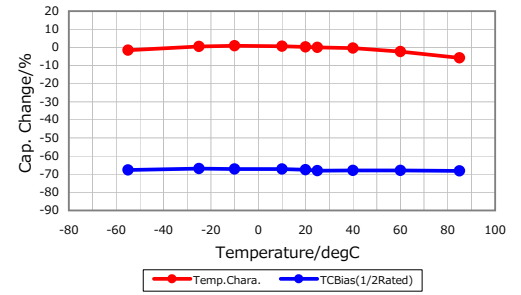
Cap. vs. Freq. Characteristic



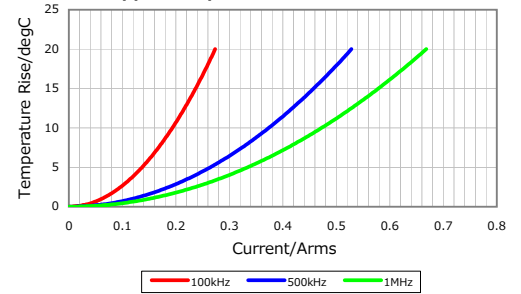
DC Bias Characteristic



Temperature Characteristic



Ripple Temperature Rise Characteristic



Characterization Sheet



All specifications are subject to change without notice.

December 22, 2012