

Surface Mount Chip Capacitors

Stacked Chip Capacitors

COG/X7R

Introduction

These ranges of both High Capacitance and High Voltage Multilayer ceramic capacitor assemblies are designed for use in high frequency switched mode power supplies, DC-DC converters and similar applications.

Low ESR and low ESL are inherent in the design giving the assemblies a high current capability up to 1MHz and offer far superior performance than either aluminium or tantalum electrolytic capacitors. Various lead options are available, making them suitable for mounting on ceramic substrate or epoxy printed circuit boards.

Summary of Standard Range

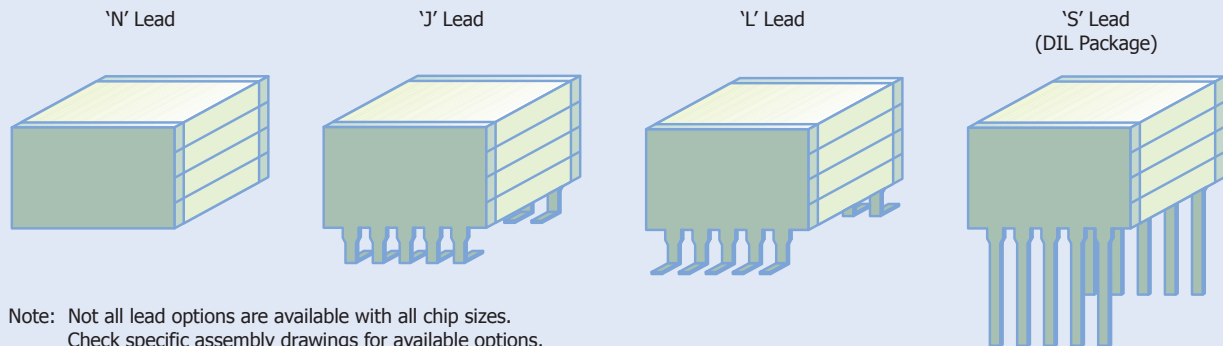
Chip sizes covered 1812; 2220; 2225; 3640; 5550; 8060.

Working Voltages 50V to 2kV as standard.

Capacitance Range 39pF to 1.8µF in Ultra Stable COG Dielectric
820pF to 68µF in Stable X7R Dielectric.

Special chip sizes, working voltages, capacitance values and specific custom requirements will be considered. Please refer all enquiries to the Sales Office.

Available Lead Options



Note: Not all lead options are available with all chip sizes. Check specific assembly drawings for available options.

Leaded assembly dimensions

	Dimensions (mm)		
	3640	5550	8060
W max	11.5	14.0	16.5
C nom	9.2	14.0	20.3
L max	11.7	16.5	22.8
No. of leads per side	4	5	6

Max Stack Height (H)

No. of Chips	Range (size)	'N' Unleaded Assemblies	'J' & 'L' leaded Assemblies	'S' leaded Assemblies
1	≤ 2225	N/A	4.5	N/A
	≥ 3640	N/A	5.5	3.25
2	≤ 2225	5.25	7.0	N/A
	≥ 3640	N/A	8.75	6.75
3	≤ 2225	7.75	9.5	N/A
	≥ 3640	N/A	12.0	10.0
4	≤ 2225	10.25	12.0	N/A
	≥ 3640	N/A	15.25	13.25
5	≤ 2225	12.75	14.5	N/A
	≥ 3640	N/A	18.5	16.5

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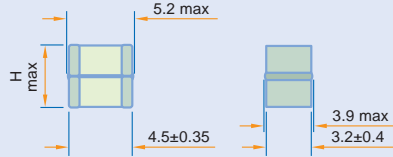
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Available Options and Dimensions (mm)

Chip Size 1812

Unleaded assembly only ('N' lead option)

■ Denotes Metallised Solderable Area

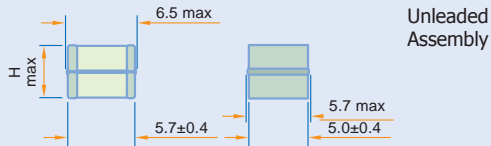


See above for dimension H

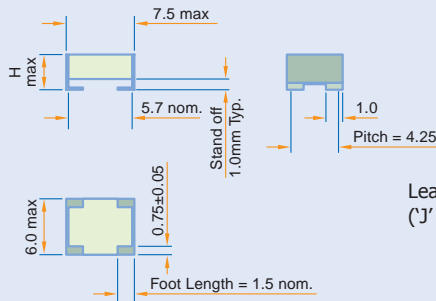
Chip Size 2220

'J' & 'L' Leaded or Unleaded ('N') assemblies

■ Denotes Metallised Solderable Area



Unleaded Assembly



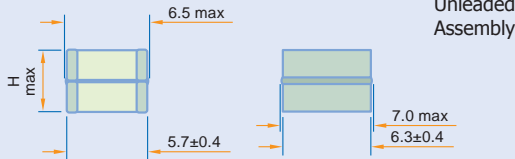
Leaded Assembly ('J' lead shown.)

See above for dimension H

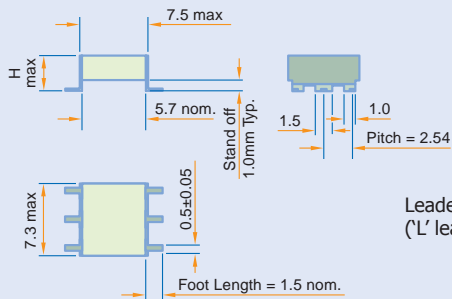
Chip Size 2225

'J' & 'L' Leaded or Unleaded ('N') assemblies

■ Denotes Metallised Solderable Area



Unleaded Assembly

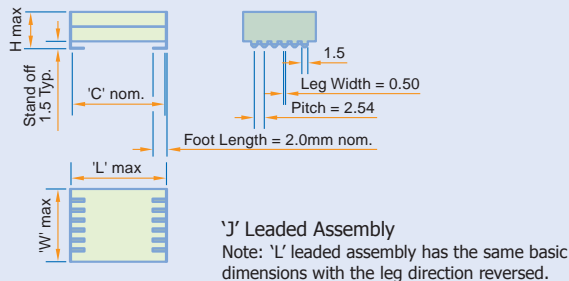


Leaded Assembly ('L' lead shown.)

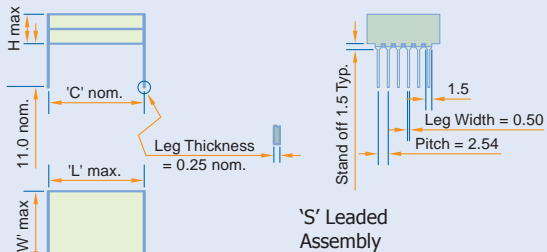
See above for dimension H

Chip Size 3640, 5550, 8060

'J', 'L' & 'S' Leaded assemblies



'J' Leaded Assembly
Note: 'L' leaded assembly has the same basic dimensions with the leg direction reversed.



'S' Leaded Assembly

See above for dimension H



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Capacitance	Code	1812 ('N' Lead only)						2220 ('N', 'J' & 'L' Lead)						2225 ('N', 'J' & 'L' Lead)					
		50	100	200	500	1K	2K	50	100	200	500	1K	2K	50	100	200	500	1K	2K
39pF	390						1												1
47	470						1												1
56	560						1												1
68	680						1												1
82	820						1												1
100	101						1												1
120	121						1												1
150	151						1												1
180	181						1												1
220	221						1												1
270	271						1												1
330	331						1												1
390	391						1												1
470	471						1												1
560	561						1												1
680	681						1												1
820	821						1												1
1.0nF	102						1												1
1.2	122						1												1
1.5	152						1												1
1.8	182						1												1
2.2	222						1												1
2.7	272						1												1
3.3	332						1												1
3.9	392						1												1
4.7	472						1												1
5.6	562						1												1
6.8	682						1												1
8.2	822						1												1
10	103						1												1
12	123						1												1
15	153						1												1
18	183						1												1
22	223						1												1
27	273						1												1
33	333						1												1
39	393						1												1
47	473						1												1
56	563						1												1
68	683						1												1
82	823						1												1
100	104						1												1
120	124						1												1
150	154						1												1
180	184						1												1
220	224						1												1
270	274						1												1
330	334						1												1
390	394						1												1
470	474						1												1
560	564						1												1
680	684						1												1
820	824						1												1
1.0µF	105						1												1
1.2	125						1												1
1.5	155						1												1
1.8	185						1												1

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notes

1. The table above indicates the number of chips required to achieve the capacitance value.
2. Higher voltages (To 5kV max) may be available on request

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Capacitance	Code	3640 (*J, *L & *S' Lead)						5550 (*J, *L & *S' Lead)						8060 (*J, *L & *S' Lead)						
		50	100	200	500	1K	2K	50	100	200	500	1K	2K	50	100	200	500	1K	2K	
39pF	390																			
47	470																			
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X7R

Capacitance	Code	1812 ('N' Lead only)						2220 ('N', 'J' & 'L' Lead)						2225 ('N', 'J' & 'L' Lead)					
		50	100	200	500	1K	2K	50	100	200	500	1K	2K	50	100	200	500	1K	2K
820pF	821						1												
1.0nF	102						1												
1.2	122						1												
1.5	152						1												
1.8	182						1												
2.2	222						1					1							
2.7	272						1					1							
3.3	332						1					1							
3.9	392						1					1							
4.7	472						1					1							
5.6	562						1					1							
6.8	682						1					1							
8.2	822						1					1							
10	103						1					1							
12	123						1					1							
15	153						1					1							
18	183						1					1							
22	223						1					1							
27	273						1					1							
33	333						1					1							
39	393						1					1							
47	473						1					1							
56	563						1					1							
68	683						1					1							
82	823						1					1							
100	104						1					1							
120	124						1					1							
150	154						1					1							
180	184						1					1							
220	224						1					1							
270	274						1					1							
330	334						1					1							
390	394						1					1							
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680	684						1					1							
820	824						1					1							
1.0µF	105	1					1					1							
1.2	125	1					1					1							
1.5	155	1					1					1							
1.8	185	1					1					1							
2.2	225	1					1					1							
2.7	275	1					1					1							
3.3	335	1					1					1							
3.9	395	1					1					1							
4.7	475	1					1					1							
5.6	565	1					1					1							
6.8	685	1					1					1							
8.2	825	1					1					1							
10	106	1					1					1							
12	126	1					1					1							
15	156	1					1					1							
18	186	1					1					1							
22	226	1					1					1							
27	276	1					1					1							
33	336	1					1					1							
39	396	1					1					1							
47	476	1					1					1							
56	566	1					1					1							
68	686	1					1					1							

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notes

1. The table above indicates the number of chips required to achieve the capacitance value.
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Capacitance	Code	3640 (*J, *L & *S Lead)						5550 (*J, *L & *S Lead)						8060 (*J, *L & *S Lead)					
		50	100	200	500	1K	2K	50	100	200	500	1K	2K	50	100	200	500	1K	2K
820pF	821																		
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Ordering Information

8060	B	500	0126	K	X	B	J	W00	5
Chip Size	Finish	Voltage	Capacitance	Tolerance	Dielectric	Packaging	Mounting Style	Customer Special Requirements	No. of Chips
	B = Bare Chip Assembly	050 = 50V 100 = 100V 200 = 200V 500 = 500V 1K0 = 1000V 2K0 = 2000V	Expressed in picofarads (pF). First digit is 0. Second and third digits are significant figures of capacitance code. The fourth digit is number of zeros following. eg: 0126=12µF.	M = ±20% Standard K = ±10% Optional	C = COG X = X7R	R = 330mm (13") reel B = Bulk	N = Bare Chip SM Assembly J = J leaded SM Assembly L = L leaded SM Assembly S = Straight leaded DIL Assembly		

Notes

- Other capacitance tolerances may also be available.
- Tape and reel packing is available on 2220 & 2225 single chip 'J' and 'L' leaded products and 1812, 2220, 2225 2-stack unleaded ('N' leaded) products. All other products will be supplied bulk packed in protective foam. Special waffle packing requirements can be considered.
- Higher working voltages and alternative chip sizes are also available by special request.

Materials

In all cases, leadframes, where fitted, will be silver plated copper alloy.

Chip to chip attachment, and chip to leadframe attachment will be by either high melting point solder (M.Pt. 300°C typ.) or high conductivity silver loaded epoxy adhesive depending on product.

Design Notes

When specifying these components, consideration must be given to their physical size, aspect ratio and mass with particular reference to thermal mismatch, mechanical shock and vibration characteristics.

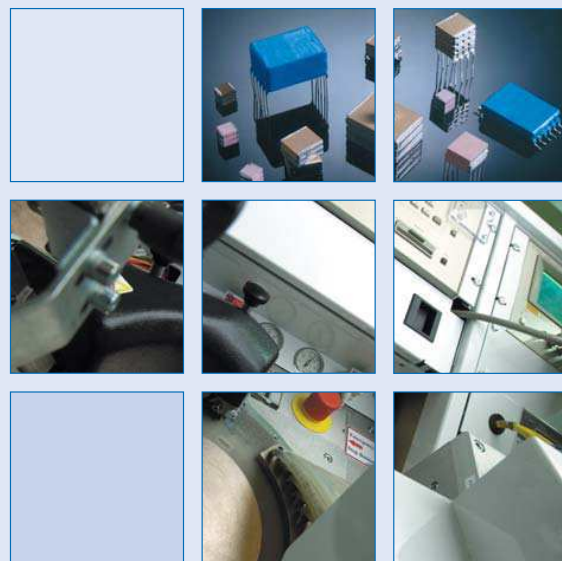
Stack sizes greater than or equal to 3640 should not be mounted directly to the board without the use of a stand off. For SM products, specify 'J' or 'L' leads. For through hole mounting specify 'S' leads.

Where possible, using a larger size chip with less chips in the stack will result in a more stable product when placed on the board, as the result of an improved aspect ratio.

A general handling recommendations sheet covering these, and other points, is available upon request from our Sales Office.

A data sheet covering recommended pad designs is available on request from our Sales Office.

Please refer all specific enquiries to the Sales Office.



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