

## 5V/3.3V DIFFERENTIAL RECEIVER

### **FEATURES**

- 3.3V and 5V power supply options
- 250ps propagation delay
- High bandwidth output transitions
- Internal 75KΩ input pull-down resistors
- Replaces SY10/100EL16
- Improved output waveform characteristics
- Available in 8-pin (3×3mm) MSOP and SOIC package

## DESCRIPTION

The SY10/100EL16V are differential receivers. The devices are functionally equivalent to the E116 devices, with higher performance capabilities. With output transition times significantly faster than the E116, the EL16V is ideally suited for interfacing with high-frequency sources.

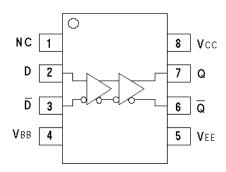
The EL16V provides a VBB output for either singleended use or as a DC bias for AC coupling to the device. The VBB pin should be used only as a bias for the EL16V as its current sink/source capability is limited. Whenever used, the VBB pin should be bypassed to ground via a  $0.01\mu$ F capacitor.

Under open input conditions (pulled to VEE), internal input clamps will force the Q output LOW.

### **PIN NAMES**

Pin	Function
D	Data Inputs
Q	Data Outputs
Vвв	Reference Voltage Output

## **PACKAGE/ORDERING INFORMATION**



8-Pin MSOP (K8-1) 8-Pin SOIC (Z8-1)

Ordering	Inform	ation <sup>(1)</sup>
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	Package	Operating	Package	Lead
Part Number	Туре	Range	Marking	Finish
SY10EL16VKC	K8-1	Commercial	HEL16V	Sn-Pb
SY10EL16VKCTR <sup>(2)</sup>	K8-1	Commercial	HEL16V	Sn-Pb
SY100EL16VKC	K8-1	Commercial	XEL16V	Sn-Pb
SY100EL16VKCTR <sup>(2)</sup>	K8-1	Commercial	XEL16V	Sn-Pb
SY10EL16VZC	Z8-1	Commercial	HEL16V	Sn-Pb
SY10EL16VZCTR <sup>(2)</sup>	Z8-1	Commercial	XEL16V	Sn-Pb
SY100EL16VZC	Z8-1	Commercial	HEL16V	Sn-Pb
SY100EL16VZCTR <sup>(2)</sup>	Z8-1	Commercial	XEL16V	Sn-Pb
SY10EL16VKI	K8-1	Industrial	HEL16V	Sn-Pb
SY10EL16VKITR <sup>(2)</sup>	K8-1	Industrial	HEL16V	Sn-Pb
SY100EL16VKI	K8-1	Industrial	XEL16V	Sn-Pb
SY100EL16VKITR <sup>(2)</sup>	K8-1	Industrial	XEL16V	Sn-Pb
SY10EL16VZI	Z8-1	Industrial	HEL16V	Sn-Pb
SY10EL16VZITR <sup>(2)</sup>	Z8-1	Industrial	XEL16V	Sn-Pb
SY100EL16VZI	Z8-1	Industrial	HEL16V	Sn-Pb
SY100EL16VZITR <sup>(2)</sup>	Z8-1	Industrial	XEL16V	Sn-Pb
SY10EL16VKG <sup>(3)</sup>	K8-1	Industrial	H16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY10EL16VKGTR <sup>(2, 3)</sup>	K8-1	Industrial	H16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY100EL16VKG <sup>(3)</sup>	K8-1	Industrial	X16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY100EL16VKGTR <sup>(2, 3)</sup>	K8-1	Industrial	X16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY10EL16VZG <sup>(3)</sup>	Z8-1	Industrial	HEL16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY10EL16VZGTR <sup>(2, 3)</sup>	Z8-1	Industrial	HEL16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY100EL16VZG <sup>(2)</sup>	Z8-1	Industrial	XEL16V with Pb-Free bar-line indicator	Pb-Free NiPdAu
SY100EL16VZGTR <sup>(2, 3)</sup>	Z8-1	Industrial	XEL16V with Pb-Free bar-line indicator	Pb-Free NiPdAu

Notes:

1. Contact factory for die availability. Dice are guaranteed at  $T_A = 25^{\circ}C$ , DC Electricals only.

2. Tape and Reel.

3. Pb-Free package is recommended for new designs.

# **PRODUCT/PROCESS INFORMATION**

Process:	Bipolar
ESD Rating:	Per Mil Std. 883 Human Body Model, >2.0kV (all pins).

# ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

Symbol	Rating		Value	Unit
V <sub>CC</sub>	Power Supply Voltage (V <sub>EE</sub> = 0)	+6.0 to 0	V	
V <sub>EE</sub>	Power Supply Voltage ( $V_{CC} = 0$ )	-6.0 to 0	V	
V <sub>IN</sub>	Input Voltage ( $V_{CC} = 0V$ , $V_{IN}$ not more negative Input Voltage ( $V_{EE} = 0V$ , $V_{IN}$ not more positive t	-6.0 to 0 +6.0 to 0	V V	
I <sub>OUT</sub>	Output Current	50 100	mA	
T <sub>LEAD</sub>	Lead Temperature Range (soldering, 20sec.)		+260	°C
T <sub>A</sub>	Operating Temperature Range		-40 to +85	°C
T <sub>store</sub>	Storage Temperature Range		-65 to +150	°C
$\theta_{JA}$	Thermal Resistance (Junction-to-Ambient)	–Still Air –500lfpm	160 109	°C/W °C/W
$\theta_{\text{JC}}$	Thermal Resistance (Junction-to-Case)		39	°C/W
ESD	Mil Std. 883 Human Body Model, All Pins		>2.0k	V

Note 1. Permanent device damage may occur if absolute maximum ratings are exceeded. This is a stress rating only and functional operation is not implied at conditions other than those detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

# DC ELECTRICAL CHARACTERISTICS<sup>(1, 2)</sup>

		TA = -40°C		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
IEE	Power Supply Current													mA
	10EL	_	18	22	9	18	22	9	18	22	9	18	22	
	100EL	-	18	22	9	18	22	9	18	22	9	21	26	
VBB	Output Reference													V
	Voltage 10EL	-1.43	_	-1.30	-1.38	—	-1.27	-1.35	_	-1.25	-1.31	_	-1.19	
	100EL	-1.38	—	-1.26	-1.38	—	-1.26	-1.38	—	-1.26	-1.38	—	-1.26	
Ін	Input HIGH Current	-	_	150	—	_	150	_	_	150	_	_	150	μA

VEE = VEE (Min.) to VEE (Max.); VCC = GND

**Note 1.** Parametric values specified at: 10/100EL16V Series:

-3.0V to -5.5V.

Note 2. specification for packaged product only.

# AC ELECTRICAL CHARACTERISTICS<sup>(1, 5)</sup>

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Т	$T_A = -40^{\circ}C$			TA = 0°C			TA = +25°C			TA = +85°C		
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
tpd	Propagation Delay to Output D (Diff D (SE		250 250	375 425	175 125	250 250	325 375	175 125	250 250	325 375	205 155	280 280	355 405	ps
tskew	Duty Cycle Skew <sup>(2)</sup> (Diff)	-	5	-	_	5	20	-	5	20	_	5	20	ps
Vpp	Minimum Input Swing <sup>(3)</sup>	150	-	-	150	_	_	150	_	_	150	_	_	mV
VCMR	Common Mode Range <sup>(4)</sup>	-1.3	-	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	V
tr tf	Output Rise/Fall Times C (20% to 80%)	100	225	350	100	225	350	100	225	350	100	225	350	ps

Note 1. Parametric values specified at: 10/100EL16V Series: -3.0V to -5.5V.

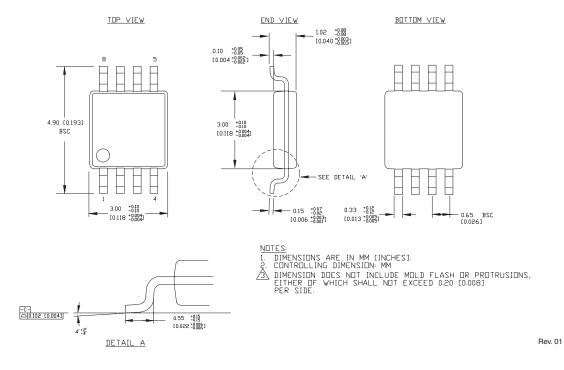
**Note 2.** Duty cycle skew is the difference between a t<sub>PLH</sub> and t<sub>PHL</sub> propagation delay through a device.

Note 3. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ~40.

**Note 4.** The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between  $V_{PP}$  min. and 1V. The lower end of the CMR range varies 1:1 with  $V_{EE}$ . The numbers in the spec table assume a nominal  $V_{EE} = -3.3V$ . Note for PECL operation, the  $V_{CMR}$  (min) will be fixed at  $3.3V - IV_{CMR}$  (min).

Note 5. Specification for packaged product only.

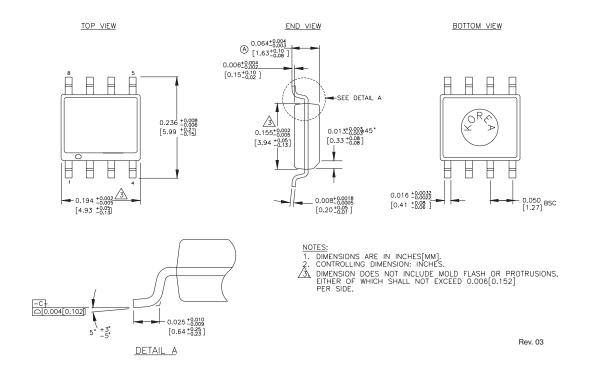
# 8-PIN MSOP (K8-1)



#### Package Notes:

Note 1. Package meets Level 1 moisture sensitivity.

### 8-PIN SOIC(Z8-1)



#### Package Notes:

Note 1. Package meets Level 1 moisture sensitivity.

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