

High voltage fast-switching NPN power transistor

Features

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Large RBSOA
- Integrated antiparallel collector-emitter diode

Applications

- Electronic ballast for fluorescent lighting
- Flyback and forward single transistor low power converters

Description

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and medium voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA. The device is designed for use in lighting applications and low cost switch-mode power supplies.

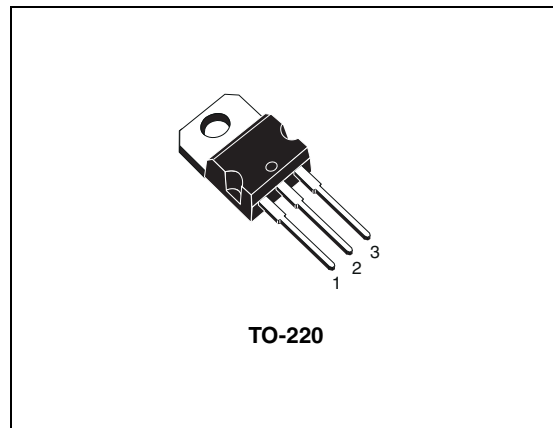


Figure 1. Internal schematic diagram

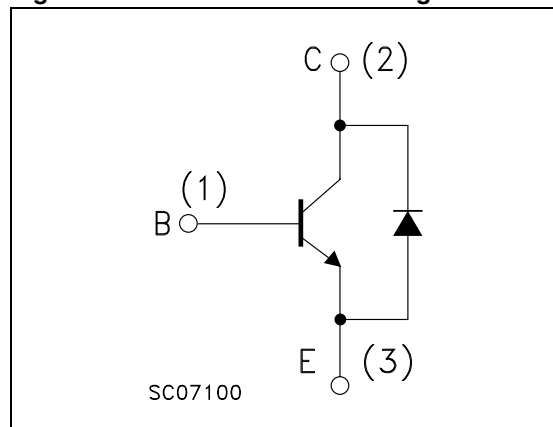


Table 1. Device summary

Order code	Marking	Package	Packaging
STL128DN	L128DN	TO-220	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage ($V_{BE} = 0$)	700	V
V_{CEO}	Collector-emitter voltage ($I_B = 0$)	400	V
V_{EBO}	Base-emitter voltage ($I_C = 0, I_B = 2 \text{ A}, t_P < 10 \mu\text{s}$)	$V_{(BR)EBO}$	V
I_C	Collector current	3	A
I_{CM}	Collector peak current ($t_P < 5 \text{ ms}$)	6	A
I_B	Base current	1.5	A
I_{BM}	Base peak current ($t_P < 5 \text{ ms}$)	3	A
P_{TOT}	Total dissipation at $T_C = 25 \text{ }^\circ\text{C}$	40	W
T_{stg}	Storage temperature	-65 to 150	$^\circ\text{C}$
T_J	Max. operating junction temperature	150	$^\circ\text{C}$

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case max	3.125	$^\circ\text{C/W}$
$R_{thj-amb}$	Thermal resistance junction-ambient max	62.5	$^\circ\text{C/W}$

2 Electrical characteristics

($T_{\text{case}} = 25\text{ °C}$ unless otherwise specified)

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{CES}	Collector cut-off current ($V_{\text{BE}} = 0$)	$V_{\text{CE}} = 700\text{ V}$			100	μA
		$V_{\text{CE}} = 700\text{ V}$ $T_{\text{c}} = 125\text{ °C}$			500	μA
I_{CEO}	Collector cut-off current ($I_{\text{B}} = 0$)	$V_{\text{CE}} = 400\text{ V}$			250	μA
$V_{(\text{BR})\text{EBO}}$	Emitter-base breakdown voltage ($I_{\text{C}} = 0$)	$I_{\text{E}} = 10\text{ mA}$	9		18	V
$V_{\text{CEO(sus)}}^{(1)}$	Collector-emitter sustaining voltage ($I_{\text{C}} = 0$)	$I_{\text{C}} = 10\text{ mA}$	400			V
$V_{\text{CE(sat)}}^{(1)}$	Collector-emitter saturation voltage	$I_{\text{C}} = 1\text{ A}$ $I_{\text{B}} = 0.2\text{ A}$			1	V
		$I_{\text{C}} = 2\text{ A}$ $I_{\text{B}} = 0.4\text{ A}$			1.5	V
$V_{\text{BE(sat)}}^{(1)}$	Base-emitter saturation voltage	$I_{\text{C}} = 1\text{ A}$ $I_{\text{B}} = 0.2\text{ A}$			1.2	V
		$I_{\text{C}} = 2\text{ A}$ $I_{\text{B}} = 0.4\text{ A}$			1.3	V
$h_{\text{FE}}^{(1)}$	DC current gain	$I_{\text{C}} = 10\text{ mA}$ $V_{\text{CE}} = 5\text{ V}$	10			
		$I_{\text{C}} = 2\text{ A}$ $V_{\text{CE}} = 5\text{ V}$	8			
V_{f}	Diode forward voltage	$I_{\text{C}} = 1\text{ A}$			2.5	V
t_{s} t_{f}	Resistive load					
	Storage time	$I_{\text{C}} = 1\text{ A}$ $I_{\text{B1}} = -I_{\text{B2}} = 0.2\text{ A}$			4.5	μs
	Fall time	$V_{\text{CC}} = 125\text{ V}$ $t_{\text{p}} = 20\text{ }\mu\text{s}$			0.4	μs

1. Pulsed duration = 300 ms, duty cycle $\leq 1.5\%$.

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Derating curve

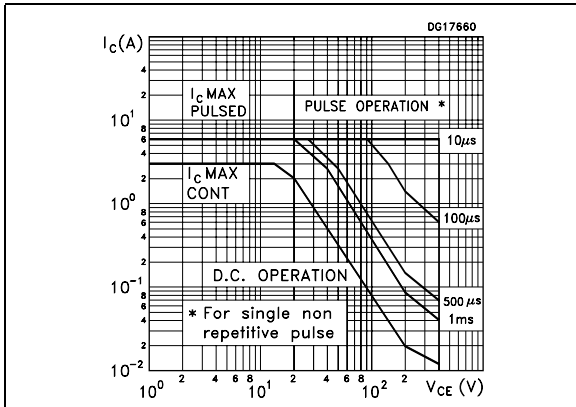


Figure 4. DC current gain

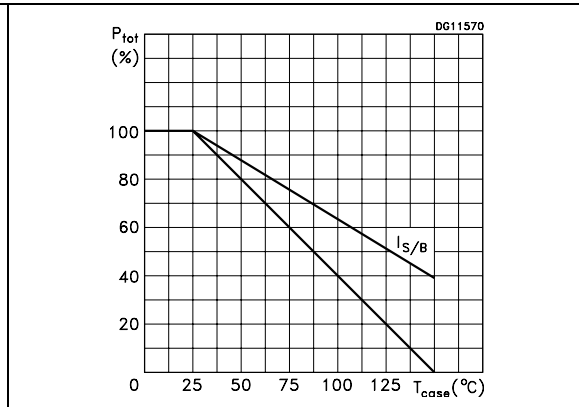


Figure 5. DC current gain

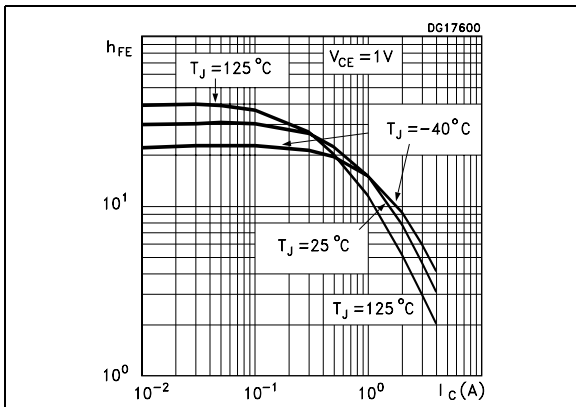


Figure 6. Collector-emitter saturation voltage

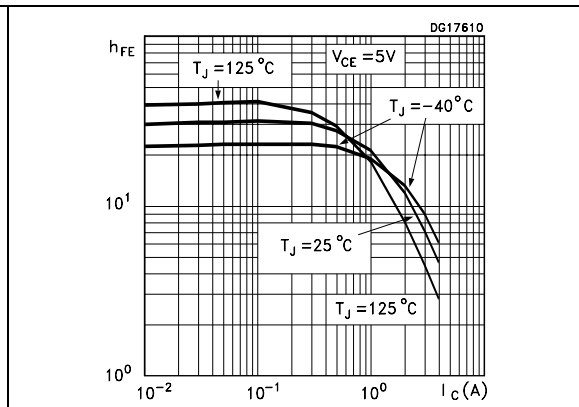


Figure 7. Base-emitter saturation voltage

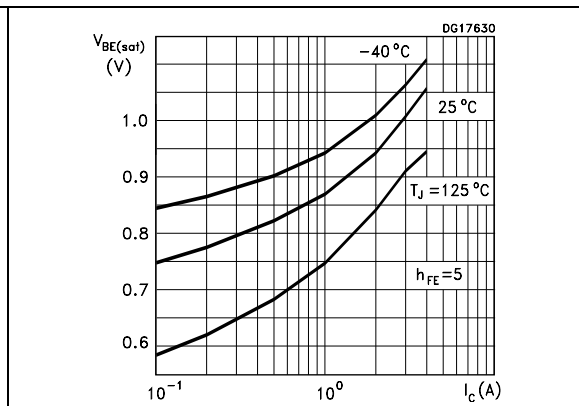
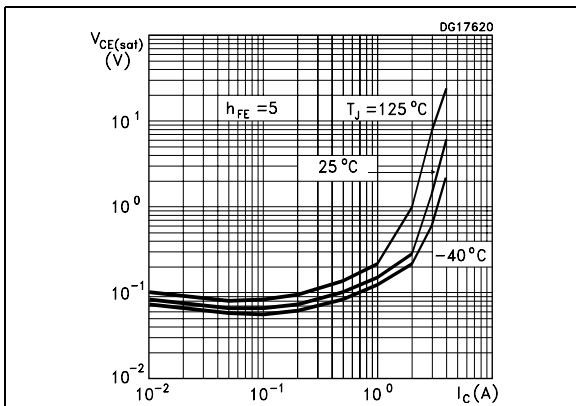


Figure 8. Freewheel diode forward voltage

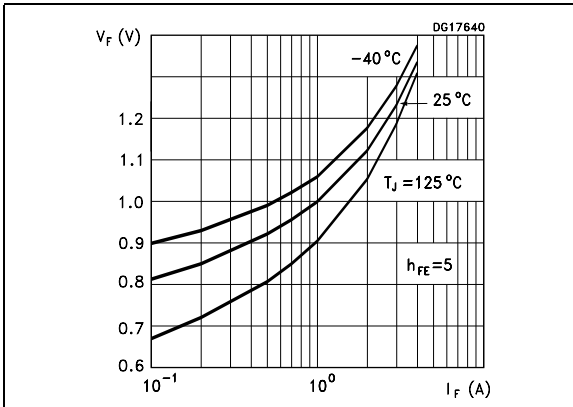


Figure 9. Resistive load switching time

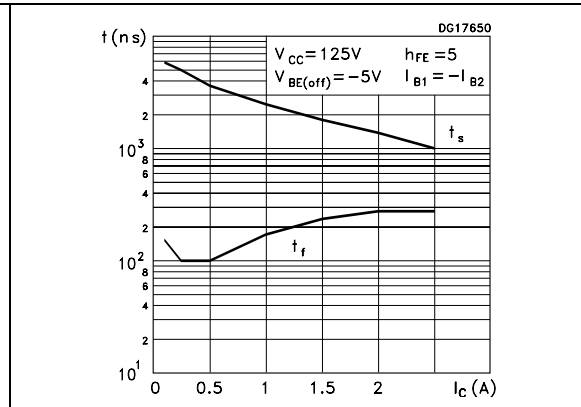
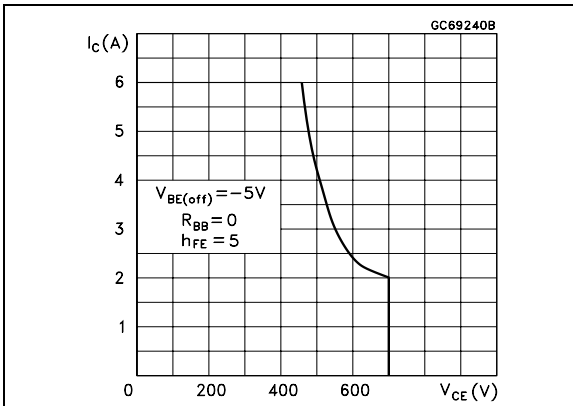
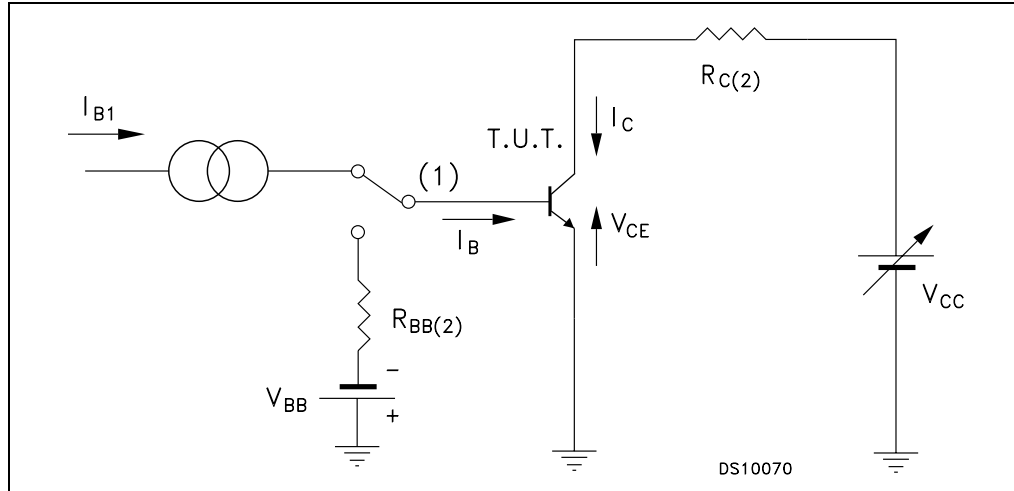


Figure 10. Reverse biased safe operating area



3 Test circuit

Figure 11. Resistive load switching test circuit



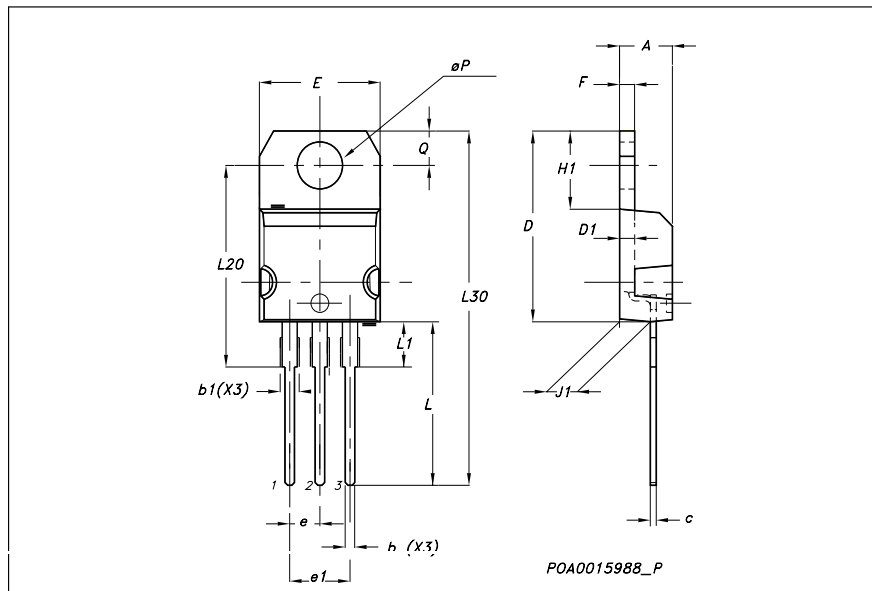
1. Fast electronic switch
2. Non-inductive resistor

4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

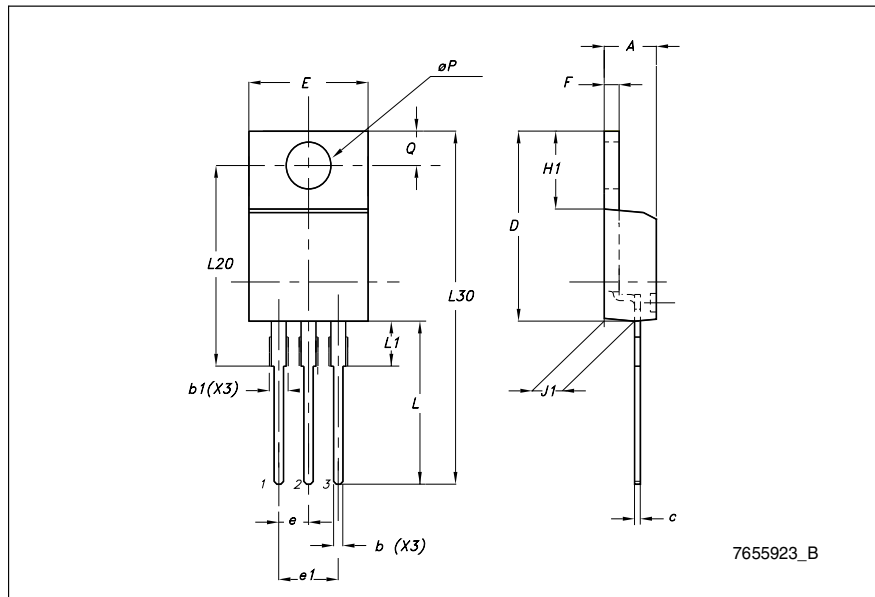
TO-220 Mechanical data

DIM.	mm.		
	MIN.	TYP	MAX.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.49		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
øP	3.75		3.85
Q	2.65		2.95



TO-220 type E mechanical data

DIM.	mm.		
	MIN.	TYP	MAX.
A	4.47		4.67
b	0.70		0.91
b1	1.17		1.37
c	0.31		0.53
D	14.60		15.70
E	9.96		10.36
e		2.54	
e1	4.98	5.08	5.18
F	1.17		1.37
H1	6.10		6.80
J1	2.52		2.82
L	12.70		13.80
L1	3.20		3.96
L20	15.21		16.77
øP	3.73		3.94
Q	2.59		2.89



5 Revision history

Table 5. Document revision history

Date	Revision	Changes
04-Oct-2007	1	First release
14-Feb-2008	2	Updated TO-220, type E, mechanical data

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