

# **4V Drive Pch MOSFET**

### RRH050P03

#### Structure

Silicon P-channel MOSFET

#### ● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).

#### Application

Switching

Packaging specifications

	Package	Taping	
Туре	Code	TB	
	Basic ordering unit (pieces)	2500	
RRH050P0	0		

#### ●Absolute maximum ratings (Ta = 25°C)

Parameter		Limits	Unit
Drain-source voltage		-30	V
Gate-source voltage		±20	V
Continuous	ΙD	±5	А
Pulsed	IDP*1	±20	А
Continuous	Is	-1.6	А
Pulsed	I <sub>sp</sub> *1	-20	А
Power dissipation		2.0	W
Channel temperature		150	°C
Range of storage temperature		-55 to +150	°C
	e Continuous Pulsed Continuous Pulsed	Pulsed   Isp*1   Pused   Isp*2   Tch   Imperature   Tstg   Isp   Tstg   Isp   Isp	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

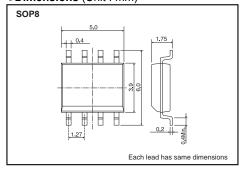
<sup>\*1</sup> Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board.

#### ●Thermal resistance

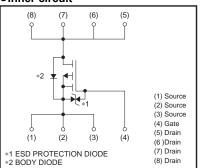
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	62.5	°C / W

<sup>\*</sup> Mounted on a ceramic board.

# ●Dimensions (Unit : mm)



#### •Inner circuit



# ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Gate-source leakage	Igss	-	-	±10	μΑ	Vgs=±20V, Vps=0V
Drain-source breakdown voltage	V(BR)DSS	-30	-	_	V	ID=-1mA, VGS=0V
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	VDS=-30V, VGS=0V
Gate threshold voltage	VGS (th)	-1.0	-	-2.5	V	VDS=-10V, ID=-1mA
Static drain-source on-state resistance		-	36	50	mΩ	ID=-5A, VGS=-10V
	RDS (on)*	-	52	72		ID=-2.5A, VGS=-4.5V
		-	58	80		In=-2.5A, Vgs=-4.0V
Forward transfer admittance	I Yfs I*	4	-	-	S	ID=-5A, VDS=-10V
Input capacitance	Ciss	_	850	_	pF	V <sub>DS</sub> =-10V
Output capacitance	Coss	-	120	-	pF	Vgs=0V
Reverse transfer capacitance	Crss	-	120	_	pF	f=1MHz
Turn-on delay time	td(on) *	-	9	-	ns	ID=-2.5A, VDD ≒ -15V
Rise time	tr *	-	25	-	ns	Vgs=-10V
Turn-off delay time	td(off) *	-	55	-	ns	RL=6.0Ω
Fall time	tr *	-	30	-	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	-	9.2	-	nC	ID=-5A, VDD≒-15V
Gate-source charge	Qgs *	-	2.4	-	nC	V <sub>GS</sub> =-5V R <sub>L</sub> =3.0Ω
Gate-drain charge	Q <sub>gd</sub> *	-	3.6	-	nC	R <sub>G</sub> =10Ω

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Forward Voltage	Vsp *	_	_	-1.2	V	Is=-5A, VGS=0V

<sup>\*</sup>Pulsed

#### •Electrical characteristic curves

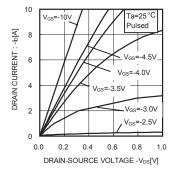


Fig.1 Typical Output Characteristics(I)

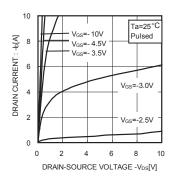


Fig.2 Typical Output Characteristics(II)

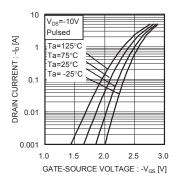
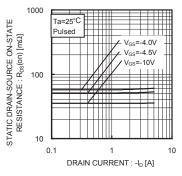


Fig.3 Typical Transfer Characteristics



 $\begin{tabular}{ll} Fig. 4 & Static Drain-Source On-State \\ & Resistance \ vs. \ Drain \ Current(I) \end{tabular}$ 

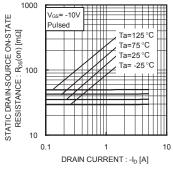


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

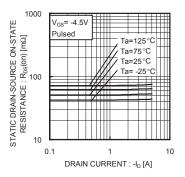
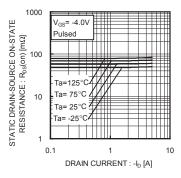


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(III)



 $\begin{tabular}{ll} Fig. 7 & Static Drain-Source On-State \\ & Resistance \ vs. \ Drain Current(IV) \end{tabular}$ 

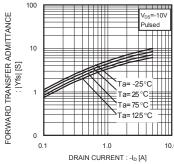


Fig.8 Forward Transfer Admittance vs. Drain Current

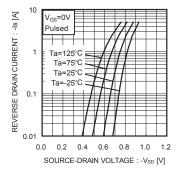


Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage

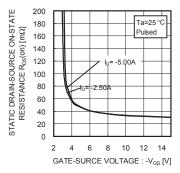


Fig.10 Static Drain-Source On-State Resistance vs. Gate Source Voltage

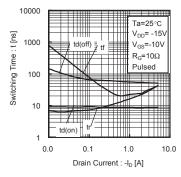


Fig.11 Switching Characteristics

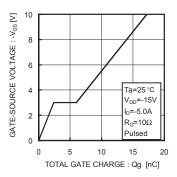


Fig.12 Dynamic Input Characteristics

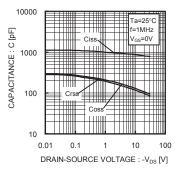


Fig.13 Typical Capacitance vs. Drain-Source Voltage

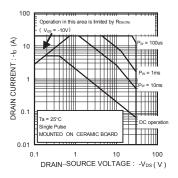
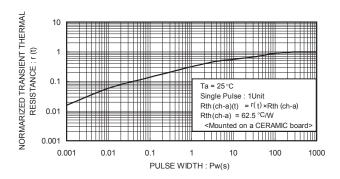


Fig.14 Maximum Safe Operating Aera



#### Measurement circuit

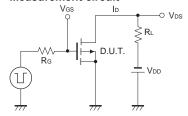


Fig.1-1 Switching Time Measurement Circuit

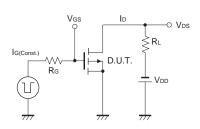


Fig.2-1 Gate Charge Measurement Circuit

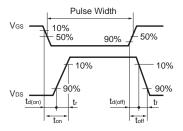


Fig.1-2 Switching Waveforms

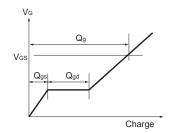


Fig.2-2 Gate Charge Waveform

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