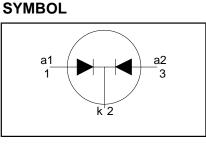
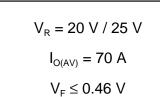
PBYR7025WT series

FEATURES

- Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- Low thermal resistance



QUICK REFERENCE DATA



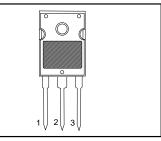
GENERAL DESCRIPTION

Dual, common cathode schottky rectifier diodes in a plastic envelope. Intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYR7025WT series is supplied in the conventional leaded SOT429 (TO247) package.

PINDESCRIPTION1anode 1 (a)2cathode (k)3anode 2 (a)tabcathode

SOT429 (TO247)



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

PINNING

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
V _{RRM} V _{RWM} V _R	Repetitive peak reverse voltage Crest working reverse voltage Continuous reverse voltage	T _{mb} ≤ 116 °C	- - -	-20 20 20 20	-25 25 25 25	V V V
I _{O(AV)}	Average output current (both diodes conducting)	square wave; δ = 0.5; T _{mb} ≤ 114 °C	-	7	0	A
I _{FRM}	Repetitive peak forward current per diode	t = 25 μs; δ = 0.5; T _{mb} ≤ 114 °C	-	7	0	A
I _{FSM}	Non-repetitive peak forward current, per diode	t = 10 ms t = 8.3 ms sinusoidal T_i = 125 °C prior to surge; with reapplied $V_{RRM(max)}$	-		00 50	AA
I _{RRM}	Repetitive peak reverse current per diode	$t_p = 2 \ \mu s; \ \delta = 0.001$	-		2	A
I _{RSM}	Non-repetitive peak reverse current per diode	t _p = 100 μs	-		2	A
T _{stg} T _j	Storage temperature Operating junction temperature		-65 -		50 50	°C C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb} R _{th j-a}	Thermal resistance junction to mounting base Thermal resistance junction to ambient	per diode both diodes in free air		- - 45	0.9 0.65 -	K/W K/W K/W

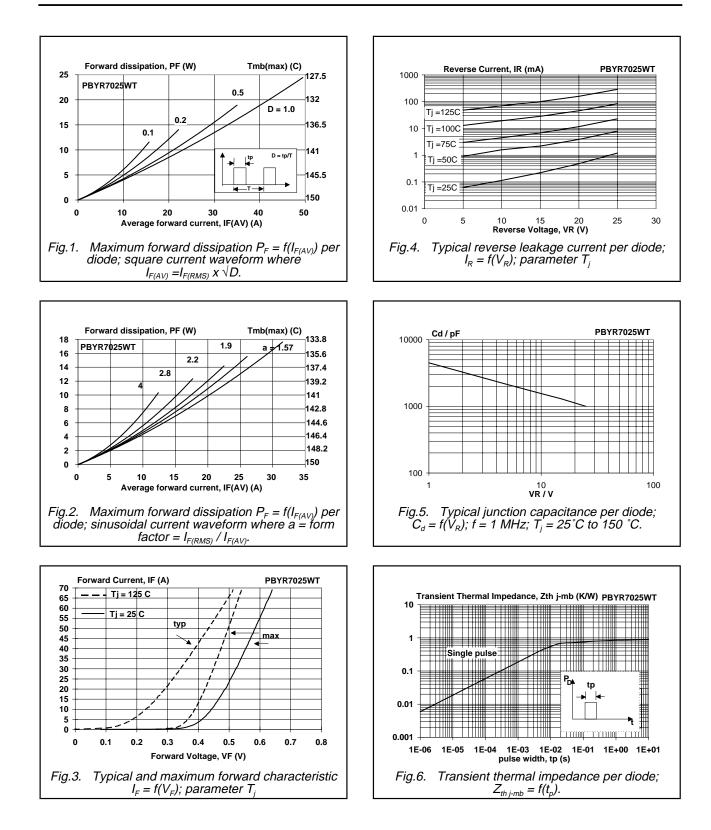
PBYR7025WT series

ELECTRICAL CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

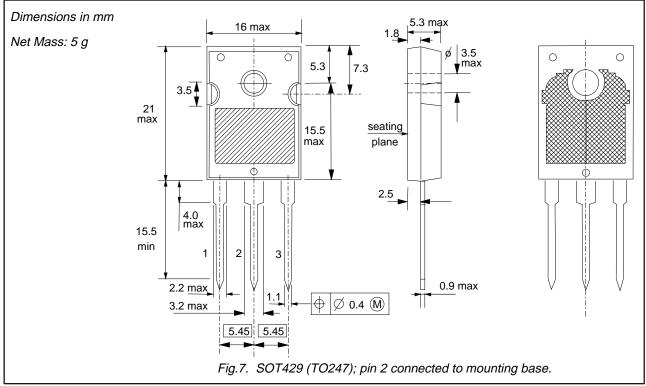
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	Forward voltage (per diode)	I _F = 35 A; T _j = 125°C I _F = 70 A; T _j = 125°C	-	0.40 0.52	0.46 0.54	V V
I _R	Reverse current (per diode)	$I_F = 70 \text{ A}$ $V_R = V_{RRM}$	-	0.58 0.8	0.64 15	V mA
C _d	Junction capacitance (per diode)	$V_{R}^{r} = V_{RRM}^{RRM}$; T _j = 100 °C f = 1MHZ; V _R = 5V; T _j = 25 °C to 125 °C	-	40 2100	120	mA pF

PBYR7025WT series



PBYR7025WT series

MECHANICAL DATA



Notes

Refer to mounting instructions for SOT429 envelope.
Epoxy meets UL94 V0 at 1/8".

PBYR7025WT series

DEFINITIONS

Data sheet status				
Objective specification	bjective specification This data sheet contains target or goal specifications for product development.			
Preliminary specification	reliminary specification This data sheet contains preliminary data; supplementary data may be published late			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.				
Application information				
Where application information is given, it is advisory and does not form part of the specification.				
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