

# **2PB709ARL**; **2PB709ASL**

45 V, 100 mA PNP general-purpose transistors
Rev. 01 — 12 November 2008

**Product data sheet** 

## **Product profile**

## 1.1 General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number[1]	Package	Package	
	NXP	JEDEC	
2PB709ARL	SOT23	TO-236AB	2PD601ARL
2PB709ASL			2PD601ASL
2PB709ARL/DG	SOT23	TO-236AB	2PD601ARL/DG
2PB709ASL/DG			2PD601ASL/DG

<sup>[1] /</sup>DG: halogen-free

## 1.2 Features

- General-purpose transistors
- Two current gain selections
- AEC-Q101 qualified
- Small SMD plastic package

## 1.3 Applications

■ General-purpose switching and amplification

### 1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-45	V
I <sub>C</sub>	collector current		-	-	-100	mΑ
h <sub>FE</sub>	DC current gain	$V_{CE} = -10 \text{ V};$ $I_C = -2 \text{ mA}$				
	h <sub>FE</sub> group R		210	-	340	
	h <sub>FE</sub> group S		290	-	460	



# 2. Pinning information

Table 3. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	base		_
2	emitter	3	3
3	collector	1 2	1 —
			sym013

# 3. Ordering information

Table 4. Ordering information

Type number[1]	Package	Package		
	Name	Description	Version	
2PB709ARL	·-	plastic surface-mounted package; 3 leads	SOT23	
2PB709ASL				
2PB709ARL/DG				
2PB709ASL/DG				

<sup>[1] /</sup>DG: halogen-free

## 4. Marking

Table 5. Marking codes

•	
Type number	Marking code <sup>[1]</sup>
2PB709ARL	SN*
2PB709ASL	SL*
2PB709ARL/DG	SS*
2PB709ASL/DG	SZ*

- [1] \* = -: made in Hong Kong
  - \* = p: made in Hong Kong
  - \* = t: made in Malaysia
  - \* = W: made in China

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# 5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	-45	V
$V_{CEO}$	collector-emitter voltage	open base	-	<b>-45</b>	V
$V_{EBO}$	emitter-base voltage	open collector	-	-6	V
I <sub>C</sub>	collector current		-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-200	mA
I <sub>BM</sub>	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	-100	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	[1] -	250	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u> _	-	500	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## 7. Characteristics

Table 8. Characteristics

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$I_{CBO}$	collector-base cut-off	$V_{CB} = -45 \text{ V}; I_E = 0 \text{ A}$	-	-	-10	nA
current	current	$V_{CB} = -45 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$	-	-	-5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	-	-	-10	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = -10 \text{ V}; I_{C} = -2 \text{ mA}$				
	h <sub>FE</sub> group R		210	-	340	
	h <sub>FE</sub> group S		290	-	460	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -100 \text{ mA};$ $I_B = -10 \text{ mA}$	[1] _	-	-500	mV

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Characteristics ...continued Table 8.  $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f <sub>T</sub>	transition frequency	$V_{CE} = -10 \text{ V}; I_{C} = -1 \text{ mA};$ f = 100 MHz				
	h <sub>FE</sub> group R		70	-	-	MHz
	h <sub>FE</sub> group S		80	-	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	-	-	5	pF

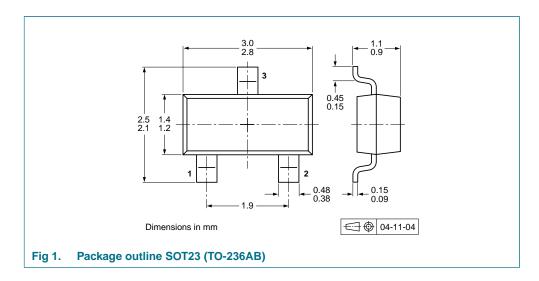
<sup>[1]</sup> Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

## **Test information**

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## **Package outline**



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# 10. Packing information

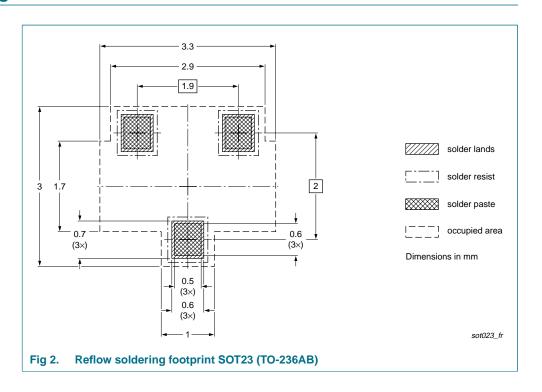
Table 9. **Packing methods** 

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number[2]	Package	ckage Description		Packing quantity	
			3000	10000	
2PB709ARL	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235	
2PB709ASL					
2PB709ARL/DG					
2PB709ASL/DG					

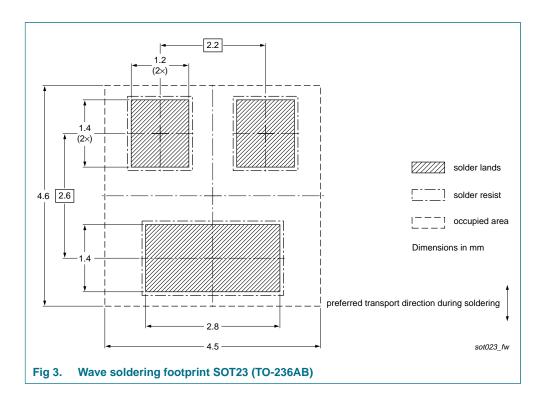
- [1] For further information and the availability of packing methods, see Section 14.
- [2] /DG: halogen-free

## 11. Soldering



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45 V, 100 mA PNP general-purpose transistors

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# 12. Revision history

## Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
2PB709AXL_1	20081112	Product data sheet	-	-

## 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 12 November 2008
Document identifier: 2PB709AXL\_1

