Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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SILICON POWER MOS FET NE5511279A

7.5 V OPERATION SILICON RF POWER LD-MOS FET FOR UHF-BAND 10 W TRANSMISSION AMPLIFIERS

DESCRIPTION

The NE5511279A is an N-channel silicon power laterally diffused MOS FET specially designed as the transmission power amplifier for 7.5 V Radio Systems. Dies are manufactured using our NEWMOS1 technology and housed in a surface mount package. This device can deliver 40.0 dBm output power with 48% power added efficiency at 900 MHz under the 7.5 V supply voltage.

FEATURES

High output power
 Pout = 40.0 dBm TYP. (f = 900 MHz, Vps = 7.5 V, Pin = 27 dBm, Ipset = 400 mA)

: Pout = 40.5 dBm TYP. (f = 460 MHz, VDS = 7.5 V, Pin = 25 dBm, IDset = 400 mA)

High power added efficiency: η_{add} = 48% TYP. (f = 900 MHz, V_{DS} = 7.5 V, P_{in} = 27 dBm, I_{Dset} = 400 mA)

: $\eta_{add} = 50\%$ TYP. (f = 460 MHz, VDs = 7.5 V, Pin = 25 dBm, IDset = 400 mA)

High linear gain
 : G_L = 15.0 dB TYP. (f = 900 MHz, V_{DS} = 7.5, P_{in} = 5 dBm V, I_{Dset} = 400 mA)

: $G_L = 18.5 \text{ dB TYP}$. (f = 460 MHz, $V_{DS} = 7.5 \text{ V}$, $P_{in} = 5 \text{ dBm}$, $I_{Dset} = 400 \text{ mA}$)

• Surface mount package : 5.7 × 5.7 × 1.1 mm MAX.

• Single supply : V_{DS} = 2.8 to 8.0 V

APPLICATIONS

• 460 MHz Radio Systems

· 900 MHz Radio Systems

ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form		
NE5511279A-T1	79A	 • 12 mm wide embossed taping • Gate pin face the perforation side of the tape • Qty 1 kpcs/reel 		W3	Gate pin face the perforation side of the tape
NE5511279A-T1A			12 mm wide embossed taping Gate pin face the perforation side of the tape Qty 5 kpcs/reel		

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: NE5511279A

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

Document No. PU10322EJ01V0DS (1st edition) Date Published June 2003 CP(K) Printed in Japan

ABSOLUTE MAXIMUM RATINGS (TA = +25°C)

Operation in excess of any one of these parameters may result in permanent damage.

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	V _{DS} Note	20	V
Gate to Source Voltage	Vgs	6.0	V
Drain Current	ΙD	3.0	Α
Total Power Dissipation	Ptot	20	W
Channel Temperature	Tch	125	°C
Storage Temperature	Tstg	−55 to +125	°C

Note Vps will be used under 12 V on RF operation.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	Vos		-	7.5	8.0	V
Gate to Source Voltage	Vgs		0	2.0	3.0	V
Drain Current	ΙD	Duty Cycle ≤ 50%, Ton ≤ 1 s	-	2.5	3.0	Α
Input Power	Pin	f = 900 MHz, Vps = 7.5 V	-	27	30	dBm



ELECTRICAL CHARACTERISTICS

(TA = +25°C, unless otherwise specified, using our standard test fixture)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Gate to Source Leak Current	Igss	Vgs = 6.0 V	-	-	100	nA
Drain to Source Leakage Current (Zero Gate Voltage Drain Current)	Ipss	V _{DS} = 8.5 V	_	-	100	nA
Gate Threshold Voltage	Vth	Vps = 4.8 V, lps = 1.5 mA	1.0	1.5	2.0	V
Thermal Resistance	Rth	Channel to Case	-	5	-	°C/W
Transconductance	g m	V _{DS} = 3.5 V, I _{DS} = 900 mA	-	2.3	-	S
Drain to Source Breakdown Voltage	BVDSS	loss = 15 μ A	20	24	-	V
Output Power	Pout	f = 900 MHz, Vps = 7.5 V,	38.5	40.0	-	dBm
Drain Current	ΙD	P _{in} = 27 dBm,	_	2.5	-	Α
Power Added Efficiency	η add	IDset = 400 mA (RF OFF)	42	48	-	%
Linear Gain	G _L ^{Note}		_	15.0	-	dB
Output Power	Pout	f = 460 MHz, V _{DS} = 7.5 V,	_	40.5	-	dBm
Drain Current	ΙD	Pin = 25 dBm,	-	2.75	-	Α
Power Added Efficiency	$\eta_{ ext{add}}$	I _{Dset} = 400 mA (RF OFF)	-	50	-	%
Linear Gain	G _L ^{Note}		-	18.5	-	dB

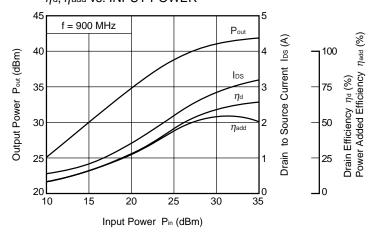
Note $P_{in} = 5 dBm$

DC performance is 100% testing. RF performance is testing several samples per wafer.

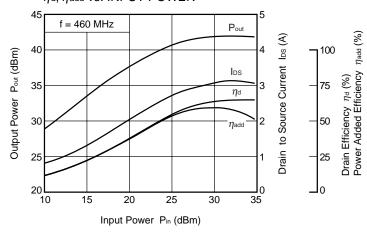
Wafer rejection criteria for standard devices is 1 reject for several samples.

TYPICAL CHARACTERISTICS (TA = +25°C, VDS = 7.5 V, IDset = 400 mA)

OUTPUT POWER, DRAIN CURRENT, η_d , η_{add} vs. INPUT POWER



OUTPUT POWER, DRAIN CURRENT, $\eta_{\rm d}$, $\eta_{\rm add}$ vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

S-PARAMETERS

S-parameters/Noise parameters are provided on the NEC Compound Semiconductor Devices Web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

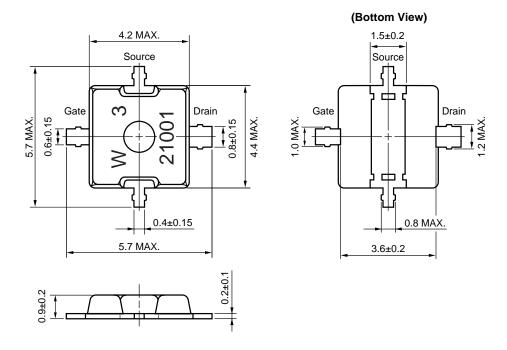
Click here to download S-parameters.

[RF and Microwave] → [Device Parameters]

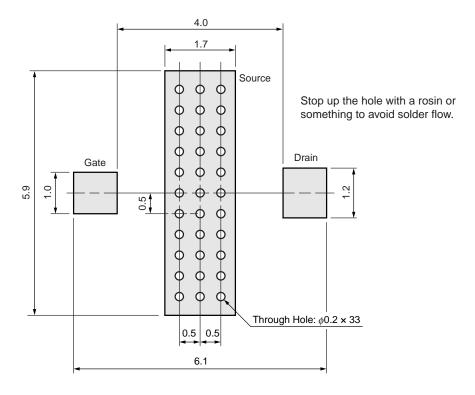
URL http://www.csd-nec.com/

PACKAGE DIMENSIONS

79A (UNIT: mm)



79A PACKAGE RECOMMENDED P.C.B. LAYOUT (UNIT: mm)



5

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) Soldering time (per pin of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350-P3

Caution Do not use different soldering methods together (except for partial heating).

NEC NE5511279A

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