

N-Channel RF Amplifier

This device is designed for HF/VHF mixer/amplifier and applications where Process 50 is not adequate. Sufficient gain and low noise for sensitive receivers. Sourced from Process 90.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	25	V
V _{GS}	Gate-Source Voltage	- 25	V
I_{GF}	Forward Gate Current	10	mA
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

Symbol	Characteristic		Мах	Units
		J210-212	*MMBFJ210-212	
PD	Total Device Dissipation	350	225	mW
	Derate above 25°C	2.8	1.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

TA = 25°C unless otherwise noted

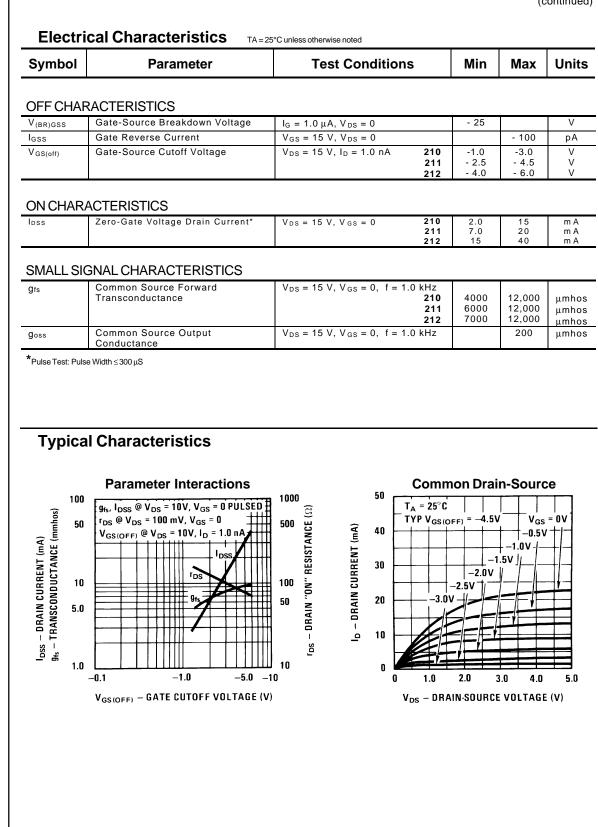
*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

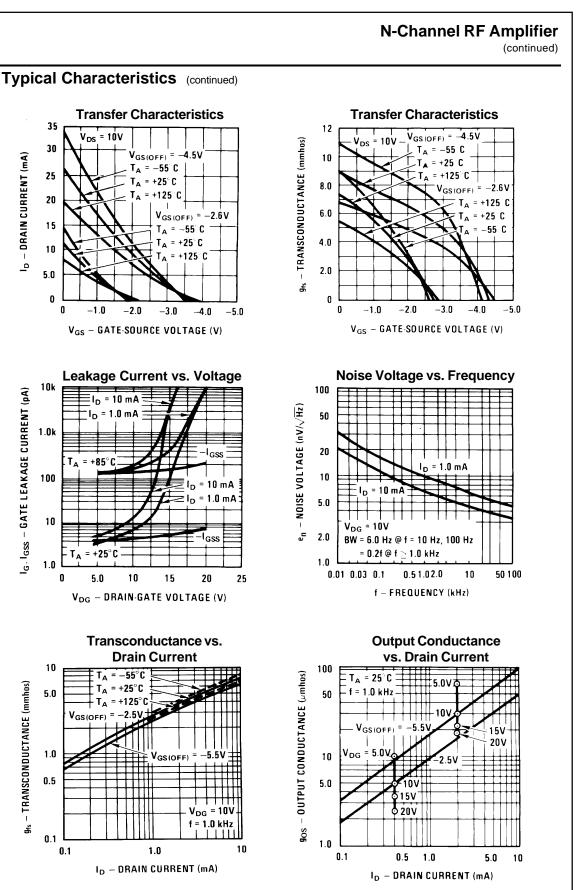
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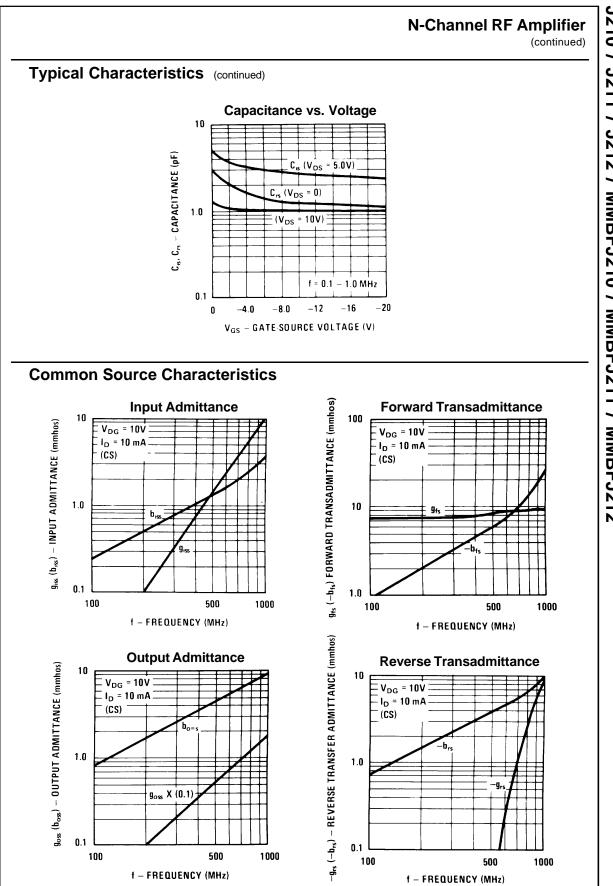
J210/J211/J212/MMBFJ210/J211/J212, Rev A

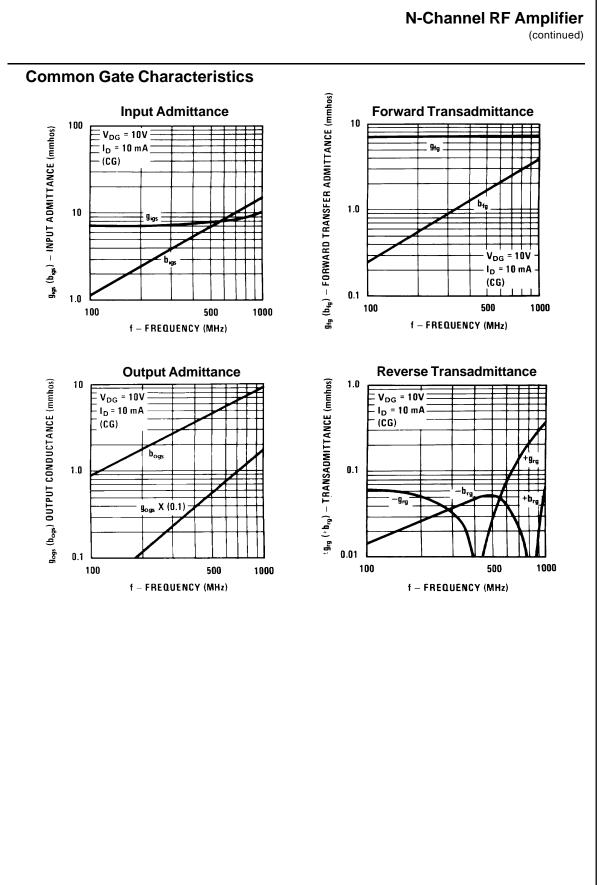
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(continued)









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PRODUCT STATUS DEFINITIONS

Definition of Terms

Product Status	Definition
Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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