

BC637; BCP55; BCX55

60 V, 1 A NPN medium power transistors

Rev. 07 — 25 June 2007

Product data sheet

1. Product profile

1.1 General description

NPN medium power transistor series.

Table 1. Product overview

Type number ^[1]	Package			PNP complement
	NXP	JEITA	JEDEC	
BC637 ^[2]	SOT54	SC-43A	TO-92	BC638
BCP55	SOT223	SC-73	-	BCP52
BCX55	SOT89	SC-62	TO-243	BCX52

[1] Valid for all available selection groups.

[2] Also available in SOT54A and SOT54 variant packages (see [Section 2](#)).

1.2 Features

- High current
- Two current gain selections
- High power dissipation capability

1.3 Applications

- Linear voltage regulators
- Low-side switches
- MOSFET drivers
- Amplifiers

1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CE0}	collector-emitter voltage	open base	-	-	60	V
I_C	collector current		-	-	1	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	1.5	A
h_{FE}	DC current gain	$V_{CE} = 2$ V; $I_C = 150$ mA	63	-	250	
	h_{FE} selection -10	$V_{CE} = 2$ V; $I_C = 150$ mA	63	-	160	
	h_{FE} selection -16	$V_{CE} = 2$ V; $I_C = 150$ mA	100	-	250	



2. Pinning information

Table 3. Pinning

Pin	Description	Simplified outline	Symbol
SOT54			
1	base	<p>001aab347</p>	<p>sym056</p>
2	collector		
3	emitter		
SOT54A			
1	base	<p>001aab348</p>	<p>sym056</p>
2	collector		
3	emitter		
SOT54 variant			
1	base	<p>001aab447</p>	<p>sym056</p>
2	collector		
3	emitter		
SOT223			
1	base		<p>sym016</p>
2	collector		
3	emitter		
4	collector		
SOT89			
1	emitter		<p>sym042</p>
2	collector		
3	base		

3. Ordering information

Table 4. Ordering information

Type number ^[1]	Package		
	Name	Description	Version
BC637 ^[2]	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54
BCP55	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223
BCX55	SC-62	plastic surface-mounted package; collector pad for good heat transfer; 3 leads	SOT89

[1] Valid for all available selection groups.

[2] Also available in SOT54A and SOT54 variant packages (see [Section 2](#) and [Section 9](#)).

4. Marking

Table 5. Marking codes

Type number	Marking code
BC637	C637
BC637-16	C63716
BCP55	BCP55
BCP55-10	BCP55/10
BCP55-16	BCP55/16
BCX55	BE
BCX55-10	BG
BCX55-16	BM

5. Limiting values

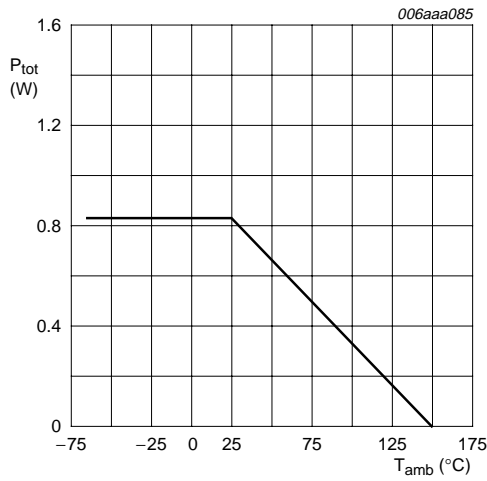
Table 6. Limiting values*In accordance with the Absolute Maximum Rating System (IEC 60134).*

Symbol	Parameter	Conditions	Min	Max	Unit		
V_{CBO}	collector-base voltage	open emitter	-	60	V		
V_{CEO}	collector-emitter voltage	open base	-	60	V		
V_{EBO}	emitter-base voltage	open collector	-	5	V		
I_C	collector current		-	1	A		
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	1.5	A		
I_{BM}	peak base current	single pulse; $t_p \leq 1$ ms	-	0.2	A		
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C					
			BC637	[1]	-	0.83	W
			BCP55	[1]	-	0.64	W
				[2]	-	0.96	W
				[3]	-	1.25	W
			BCX55	[1]	-	0.5	W
[2]	-	0.85	W				
[3]	-	1.25	W				
T_j	junction temperature		-	150	°C		
T_{amb}	ambient temperature		-65	+150	°C		
T_{stg}	storage temperature		-65	+150	°C		

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

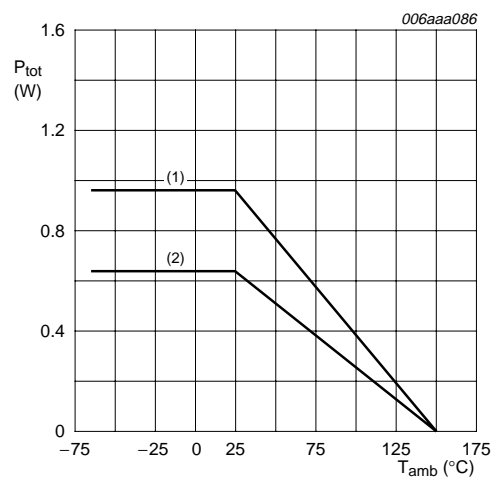
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



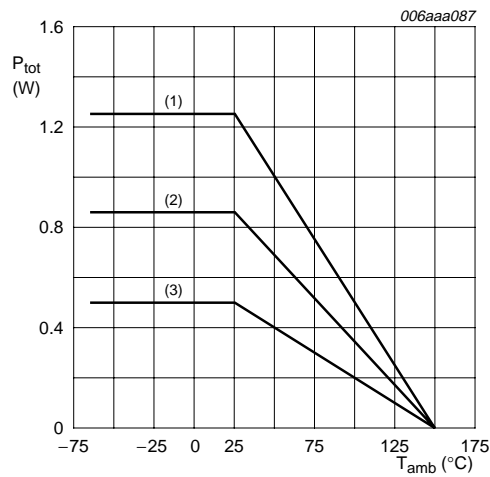
FR4 PCB, standard footprint

Fig 1. Power derating curve SOT54



- (1) FR4 PCB, mounting pad for collector 1 cm²
- (2) FR4 PCB, standard footprint

Fig 2. Power derating curves SOT223



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

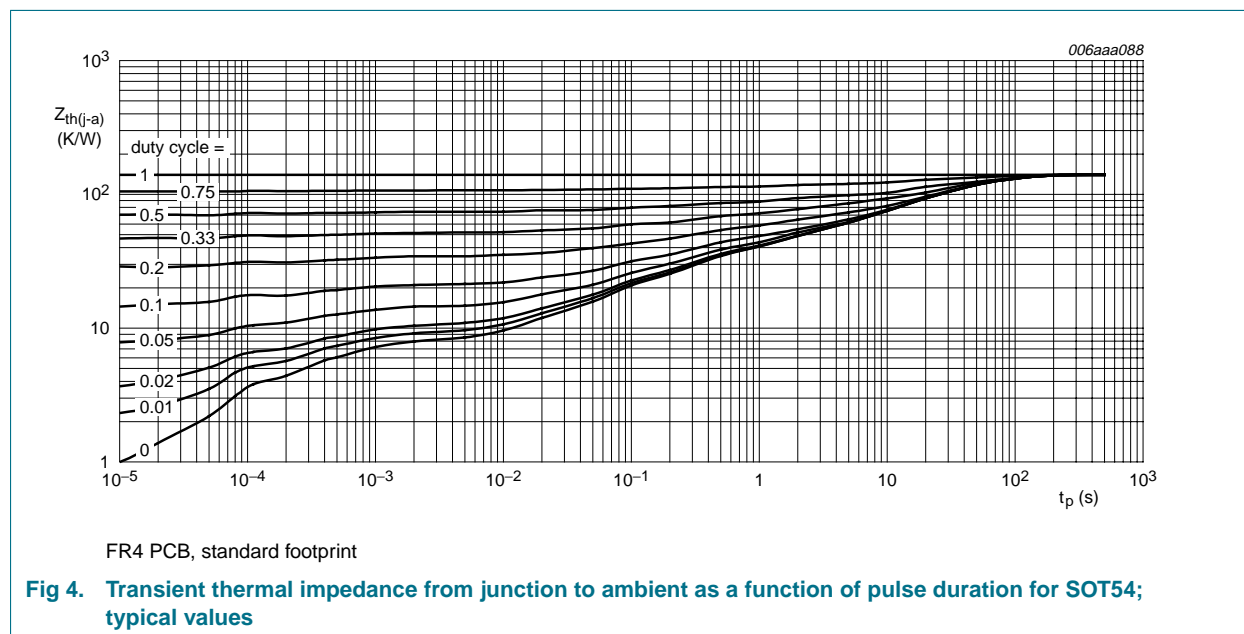
Fig 3. Power derating curves SOT89

6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit		
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air						
			BC637	[1]	-	-	150	K/W
			BCP55	[1]	-	-	195	K/W
				[2]	-	-	130	K/W
			BCX55	[1]	-	-	250	K/W
				[2]	-	-	145	K/W
[3]	-	-		100	K/W			
$R_{th(j-sp)}$	thermal resistance from junction to solder point							
		BC637	-	-	40	K/W		
		BCP55	-	-	17	K/W		
		BCX55	-	-	30	K/W		

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



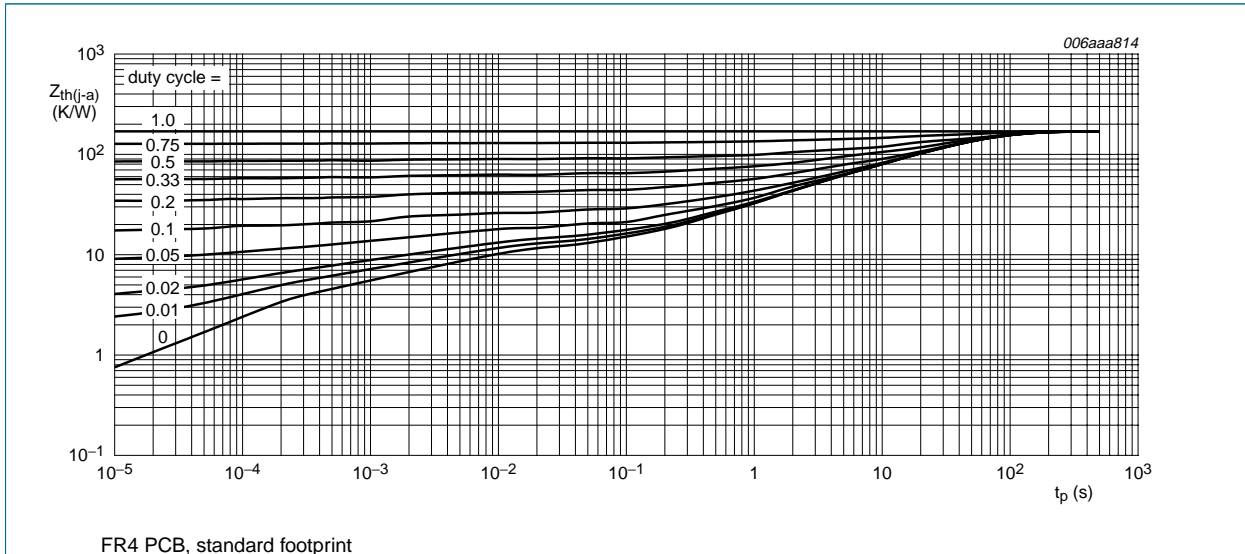


Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values

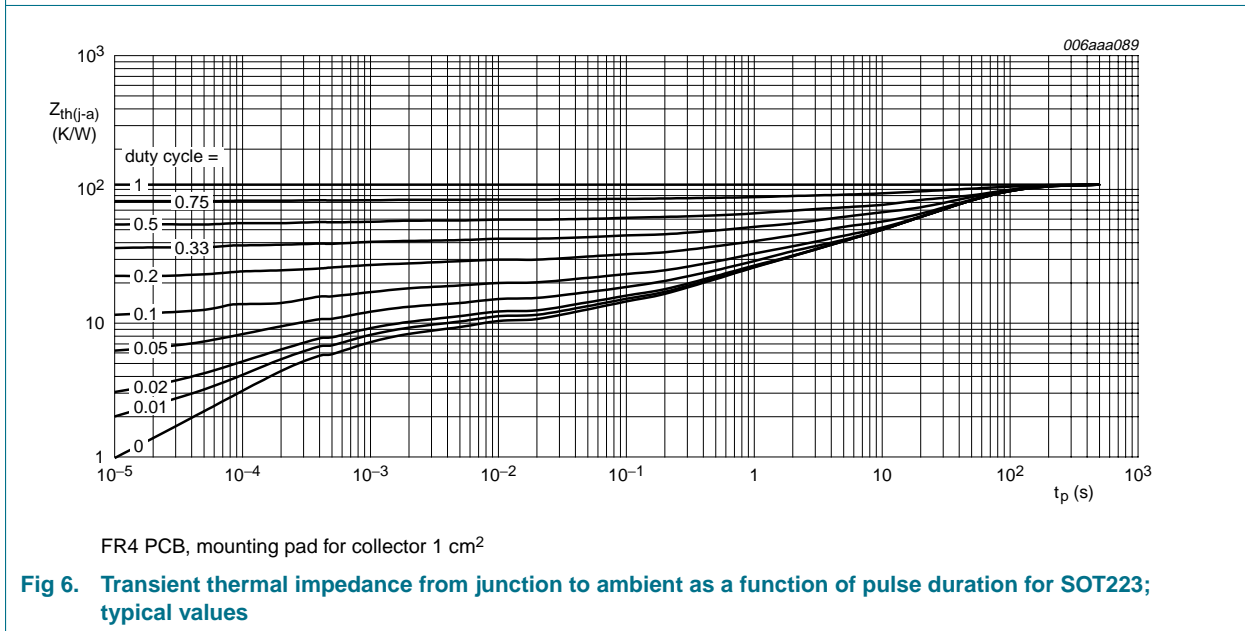
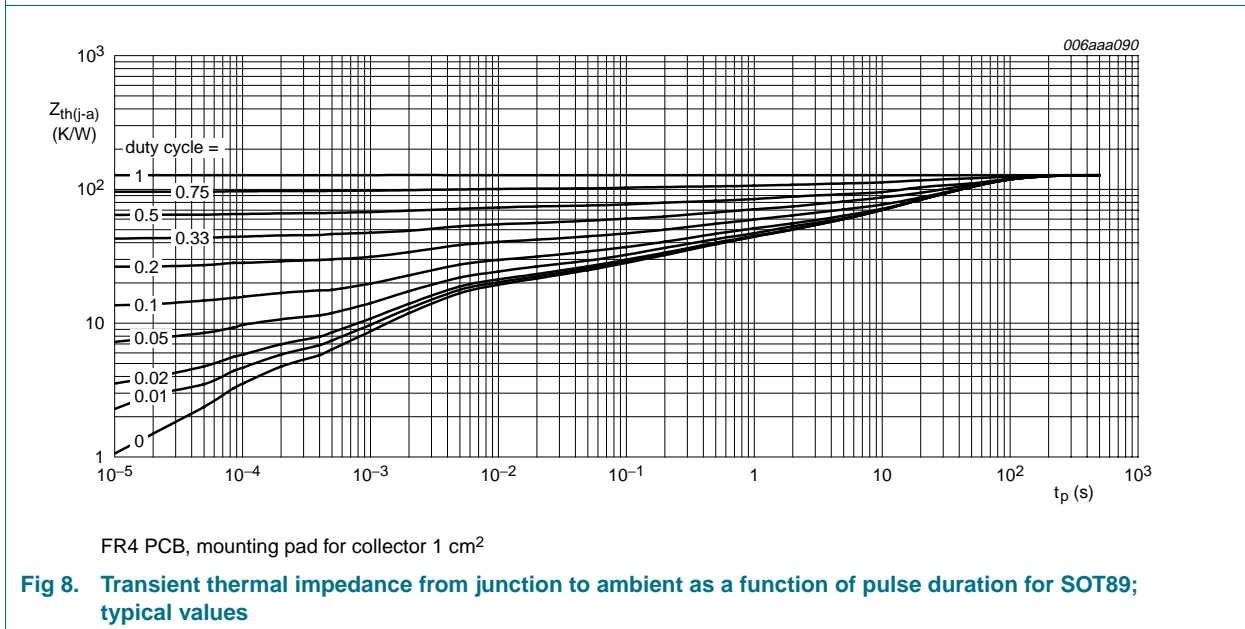
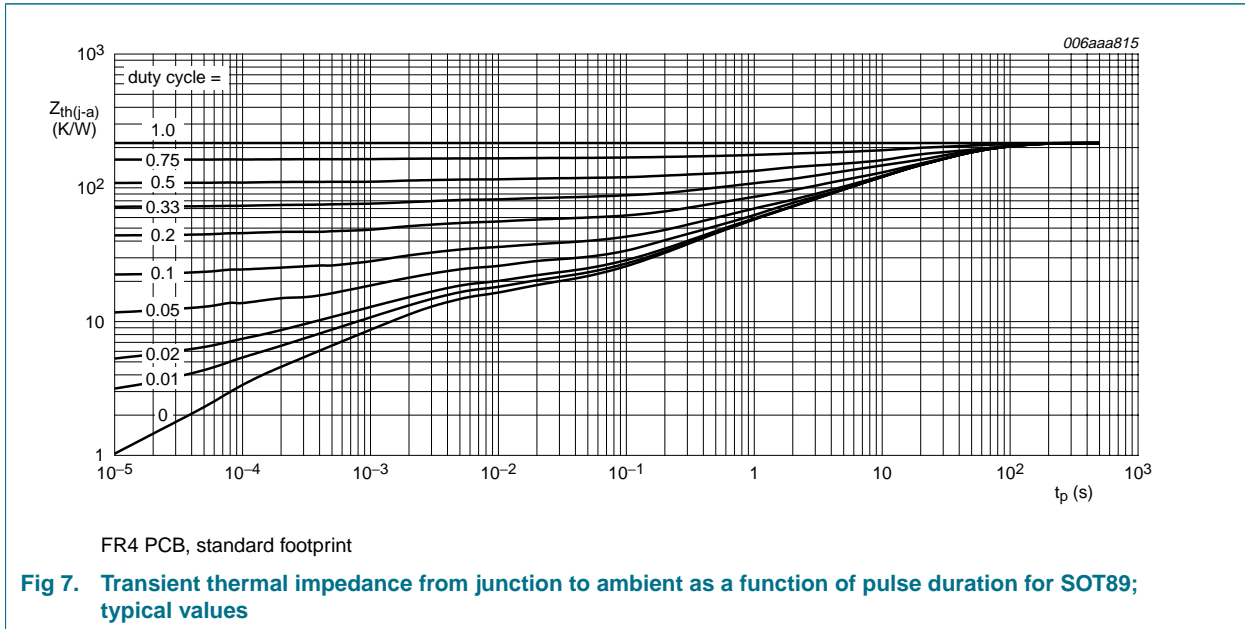
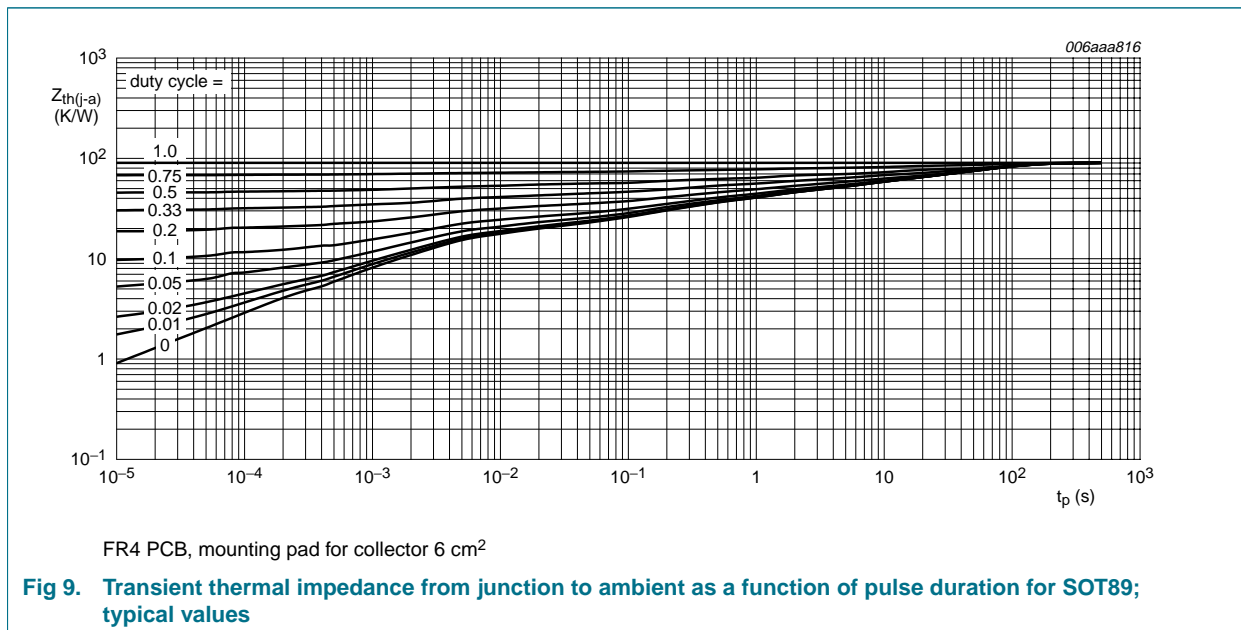


Fig 6. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values





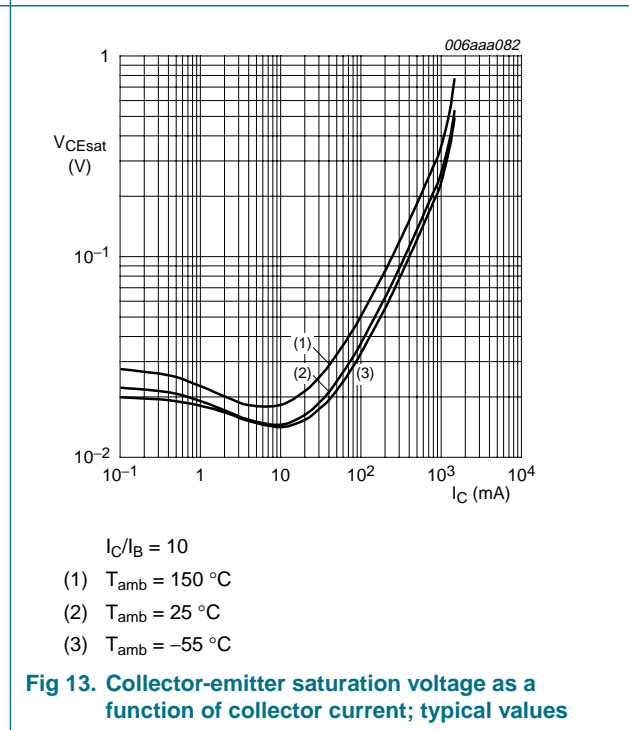
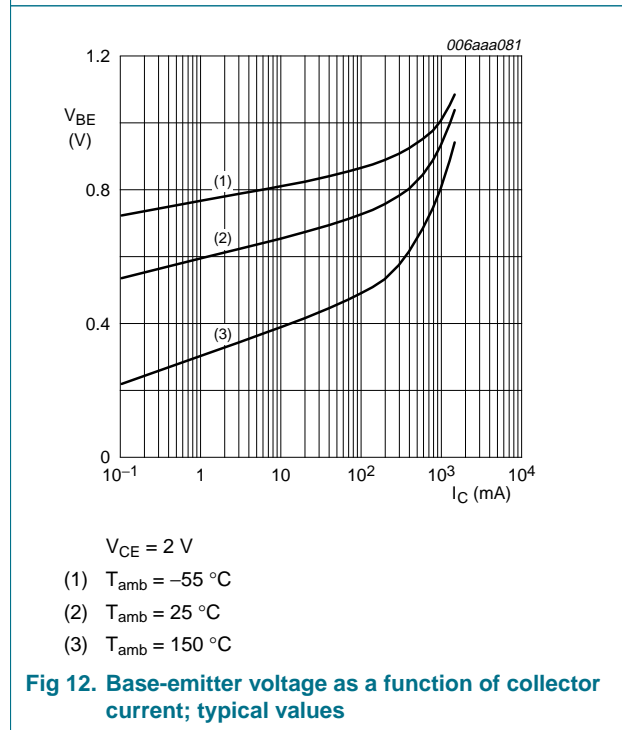
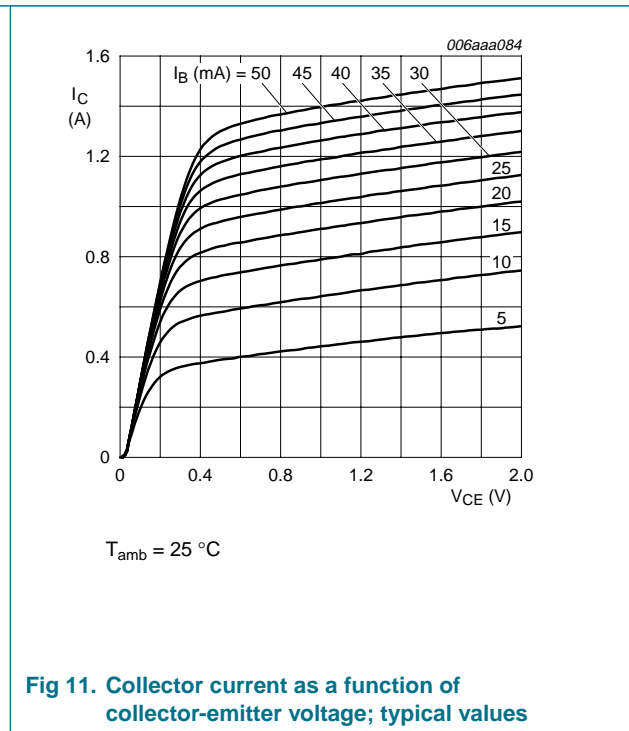
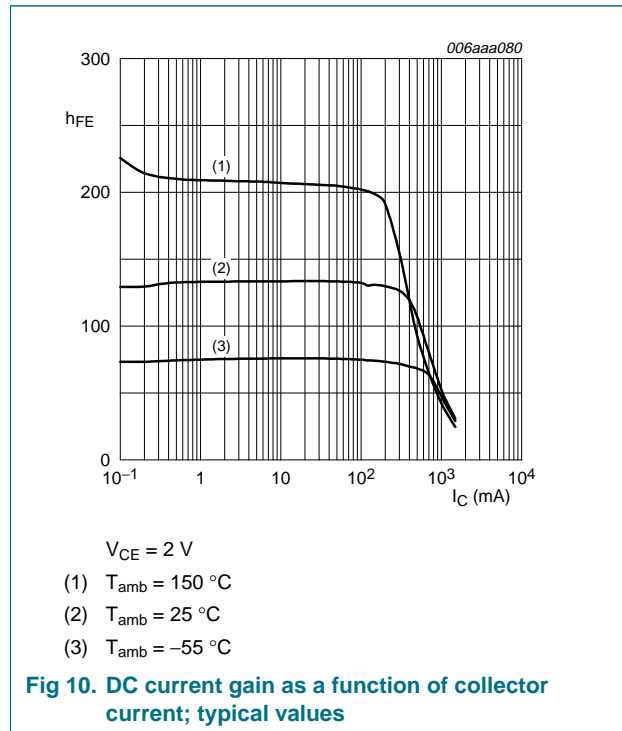
7. Characteristics

Table 8. Characteristics

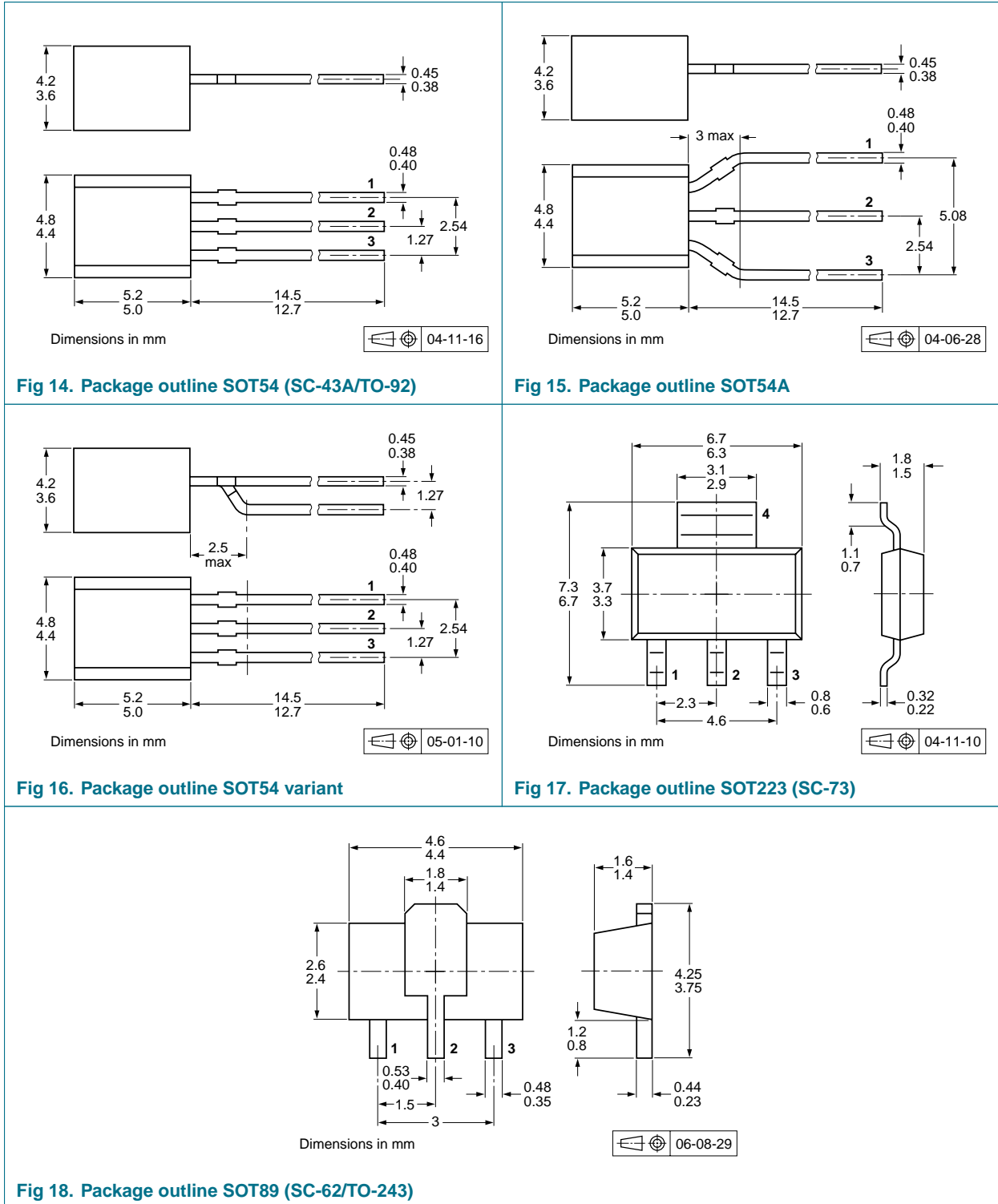
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A	-	-	100	nA
		V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V				
		I _C = 5 mA	63	-	-	
		I _C = 150 mA	63	-	250	
		I _C = 500 mA	[1] 40	-	-	
	DC current gain	V _{CE} = 2 V				
	h _{FE} selection -10	I _C = 150 mA	63	-	160	
	h _{FE} selection -16	I _C = 150 mA	100	-	250	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1] -	-	500	mV
V _{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1] -	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = I _e = 0 A; f = 1 MHz	-	6	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz	100	180	-	MHz

[1] Pulse test: t_p ≤ 300 μs; δ = 0.02.



8. Package outline



9. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.^[1]

Type number ^[2]	Package	Description	Packing quantity			
			1000	4000	5000	10000
BC637	SOT54	bulk, straight leads	-	-	-412	-
	SOT54A	tape and reel, wide pitch	-	-	-	-116
		tape ammopack, wide pitch	-	-	-	-126
	SOT54 variant	bulk, delta pinning	-	-	-112	-
BCP55	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135	-	-
BCX55	SOT89	8 mm pitch, 12 mm tape and reel; T1	^[3] -115	-135	-	-
		8 mm pitch, 12 mm tape and reel; T3	^[4] -120	-	-	-

[1] For further information and the availability of packing methods, see [Section 12](#).

[2] Valid for all available selection groups.

[3] T1: normal taping

[4] T3: 90° rotated taping

10. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC637_BCP55_BCX55_7	20070625	Product data sheet	-	BC637_BCP55_BCX55_6
Modifications:		<ul style="list-style-type: none"> • The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. • Legal texts have been adapted to the new company name where appropriate. • Table 1 “Product overview”: amended • Section 1.2 “Features”: amended • Section 1.3 “Applications”: amended • Table 2 “Quick reference data”: I_C parameter redefined to collector current • Table 2 “Quick reference data”: I_{CM} condition added • Figure 2 and 3: amended • Table 6 “Limiting values”: I_C parameter redefined to collector current • Table 6 “Limiting values”: I_{CM} condition added • Table 6 “Limiting values”: P_{tot} values for BCP55 and BCX55 adapted • Table 7 “Thermal characteristics”: R_{th(j-a)} values for BCP55 and BCX55 rounded • Figure 4: Z_{th} redefined to Z_{th(j-a)} transient thermal impedance from junction to ambient • Figure 4: t_p parameter redefined to pulse duration • Figure 5: added • Figure 6: Z_{th} redefined to Z_{th(j-a)} transient thermal impedance from junction to ambient • Figure 6: t_p parameter redefined to pulse duration • Figure 7: added • Figure 8: Z_{th} redefined to Z_{th(j-a)} transient thermal impedance from junction to ambient • Figure 8: t_p parameter redefined to pulse duration • Figure 9: added • Figure 11: amended • Table 9 “Packing methods”: new packing method for BCX55 added • Section 11 “Legal information”: updated 		
BC637_BCP55_BCX55_6	20050218	Product data sheet	CPCN200405029	BC635_637_639_4 BCP54_55_56_5 BCX54_55_56_4
BC635_637_639_4	20011010	Product specification	-	BC635_637_639_3
BCP54_55_56_5	20030206	Product specification	-	BCP54_55_56_4
BCX54_55_56_4	20011010	Product specification	-	BCX54_55_56_3

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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