

General Description

The IH5040 family consists of seven CMOS analog switches that are intended for general-purpose applications. These switches are latch-up proof, break-beforemake single, dual, and quad versions of the popular switch formats SPST, SPDT, DPST, and 4PST. Key features of the family include a low, 1nA leakage current and a quiescent current of less than 1µA.

Maxim's IH5040 family has faster switching times than the original manufacturer's devices. All devices are bidirectional and maintain almost constant on resistance throughout their operating range. These switches are guaranteed to operate from ±4.5V to ±18V, and will switch input signals that include the supplies.

Applications

PBX, PABX

Guidance and Control Systems

Test Equipment

Sample-and-Holds

Military Radios

Features

- Improved Second Source
- Guaranteed ±4.5V to ±18V Operation
- Input Voltage Range Includes Supplies
- Latchup-Proof Construction
- TTL/CMOS Logic Compatible
- >1uA Quiescent Current
- Monolithic, Low-Power CMOS Design

Ordering Information

Pin Configurations &

Switching-State Diagrams

V+

MAXIM

IH5040-SPST

13

DIP/SO

GND

11

14

V-

D

PART	TEMP.	RANGE	PIN	-PACKAGE
SINGLE POLE, S	SINGLE THR	OW (SPST)	
IH5040CPE	0°C to	+70°C	16	Plastic DIP
IH5040CWE	0°C to	+70°C	16	Wide SO
IH5040 CJE	0°C to	+70°C	16	CERDIP
IH5040C/D	0°C to	+70°C	Die	ce*
IH5040MJE	-55°C to	+125°C	16	CERDIP**

Ordering Information continued at end of data sheet.

Contact factory for dice specifications.

TOP VIEW

** Contact factory for availability and processing to MIL-STD-883.

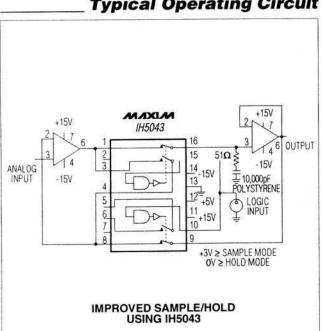
Vi 12

16

15

IN

S



Typical Operating Circuit

M/XI/M

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

V+ to V-			4\
V+ to Vp			01
V _D to V			0/
Vp to Vs			2
VL to V			31
VL to VIN			01
VL to GND			01
VIN to GND	0.1 1 1 1 1 1 1 1 1		01
Digital Inputs	and manager	(V+ + 0.3V) to (V+ - 44'	V)
Vs or Vp (Note 1)		0.3V to (V+ + 0.3	3V
Current (any terminal)	in Indian		nA

0
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
Plastic DIP (derate 10.53mW/°C above +70°C) 842mW
Wide SO (derate 9.52mW/°C above +70°C)
CERDIP (derate 10.00mW/°C above +70°C)
TO-100 (derate 6.67mW/°C above +70°C)
Operating Temperature Ranges:
IH504_C0°C to +70°C
IH504_M55°C to +125°C
Storage Temperature Range
Lead Temperature (soldering, 10sec) +300°C

Note 1: Signals on S, D, and digital inputs that exceed V- or V+ will be clamped by internal diodes. Limit forward diode current to 30mA maximum.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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ELECTRICAL CHARACTERISTICS

(V+ = 15V, V- = -15V, V_L = 5V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	COND	TIONS		IH504_	м	IH504_C		UNITS	
				MIN	TYP	MAX	MIN	ТҮР	MAX	
	lawow	V1N = 2.4V	T _A = +25°C	-1		1	-1		1	
Input Logic Current	lin(on)	VIN = 2.4V	TA = TMAX	-10		10	-10		10	μА
	Invoro	VIN = 0.8V	TA = +25°C	-1		1	-1		1	μA V V
	lin(OFF)	VIN = 0.0V	TA = TMAX	-10		10	-10		10	
Input Logic Low	VIL	TA = TMIN to	Тмах			0.8			0.8	V
Input Logic High	VIH	$T_A = T_{MIN}$ to	Тмах	2.4			2.4			V
Drain-Source On Resistance	(DOVON)	Is = 10mA,	TA = +25°C			75			80	100
Drain-Source On Resistance	rDS(ON)	VANALOG = -10V to 10V	TA = TMAX			150			130	
Channel-to-Channel rDS(ON) Match	ΔrDS(ON)				3			5		Ω
Minimum Analog-Signal Handling Capability	VANALOG			-15		15	-15		15	V
Switch-Off Leakage Current	ID/IS(OFF)	VANALOG =	TA = +25°C	-1		1	-5		5	nA
owner-on Leakage ounent	UNS(UPP)	-10V to 10V	TA = TMAX	-100		100	-100		100	11A

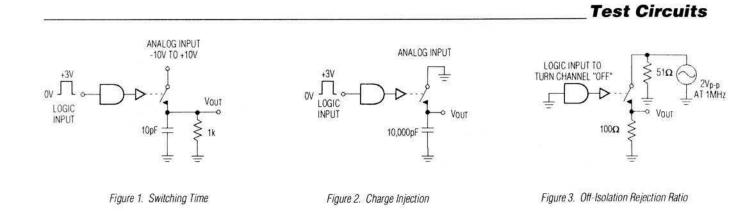
ELECTRICAL CHARACTERISTICS (continued)

(V+ = 15V, V- = -15V, V_L = 5V, T_A = +25°C, unless otherwise noted.)

DADAMETED	SYMBOL	CON	DITIONS	IH504_M IH504_C		0	UNITS			
PARAMETER	STMBUL	CON	JIIIONS	MIN	TYP	MAX	MIN	ТҮР	мах	UNITS
Switch-On Leakage Current	Incom	Vp = Vs =	TA = +25°C	-2		2	-10		10	nA
Switch-On Leakage Current	ID(ON)	-10V to 10V	TA = TMAX	-200	0.0000000000000000000000000000000000000	200	-100		100	nA
Switch-On Time	ton	Figure 1				400			400	ns
Switch-Off Time	toff	Figure 1				200			200	ns
Charge Injection	Q(INJ)	Figure 2 (No	te 2)		15			20		mV
Minimum Off-Isolation Rejection Ratio	OIRR	Figure 3, CL	< 5pF		54			50		dB
V+ Quiescent Current	I+Q	VIN = 0V	$T_A = +25^{\circ}C$			1			10	
v+ Quiescent Current	I+Q	and 5V	TA = TMAX			10			100	μA
V- Quiescent Current	1.5	VIN = OV	TA = +25°C	-1			-10			
v- Quiescent Current	I-Q	and 5V	TA = TMAX	-10			-100			μA
VI Quiescent Current	1.0	VIN = 0V	TA = +25°C			1			10	
VL Quiescent Current	ILQ	and 5V	TA = TMAX			10			100	μA
Cround Quiesesst Qurrent	louin	VIN = 0V	TA = +25°C	-1		and an end of the	-10			
Ground Quiescent Current	IGND	and 5V	TA = TMAX	-10			-100			μA
Minimum Channel-to-Channel Cross-Coupling Rejection Ratio	CCRR	One channe	l off (Note 2)		54			50		dB
Power-Supply Range for Continuous Operation	VOP	(Notes 2, 3)		±4.5		±18	±4.5		±18	V

Note 2: Not production tested.

Note 3: Electrical characteristics, such as on resistance, will change when power supplies other than ±15V are used.

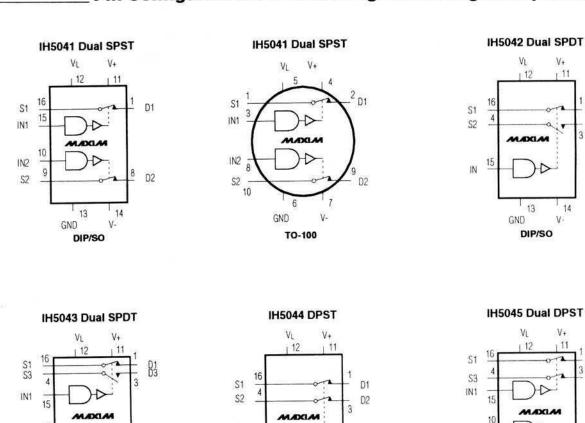


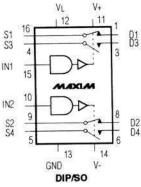
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Pin Configurations & Switching-State Diagrams (continued)







15

13

DIP/SO

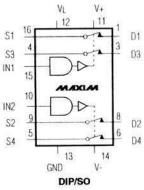
GND

14

V-

D1

D2 3



MIXIM

IH5047 4PST

IN

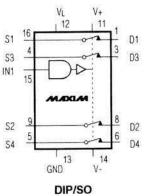
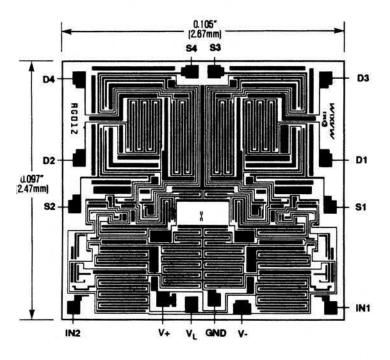


Table 1.	Using the IH5040 Family with Only Two
	Supplies

SUPPLY VOLTAGES (V)	MINIMUM LOGIC I/P FOR "1" STATE (V)				
±15	12.6				
±12	9.6				
±10	7.6				
±5	2.6				

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Chip Topography



PART TEMP. RANGE **PIN-PACKAGE** DUAL, SINGLE POLE, SINGLE THROW (DUAL SPST) IH5041CPE 0°C to +70°C 16 Plastic DIP IH5041CWE 0°C to +70°C 16 Wide SO IH5041CJE 0°C to +70°C 16 CERDIP IH5041CTW 0°C to +70°C 16 TO-100[†] IH5041C/D 0°C to +70°C Dice* IH5041MJE -55°C to +125°C 16 CERDIP** IH5041MTW -55°C to +125°C 16 TO-100[†] SINGLE POLE, DOUBLE THROW (SPDT) IH5042CPE 0°C to +70°C 16 Plastic DIP IH5042CWE 0°C to +70°C 16 Wide SO 16 CERDIP IH5042CJE 0°C to +70°C IH5042C/D 0°C to +70°C Dice* IH5042MJE -55℃ to +125℃ 16 CERDIP** DUAL, SINGLE POLE, DOUBLE THROW (DUAL SPDT) IH5043CPE 0°C to +70°C 16 Plastic DIP IH5043CWE 0°C to +70°C 16 Wide SO IH5043CJE 0°C to +70°C 16 CERDIP IH5043C/D 0°C to +70°C Dice* IH5043MJE -55°C to +125°C 16 CERDIP** DOUBLE POLE, SINGLE THROW (DPST) IH5044CPE 16 Plastic DIP 0°C to +70°C IH5044CWE 0°C to +70°C 16 Wide SO IH5044CJE 0°C to +70°C 16 CERDIP IH5044C/D 0°C to +70°C Dice* 16 CERDIP** IH5044MJE -55°C to +125°C DUAL, DOUBLE POLE, SINGLE THROW (DUAL DPST) IH5045CPE 0°C to +70°C 16 Plastic DIP IH5045CWE 0°C to +70°C 16 Wide SO IH5045CJE 0°C to +70°C 16 CERDIP IH5045C/D 0°C to +70°C Dice* IH5045MJE -55°C to +125°C 16 CERDIP** QUAD POLE, SINGLE THROW (4PST) IH5047CPE 0°C to +70°C 16 Plastic DIP IH5047CWE 0°C to +70°C 16 Wide SO IH5047CJE 0°C to +70°C 16 CERDIP IH5047C/D 0°C to +70°C Dice* 16 CERDIP** -55°C to +125°C IH5047MJE

Ordering Information (continued)

Contact factory for dice specifications.

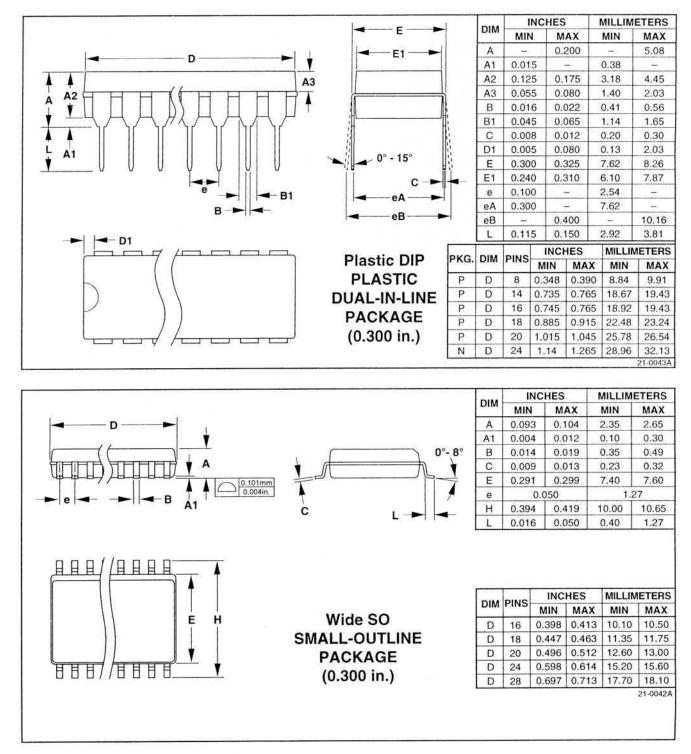
** Contact factory for availability and processing to MIL-STD-883.

[†] Contact factory for availability.



Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information .go to **www.maxim-ic.com/packages**.)



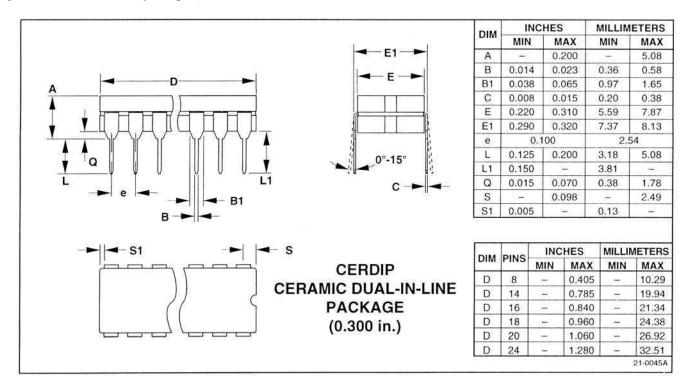


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Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)



IH5040-IH5045/IH5047



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