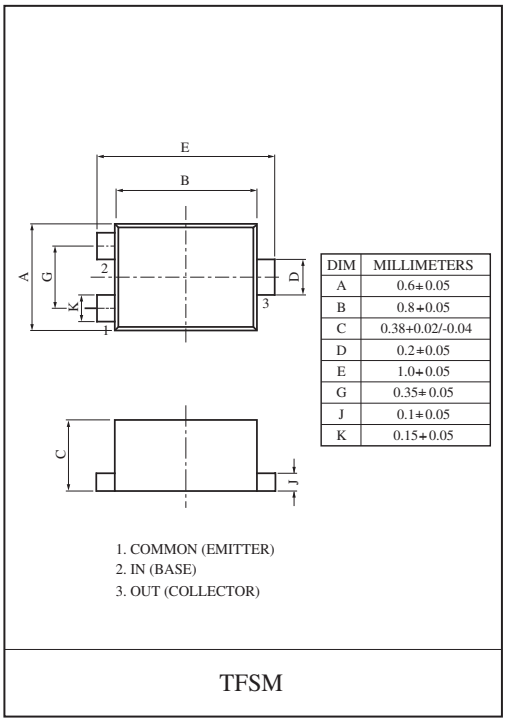
SWITCHING APPLICATION.
INTERFACE CIRCUIT AND DRIVER CIRCUIT APPLICATION.

FEATURES

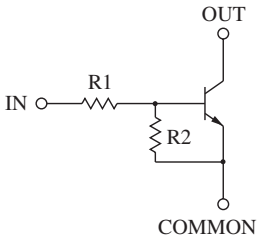
- With Built-in Bias Resistors.
- Simplify Circuit Design.
- Reduce a Quantity of Parts and Manufacturing Process.
- Thin Fine Pitch Small Package.



DIM	MILLIMETERS
A	0.6±0.05
B	0.8±0.05
C	0.38+0.02/-0.04
D	0.2±0.05
E	1.0±0.05
G	0.35±0.05
J	0.1±0.05
K	0.15±0.05

TFSM

EQUIVALENT CIRCUIT



BIAS RESISTOR VALUES

TYPE NO.	R1(kΩ)	R2(kΩ)
KRC151F	4.7	4.7
KRC152F	10	10
KRC153F	22	22
KRC154F	47	47

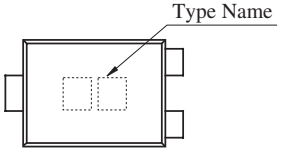
MAXIMUM RATING (Ta=25℃)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Output Voltage	KRC151F~154F	V_O	20	V
Input Voltage		V_I	10/-10	V
Output Current		I_O	50	mA
Power Dissipation		P_D	50	mW
Junction Temperature		T_j	150	℃
Storage Temperature Range		T_{stg}	-55～150	℃

MARK SPEC

TYPE	KRC151F	KRC152F	KRC153F	KRC154F
MARK	FA	FB	FC	FD

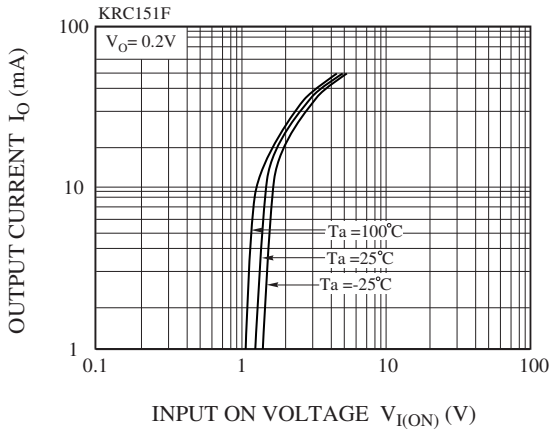
Marking



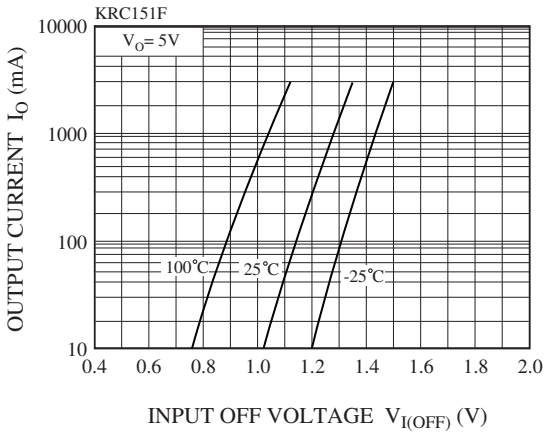
ELECTRICAL CHARACTERISTICS (Ta=25℃)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Cut-off Current	KRC151F~154F	$I_{O(OFF)}$	V_O=20V, V_I=0	-	-	500	nA
DC Current Gain	KRC151F	G_I	V_O=5V, I_O=10mA	30	-	-	
	KRC152F			60	-	-	
	KRC153F			100	-	-	
	KRC154F			120	-	-	
Output Voltage	KRC151F~154F	$V_{O(ON)}$	I_O=5mA, I_I=0.25mA	-	-	0.15	V
Input Voltage (ON)	KRC 151F	$V_{I(ON)}$	V_O=0.2V, I_O=5mA	-	-	2.0	V
	KRC 152F			-	-	2.2	
	KRC 153F			-	-	2.7	
	KRC 154F			-	-	3.6	
Input Voltage (OFF)	KRC 151F~154F	$V_{I(OFF)}$	V_O=5V, I_O=0.1mA	0.8	-	1.5	V
Input Current	KRC 151F	I_I	V_I=5V	-	-	1.8	mA
	KRC 152F			-	-	0.88	
	KRC 153F			-	-	0.36	
	KRC 154F			-	-	0.18	

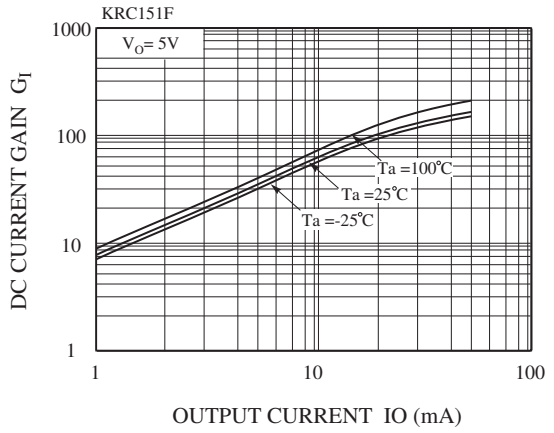
I_O - $V_{I(ON)}$



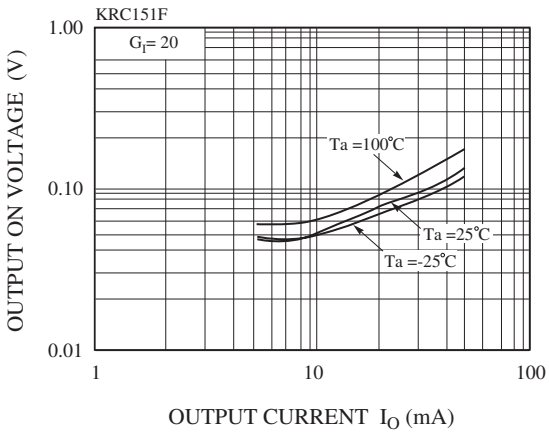
I_O - $V_{I(OFF)}$



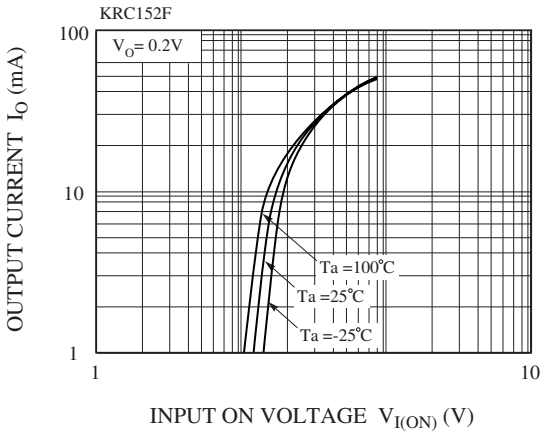
G_I - I_O



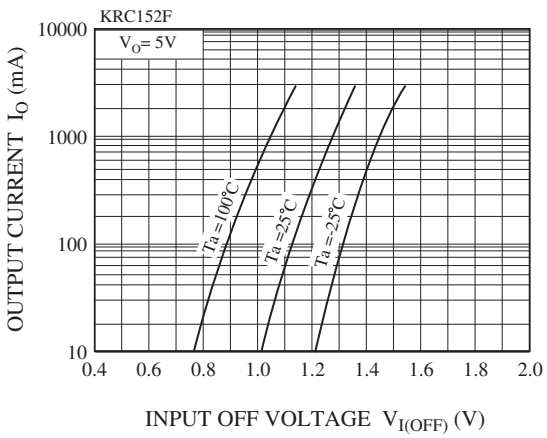
$V_{O(ON)}$ - I_O



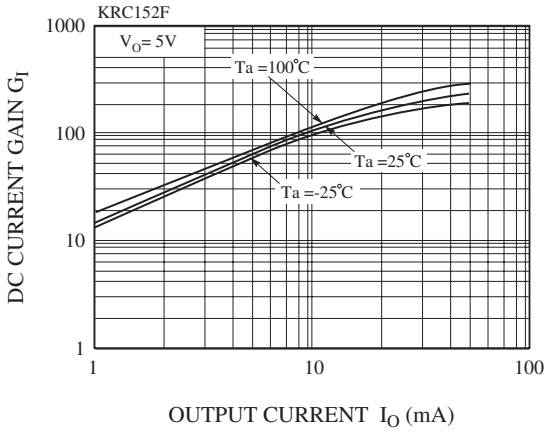
I_O - $V_{I(ON)}$



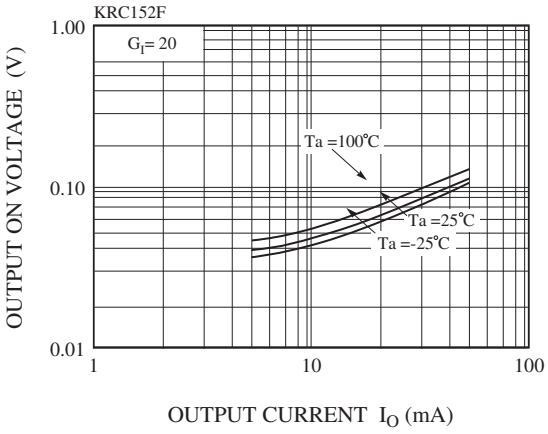
I_O - $V_{I(OFF)}$



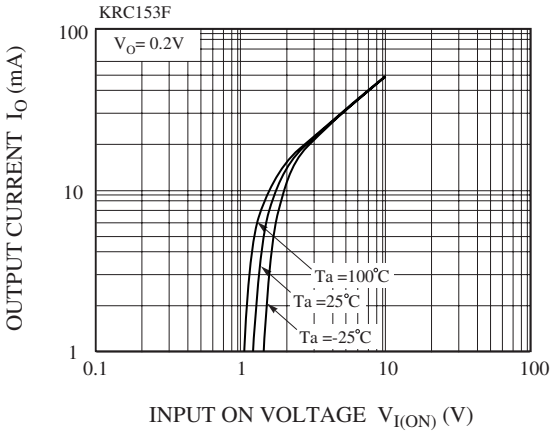
G_I - I_O



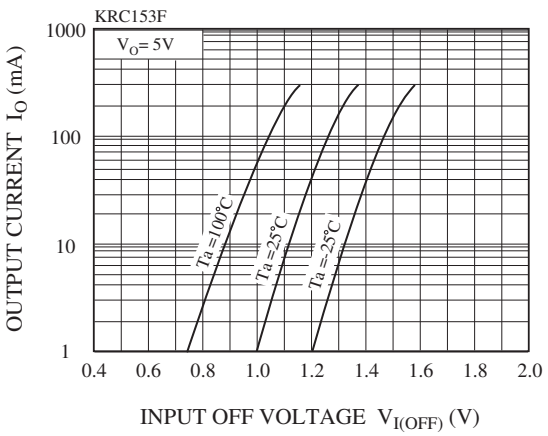
$V_{O(ON)}$ - I_O



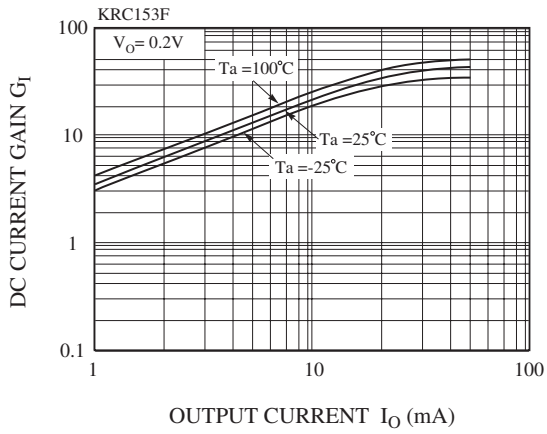
I_O - $V_{I(ON)}$



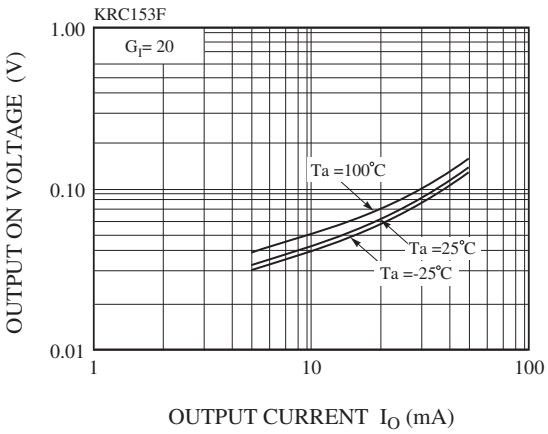
I_O - $V_{I(OFF)}$



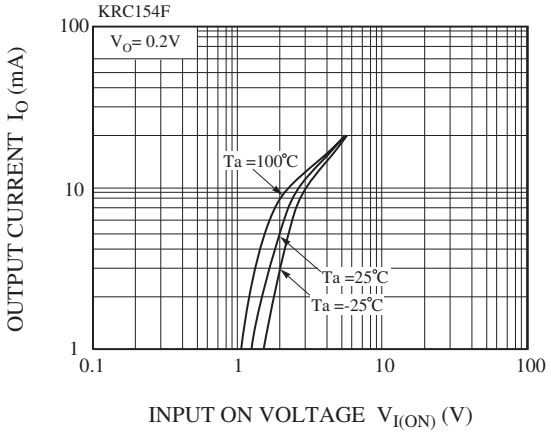
G_I - I_O



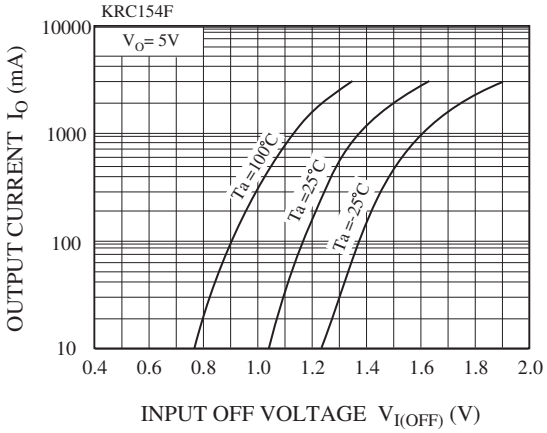
$V_{O(ON)}$ - I_O



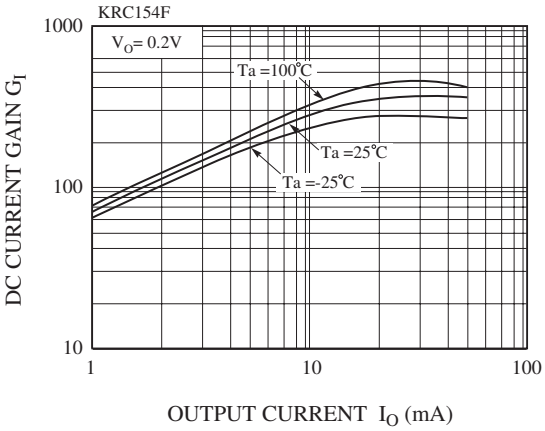
I_O - $V_{I(ON)}$



I_O - $V_{I(OFF)}$



G_I - I_O



$V_{O(ON)}$ - I_O