AUTOMOTIVE MOSFET

International Rectifier

AUIRF7343Q

HEXFET® Power MOSFET

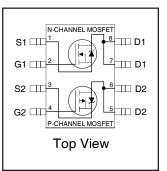
Features

- Advanced Planar Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- · Available in Tape & Reel
- 150°C Operating Temperature
- Automotive [Q101] Qualified*
- Lead-Free, RoHS Compliant

Description

Specifically designed for Automotive applications, these HEXFET® Power MOSFET's in a Dual SO-8 package utilize the lastest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of these Automotive qualified HEXFET Power MOSFET's are a 150°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These benefits combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.

The efficient SO-8 package provides enhanced thermal characteristics and dual MOSFET die capability making it ideal in a variety of power applications. This dual, surface mount SO-8 can dramatically reduce board space and is also available in Tape & Reel.



	_	
	N-Ch	P-Ch
V _{(BR)DSS}	55V	-55V
R _{DS(on)} typ.	0.043Ω	0.095Ω
max.	0.095Ω	0.105Ω
I _D	4.7A	-3.4A



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Ma	x.	Units	
	Parameter	N-Channel	P-Channel		
V _{DS}	Drain-Source Voltage	55	-55	V	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	4.7	-3.4		
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	3.8	-2.7	Α	
I _{DM}	Pulsed Drain Current ①	38	-27		
P _D @T _A = 25°C	Power Dissipation®	2.0	2.0		
P _D @T _A = 70°C	Power Dissipation®	1.0	1.3		
E _{AS}	Single Pulse Avalanche Energy®	72	114	mJ	
I _{AR}	Avalanche Current	4.7	-3.4	Α	
E _{AR}	Repetitive Avalanche Energy	0.2	0.20		
V_{GS}	Gate-to-Source Voltage	± 2	20	V	
dv/dt	Peak Diode Recovery dv/dt ②	5.0	-5.0	V/ns	
TJ	Operating Junction and	EE to	FF to 11F0		
T _{STG}	Storage Temperature Range	-55 to + 150		°C	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient ^⑤		62.5	°C/W

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^{*}Qualification standards can be found at http://www.irf.com/

Static Electrical Characteristics @ $T_J = 25$ °C (unless otherwise stated)

	Parameter		Min.	Тур.	Max.	Units	Conditions
V	Drain-to-Source Breakdown Voltage	N-Ch	55			V	$V_{GS} = 0V, I_D = 250\mu A$
V _{(BR)DSS}	Diaiii-to-Source Breakdown Voltage	P-Ch	-55			v	$V_{GS} = 0V, I_D = -250\mu A$
A)/ /AT	Prockdown Voltage Tomp, Coefficient	N-Ch		0.059	_	V/°C	Reference to 25°C, I _D = 1mA
$\Delta V_{(BR)DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient	P-Ch		0.054		V/ C	Reference to 25°C, I _D = -1mA
		N-Ch		0.043	0.050		V _{GS} = 10V, I _D = 4.7A ⁽⁴⁾
D	Static Drain-to-Source On-Resistance	0.056 0.065	Ω	V _{GS} = 4.5V, I _D = 3.8A ④			
R _{DS(on)}	Static Drain-to-Source Off-nesistance	P-Ch		0.095	0.105	1 12	V _{GS} = -10V, I _D = -3.4A ④
		P-CII		0.150	0.170		$V_{GS} = -4.5V, I_D = -2.7A$ @
V _{GS(th)}	Gate Threshold Voltage	N-Ch	1.0			V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
V GS(th)	Gate Theshold Voltage	P-Ch	-1.0			v	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
ofo	Forward Transconductance	N-Ch	7.9			S	$V_{DS} = 10V, I_{D} = 4.5A$ ④
gfs	Forward Transconductance	P-Ch	3.3			٦	$V_{DS} = -10V, I_{D} = -3.1A$ ④
		N-Ch			2.0		$V_{DS} = 55V$, $V_{GS} = 0V$
	Drain to Source Leekage Current	P-Ch			-2.0		$V_{DS} = -55V, V_{GS} = 0V$
I _{DSS}	Drain-to-Source Leakage Current	N-Ch			25	μA	$V_{DS} = 55V, V_{GS} = 0V, T_{J} = 55^{\circ}C$
		P-Ch			-25		$V_{DS} = -55V, V_{GS} = 0V, T_{J} = 55^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage				± 100	nA	$V_{GS} = \pm 20V$

Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)

	Parameter		Min.	Тур.	Max.	Units	Conditions	
Q _g	Total Gate Charge	N-Ch		24	36		N-Channel	
Q g	Total Gate Charge	P-Ch		26	38		$I_D = 4.5 A V_{DS} = 44 V, V_{GS} = 10 V$	
Q_{gs}	Gate-to-Source Charge	N-Ch		2.3	3.4	nC		
G gs	date-to-source charge	P-Ch		3.0	4.5		P-Channel	4
Q_{gd}	Gate-to-Drain ("Miller") Charge	N-Ch		7.0	10		$I_D = -3.1 \text{A V}_{DS} = -44 \text{V}, \text{ V}_{GS} = -10 \text{V}$	
G ga	date to Brain (while) Gharge	P-Ch		8.4	13			
t _{d(on)}	Turn-On Delay Time	N-Ch		8.3	12		N-Channel	
ra(on)	Turi-On Delay Time	P-Ch		14	22		$V_{DD} = 28V, ID=1.0A, RG = 6.0\Omega$	
t.	Rise Time	N-Ch		3.2	4.8		$R_D = 28\Omega$	
l _r	Tuse Time	P-Ch		10	15	ns	P-Channel	4
trs	Turn-Off Delay Time	N-Ch		32	48	113	$V_{DD} = -28V$, ID=-1.0A, RG = 6.0Ω	
t _{d(off)}	Turn-On Delay Time	P-Ch		43	64		$R_D = 28\Omega$	
t _f	Fall Time	N-Ch		13	20			
ч	Tall Tillle	P-Ch		22	32			
C _{iss}	Input Capacitance	N-Ch		740			N-Channel	
ISS	при Сараснанос	P-Ch		690			$VGS = 0V, V_{DS} = 25V, f = 1.0Mhz$	
Coss	Output Capacitance	N-Ch		190		pF		
OSS	Carpar Capacharice	P-Ch		210		"	P-Channel	
C _{rss}	Reverse Transfer Capacitance	N-Ch		71			$VGS = 0V, V_{DS} = -25V, f = 1.0Mhz$	
- 188	Tieverse Transier Oapacitance	P-Ch		86				

Diode Characteristics

	Parameter		Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current	N-Ch	_		2.0		
	(Body Diode)	P-Ch			-2.0	_	
I _{SM}	Pulsed Source Current	N-Ch	_		38	Α	
	(Body Diode) ①	P-Ch	_		-27		
V	Diode Forward Voltage	N-Ch		0.70	1.2	V	$T_J = 25^{\circ}\text{C}, I_S = 2.0\text{A}, V_{GS} = 0\text{V}$ $T_J = 25^{\circ}\text{C}, I_S = -2.0\text{A}, V_{GS} = 0\text{V}$ 3
V_{SD}	Diode i orward voltage	P-Ch		-0.80	-1.2	V	$T_J = 25^{\circ}C$, $I_S = -2.0A$, $V_{GS} = 0V$ ③
	Reverse Recovery Time	N-Ch	_	60	90	ns	N-Channel
t _{rr}	neverse necovery Time	P-Ch		54	80	115	$T_J = 25^{\circ}C$, $I_F = 2.0A$ di/dt = 100A/ μ s f
0	Reverse Recovery Charge	N-Ch	_	120	170	nC	P-Channel
Q _{rr}	neverse necovery Charge	P-Ch		85	130	TIC	$T_J = 25^{\circ}C$, $I_F = -2.0A$ di/dt = 100A/ μ s f

Notes:

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① Repetitive rating; pulse width limited by max. junction temperature.

⁽See fig. 22) N-Channel $I_{SD} \le 4.7A$, $di/dt \le 220A/\mu s$, $V_{DD} \le V_{(BR)DSS}$, $T_J \le 150^{\circ}C$ P-Channel $I_{SD} \le -3.4A$, $di/dt \le -150A/\mu s$, $V_{DD} \le V_{(BR)DSS}$, $T_J \le 150^{\circ}C$

P-Channel Starting $T_J = 25$ °C, L = 20mH $R_G = 25\Omega$, $I_{AS} = -3.4A$.

⁴ Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.

Qualification Information[†]

		Automotive (per AEC-Q101) ††				
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification				
		level is granted by extension of the higher Automore level.				
Moisture Sensiti	vity Level	SO-8 MSL1				
	Machine Model		Class M2 (200V)			
		(per AEC-Q101-002)				
505	Human Body Model		Class H1A (500V)			
ESD		(per AEC-Q101-001)				
Charged Device		Class C5 (1125V)				
	Model	(per AEC-Q101-005)				
RoHS Compliant Yes			Yes			

[†] Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

^{††} Exceptions to AEC-Q101 requirements are noted in the qualification report.

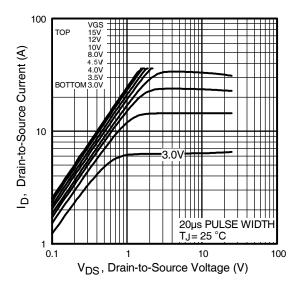


Fig 1. Typical Output Characteristics

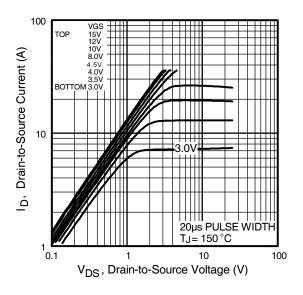


Fig 2. Typical Output Characteristics

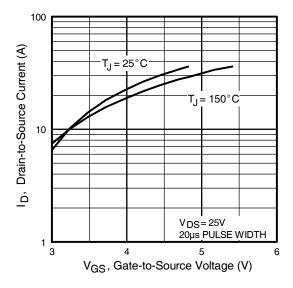


Fig 3. Typical Transfer Characteristics

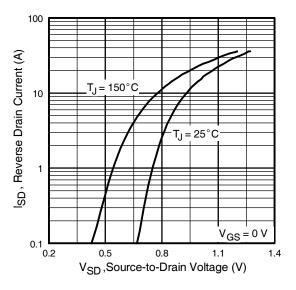


Fig 4. Typical Source-Drain Diode Forward Voltage

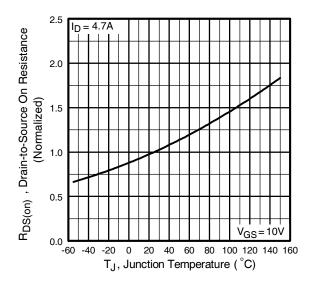


Fig 5. Normalized On-Resistance
Vs. Temperature

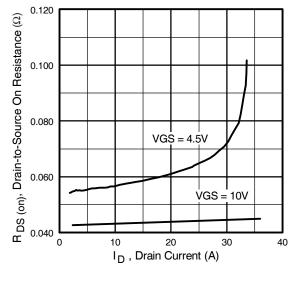


Fig 6. Typical On-Resistance Vs. Drain Current

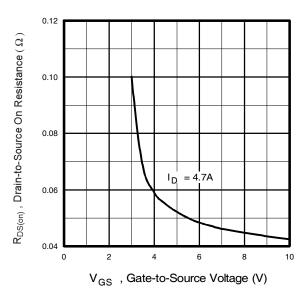


Fig 7. Typical On-Resistance Vs. Gate Voltage

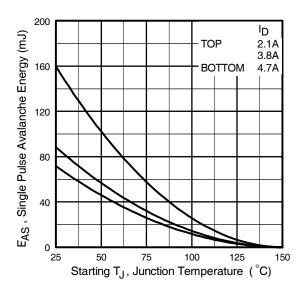
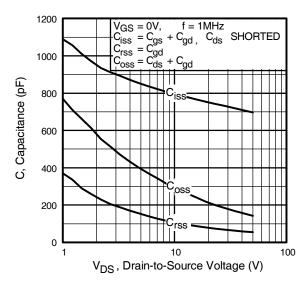


Fig 8. Maximum Avalanche Energy Vs. Drain Current





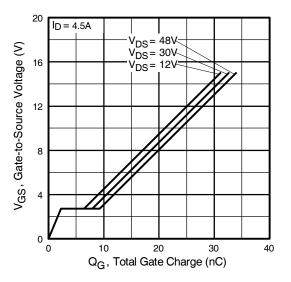


Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

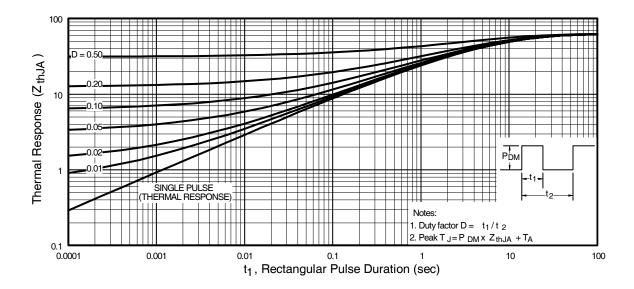


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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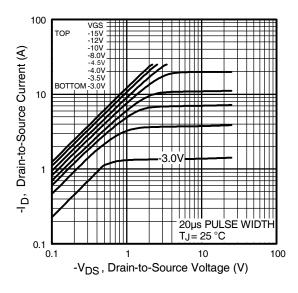


Fig 12. Typical Output Characteristics

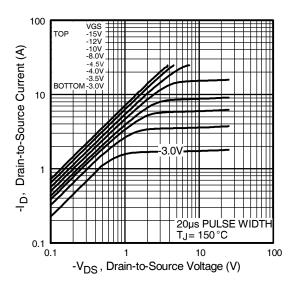


Fig 13. Typical Output Characteristics

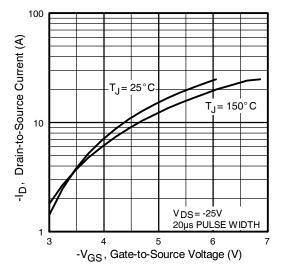


Fig 14. Typical Transfer Characteristics

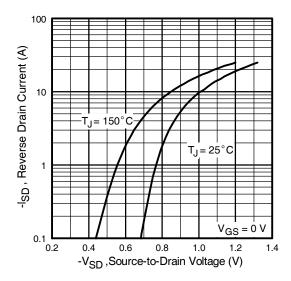


Fig 15. Typical Source-Drain Diode Forward Voltage

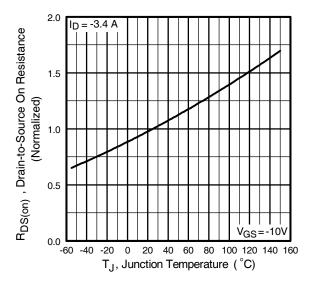


Fig 16. Normalized On-Resistance Vs. Temperature

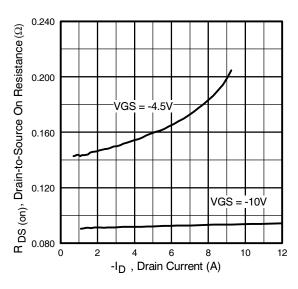


Fig 17. Typical On-Resistance Vs. Drain Current

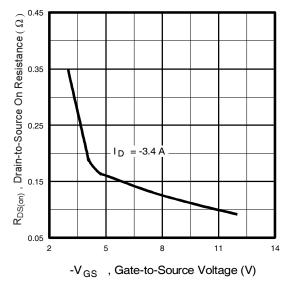


Fig 18. Typical On-Resistance Vs. Gate Voltage

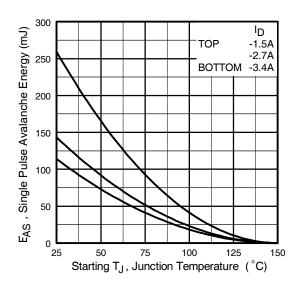


Fig 19. Maximum Avalanche Energy Vs. Drain Current

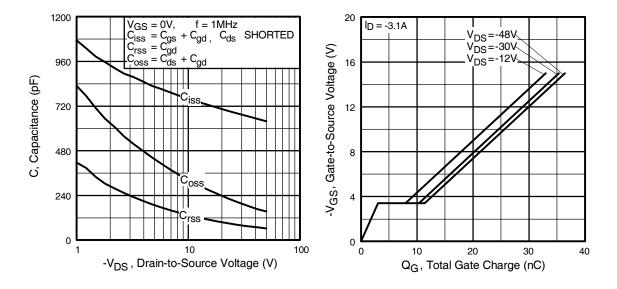


Fig 20. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 21. Typical Gate Charge Vs. Gate-to-Source Voltage

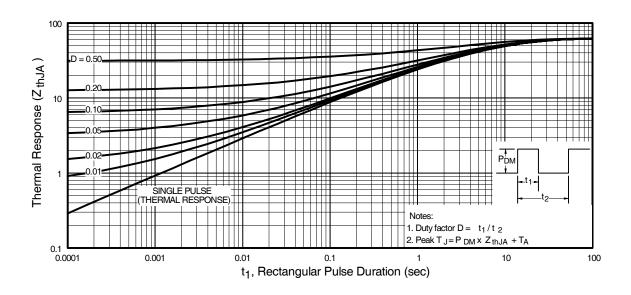
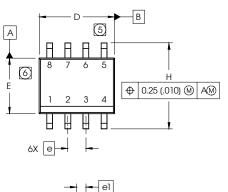


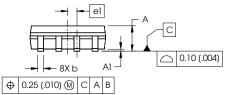
Fig 22. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

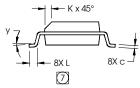
SO-8 Package Outline

Dimensions are shown in millimeters (inches)



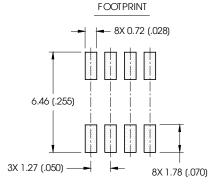
DIM	INC	HES	MILLIM	ETERS
DIIVI	MIN	MAX	MIN	MAX
Α	.0532	.0688	1.35	1.75
Al	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
С	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
Е	.1497	.1574	3.80	4.00
е	.050 B	ASIC	1.27 B	ASIC
еl	.025 B	ASIC	0.635 E	BASIC
Н	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
У	0°	8°	0°	8°



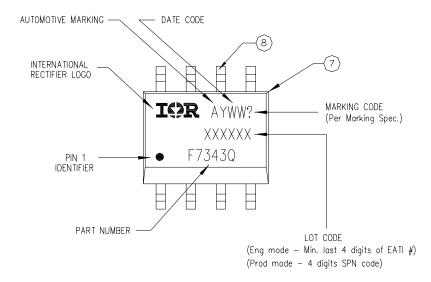


NOTES:

- 1. DIMENSIONING & TOLERANGING PER ASME Y14.5M-1994.
- 2. CONTROLLING DIMENSION: MILLIMETER
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- (5) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
- (6) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
- ① DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO ASUBSTRATE.



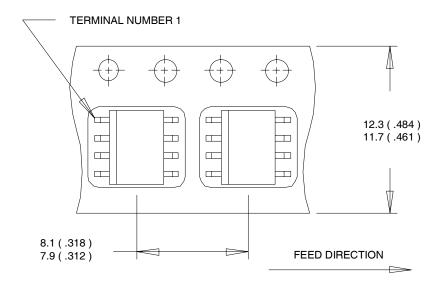
SO-8 Part Marking



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

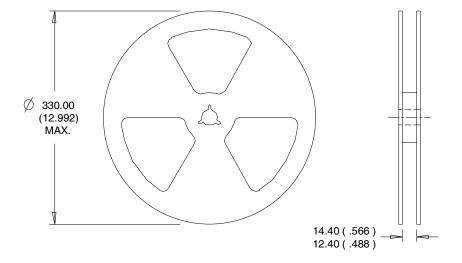
SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
- 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

AUIRF7343Q

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Ordering Information

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRF7343Q	SO-8	Tube	95	AUIRF7343Q
		Tape and Reel	4000	AUIRF7343QTR

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International

AUIRF7343Q

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