

SINGLE/DUAL CHANNEL INTELLIGENT POWER LOW SIDE SWITCH

Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

Description

The AUIPS1041(L)(R) and AUIPS1042G are three terminal Intelligent Power Switches (IPS) featuring low side MOSFETs with over-current, over-temperature, ESD protection and drain to source active clamp. The AUIPS1042G is a dual channel device while the AUIPS1041 is a single channel. These devices offer protections and the high reliability required in harsh environments. Each switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 4.5A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

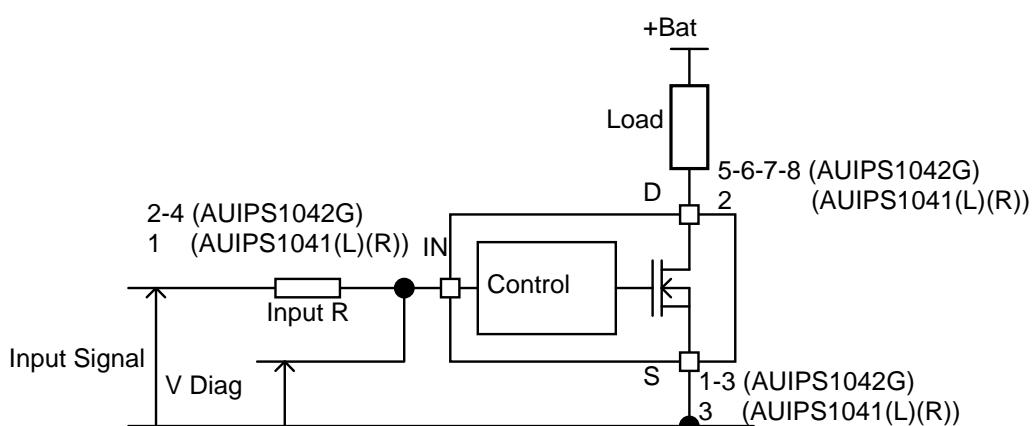
Product Summary

Rds(on)	100mΩ (max.)
Vclamp	39V
Ishutdown	4.5A (typ.)

Packages



Typical Connection



Qualification Information[†]

Qualification Level		Automotive (per AEC-Q100 ^{††})	
Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
Moisture Sensitivity Level	DPAK-3L	MSL1, 260° C (per IPC/JEDEC J-STD-020)	
	SOT223-3L	MSL2, 260° C (per IPC/JEDEC J-STD-020)	
	8L-SOICN	MSL2, 260° C (per IPC/JEDEC J-STD-020)	
ESD	Machine Model	Class M4 (+/-450V) (per AEC-Q100-003)	
	Human Body Model	Class H2 (+/-2500V) (per AEC-Q100-002)	
	Charged Device Model	Class C4 (+/-1000V) (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

[†] Qualification standards can be found at International Rectifier's web site <http://www.irf.com/>

^{††} Exceptions to AEC-Q100 requirements are noted in the qualification report.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. ($T_j = -40^\circ\text{C}..150^\circ\text{C}$, $V_{cc}=6..36\text{V}$ unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
V_{ds}	Maximum drain to source voltage	-0.3	36	V
V_{ds} cont	Maximum continuous drain to source voltage	-	28	V
V_{in}	Maximum input voltage	-0.3	6	V
I_{sd} cont.	Max diode continuous current (limited by thermal dissipation)	—	1.5	A
P_d	Maximum power dissipation (internally limited by thermal protection) $R_{th}=60^\circ\text{C}/\text{W}$ AUIPS1041L 1" sqr. Footprint		2	W
	$R_{th}=100^\circ\text{C}/\text{W}$ AUIPS1042G std. footprint		1.25	
T_j max.	Max. storage & operating temperature junction temperature	-40	150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R_{th1}	Thermal resistance junction to ambient AUIPS1041L SOT-223 std. footprint	100	—	°C/W
R_{th2}	Thermal resistance junction to ambient AUIPS1041L SOT-223 1" sqr. Footprint	60	—	
R_{th1}	Thermal resistance junction to ambient AUIPS1041R D-Pak std. footprint	70	—	
R_{th2}	Thermal resistance junction to case AUIPS1041R D-Pak	6	—	
R_{th1}	Thermal resistance junction to ambient AUIPS1042G SO-8 std. Footprint 1 die active	100	—	
R_{th1}	Thermal resistance junction to ambient AUIPS1042G SO-8 std. footprint 2 die active	130	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
V_{IH}	High level input voltage	4.5	5.5	V
V_{IL}	Low level input voltage	0	0.5	
I_{ds}	Continuous drain current, $T_{ambient}=85^\circ\text{C}$, $T_j=125^\circ\text{C}$, $V_{in}=5\text{V}$ $R_{th}=60^\circ\text{C}/\text{W}$ AUIPS1041L 1" sqr. Footprint	—	1.95	A
	Continuous drain current, $T_{ambient}=85^\circ\text{C}$, $T_j=125^\circ\text{C}$, $V_{in}=5\text{V}$ $R_{th}=50^\circ\text{C}/\text{W}$ AUIPS1041R 1" sqr. Footprint	—	2.2	
	Continuous drain current, $T_{ambient}=85^\circ\text{C}$, $T_j=125^\circ\text{C}$, $V_{in}=5\text{V}$ $R_{th}=100^\circ\text{C}/\text{W}$ AUIPS1042G 1" sqr. Footprint - 1 die active	—	1.5	
	Continuous drain current, $T_{ambient}=85^\circ\text{C}$, $T_j=125^\circ\text{C}$, $V_{in}=5\text{V}$ $R_{th}=130^\circ\text{C}/\text{W}$ AUIPS1042G 1" sqr. Footprint - 2 die active	—	0.7	
R_{in}	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max. recommended load inductance (including line inductance) (1)	—	20	μH
Max. F	Max. frequency	—	2000	Hz
Max. t rise	Max. input rising time	—	1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

Static Electrical Characteristics

T_j= -40..150°C , V_{CC}=6..28V (unless otherwise specified), typical value are given for T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{DS(on)}	ON state resistance T _j =25°C	—	80	100	mΩ	V _{IN} =5V, I _D =3A
	ON state resistance T _j =150°C	—	135	175		
I _{DSS1}	Drain to source leakage current	—	0.1	2	μA	V _{CC} =14V, T _j =25°C
I _{DSS2}	Drain to source leakage current	—	0.2	4		V _{CC} =28V, T _j =25°C
V _{clamp1}	Drain to source clamp voltage 1	36	38	—	V	I _D =10mA
V _{clamp2}	Drain to source clamp voltage 2	—	39	42		I _D =1A
V _{in clamp}	IN to source pin clamp voltage	5.5	6.5	7.5		I _{IN} =1mA
V _{th}	Input threshold voltage	—	1.7	—		I _D =10mA

Switching Electrical Characteristics

V_{CC}=14V, Resistive load=5Ω, R_{IN}=0Ω, V_{IN}=5V, T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{don}	Turn-on delay time to 20%	2	7	15	μs	See figure 2
T _r	Rise time 20% to 80%	2	7	20		
T _{doff}	Turn-off delay time to 80%	15	40	150		
T _f	Fall time 80% to 20%	4	10	20		
E _{on} + E _{off}	Turn on and off energy	—	0.2	—	mJ	

Protection Characteristics

T_j= -40..150°C , V_{CC}=6..28V (unless otherwise specified), typical value are given for T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{SD}	Over temperature threshold	150(2)	165	—	°C	See figure 1
I _{SD}	Over current threshold	2.7	4.5	6	A	See figure 1
O _V	Over voltage protection (not active when the device is ON)	34	37	—	V	
V _{reset}	IN protection reset threshold	—	1.7	—	V	
T _{reset}	Time to reset protection	15(2)	50	200	μs	V _{IN} =0V, T _j =25°C

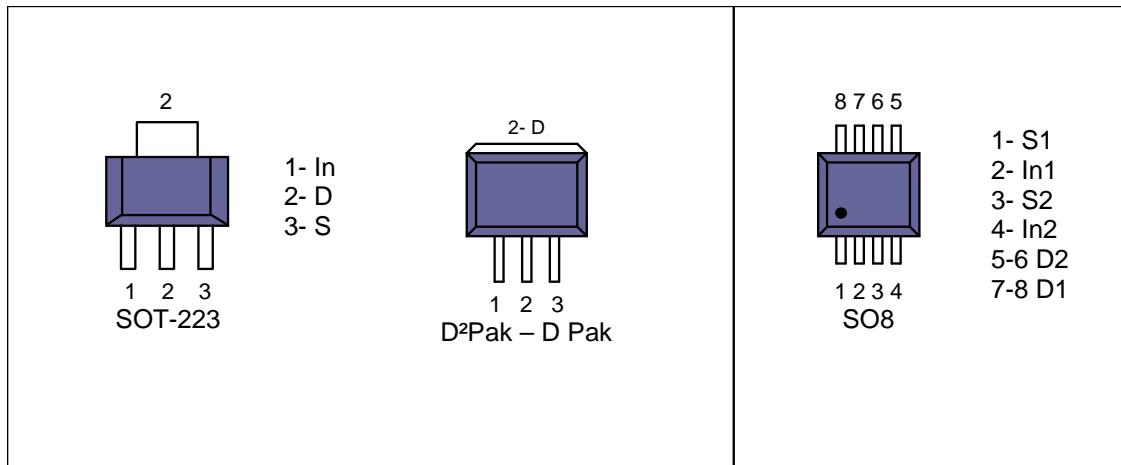
(2) Guaranteed by design

Diagnostic

T_j= -40..150°C , V_{CC}=6..28V (unless otherwise specified), typical value are given for T_j=25°C

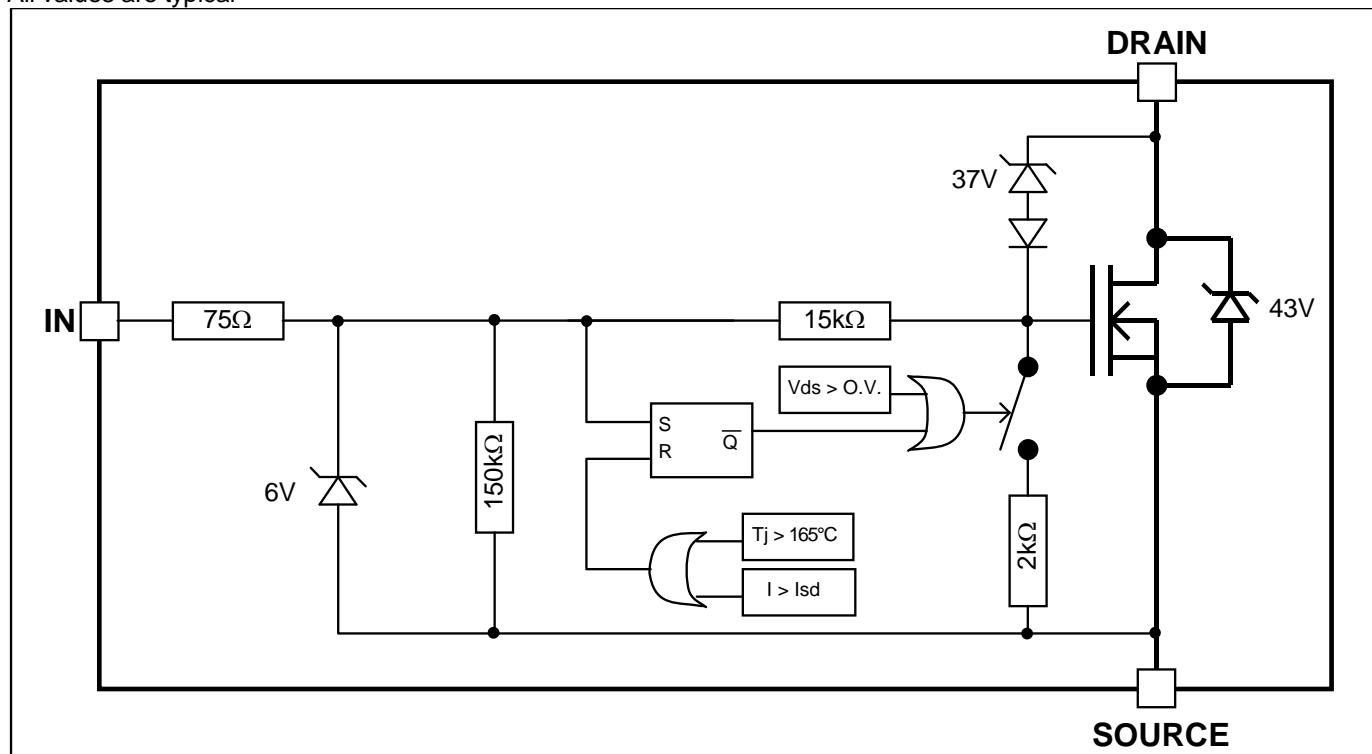
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I _{IN, on}	ON state IN positive current	10	32	80	μA	V _{IN} =5V V _{IN} =5V
I _{IN, off}	OFF state IN positive current (after protection latched)	120	230	350		

Lead Assignments



Functional Block Diagram

All values are typical



All curves are typical values. Operating in the shaded area is not recommended.

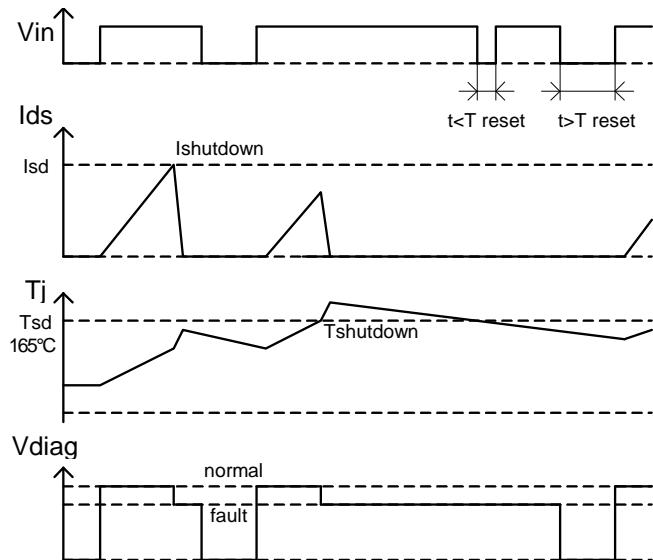


Figure 1 – Timing diagram

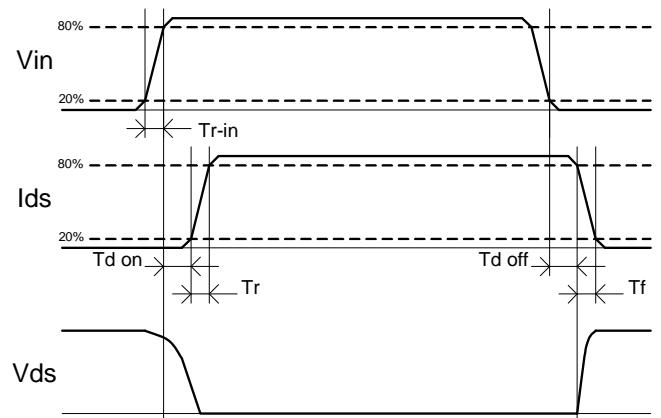


Figure 2 – IN rise time & switching definitions

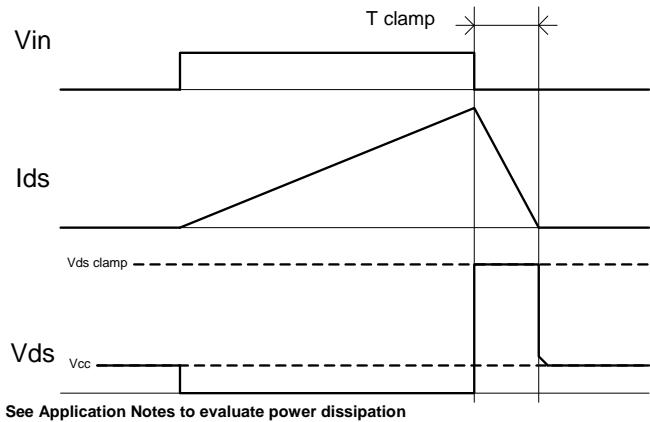


Figure 3 – Active clamp waveforms

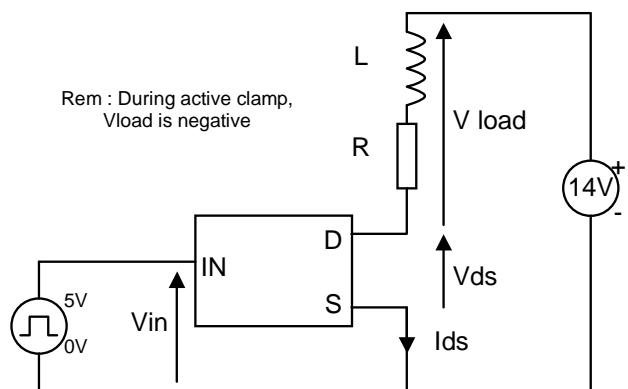


Figure 4 – Active clamp test circuit

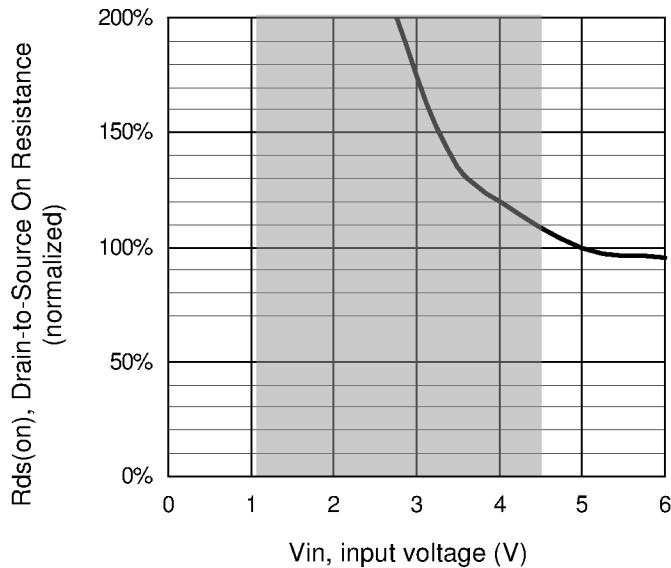


Figure 5 – Normalized R_{ds(on)} (%) Vs Input voltage (V)

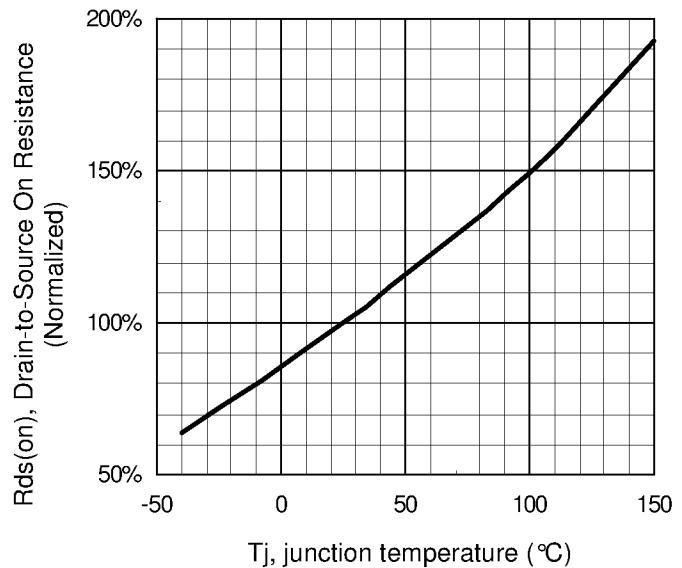


Figure 6 - Normalized R_{ds(on)} (%) Vs T_j (°C)

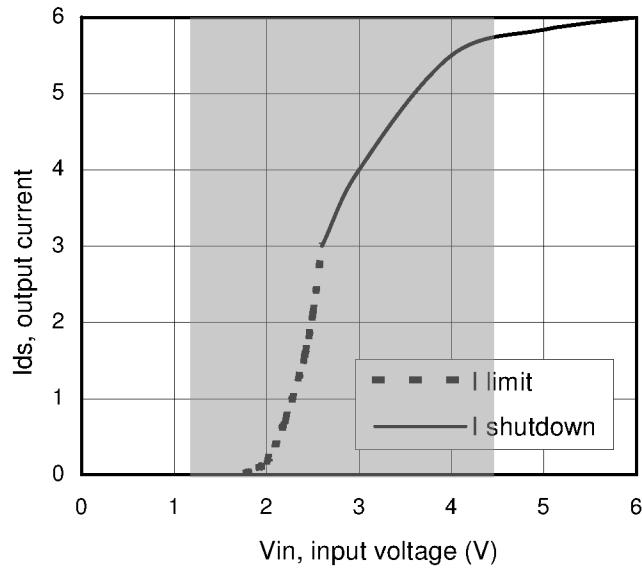


Figure 7 – Current limitation and current shutdown Vs Input voltage (V)

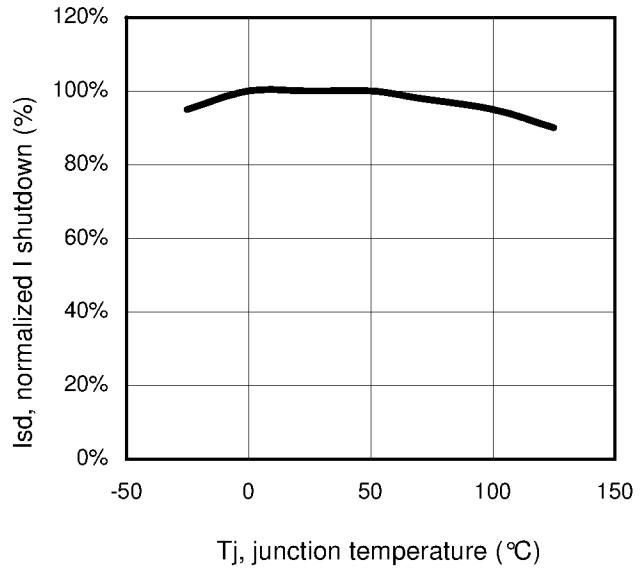


Figure 8 – Normalized I shutdown (%) Vs junction temperature (°C)

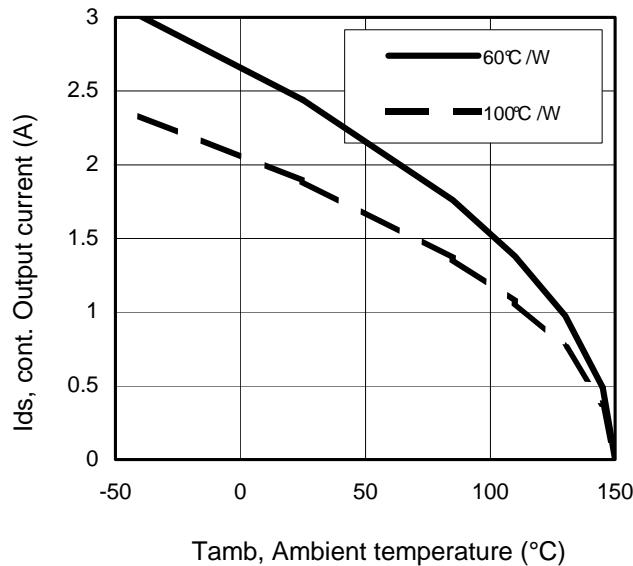


Figure 9 – Max. continuous output current (A) Vs Ambient temperature (° C)

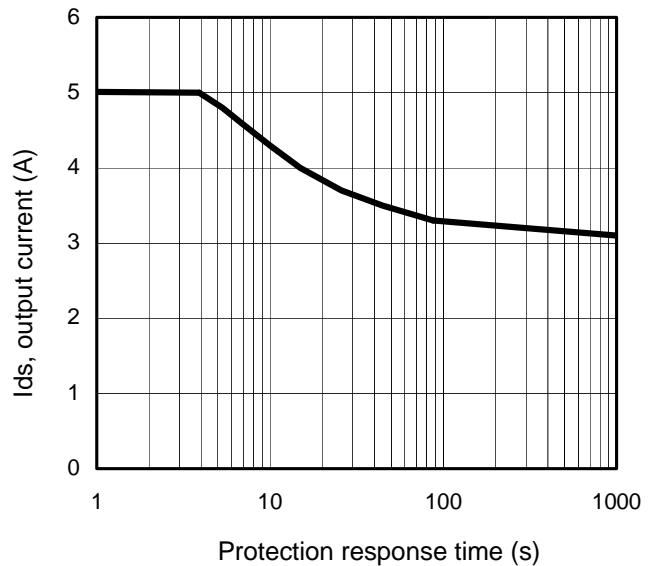


Figure 10 – Ids (A) Vs over temperature protection response time (s) / IPS1041L

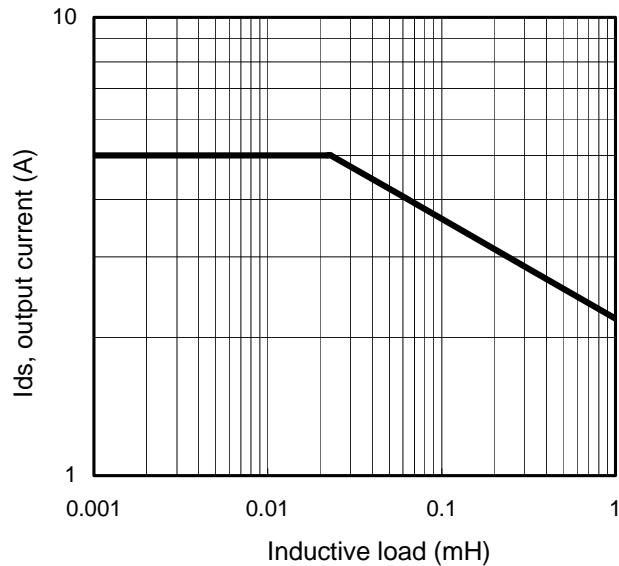


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)

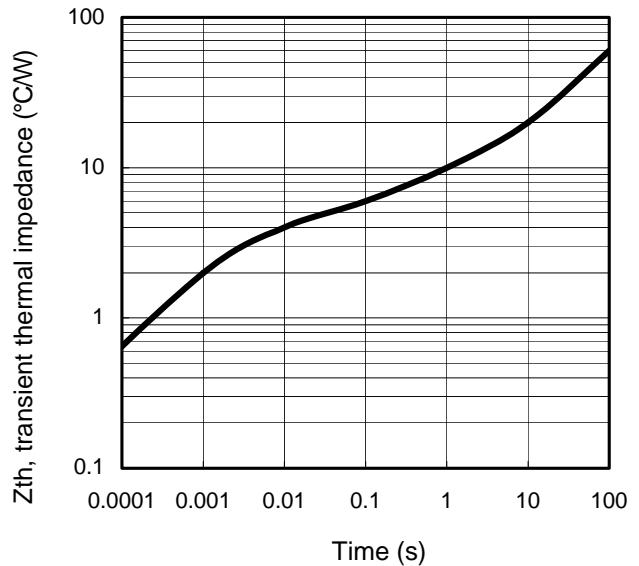


Figure 12 – Transient thermal impedance (° C/W) Vs time (s)

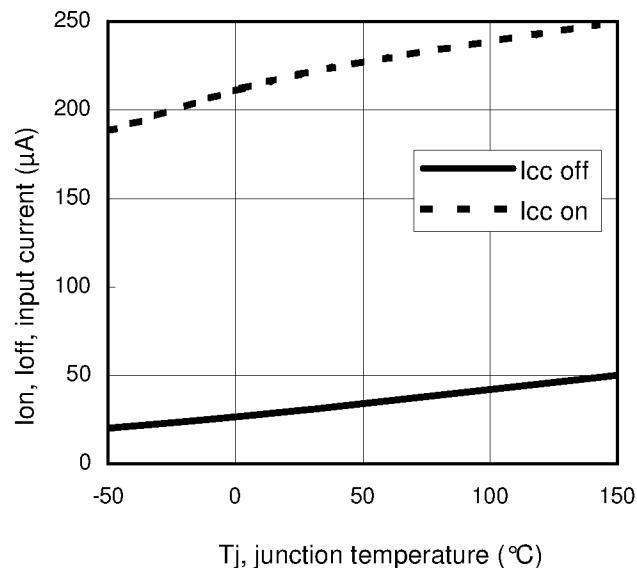


Figure 13 – Input current (µA) On and Off Vs junction temperature (°C)

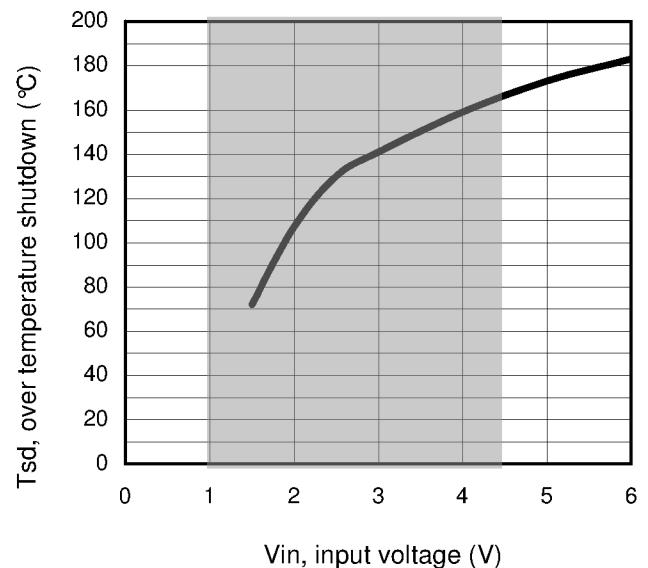
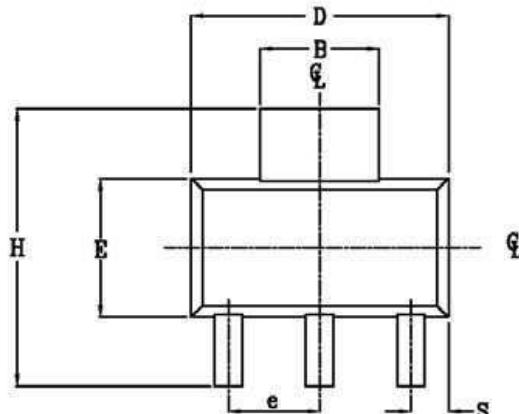


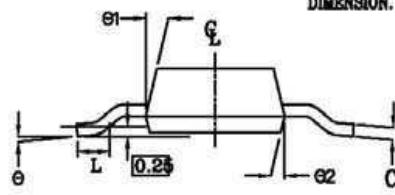
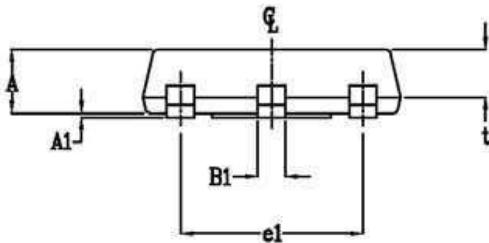
Figure 14 – Over temperature shutdown (°C) Vs input voltage (V)

Case Outline - SOT-223 - Automotive Q100 PbF MSL2 qualified



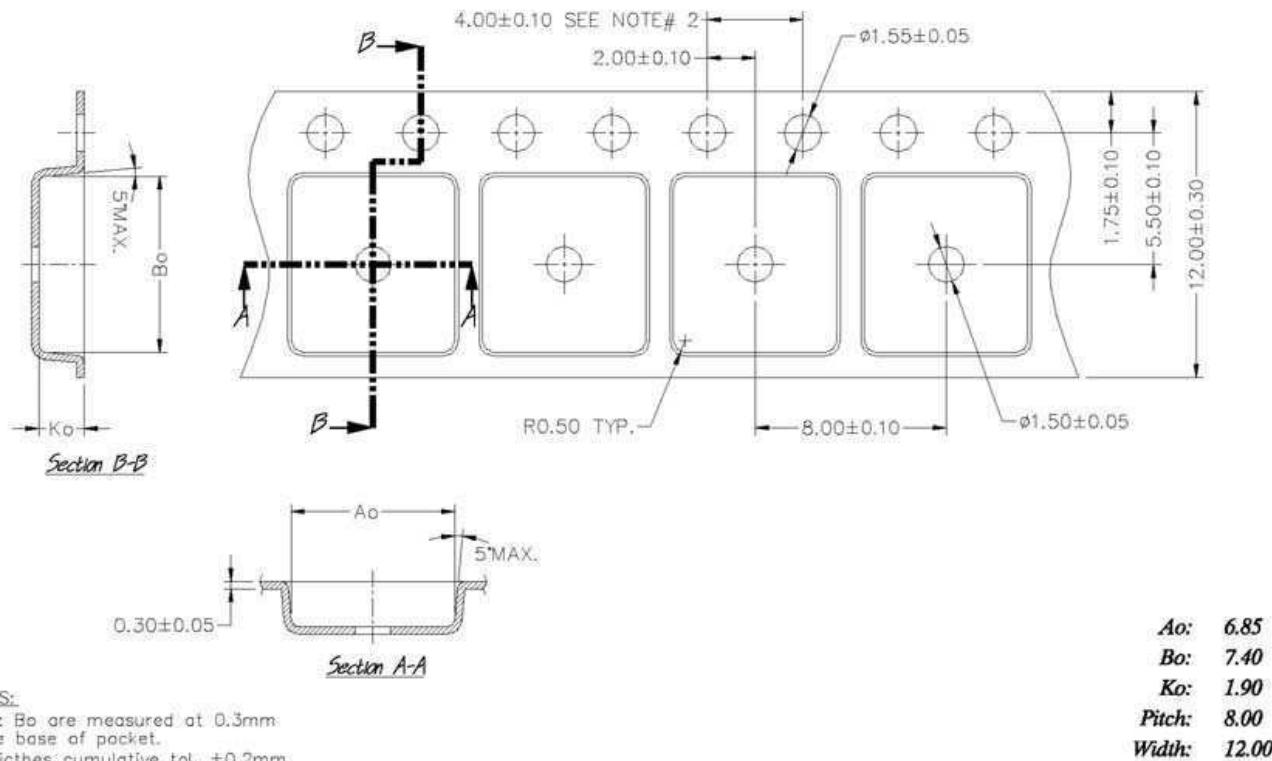
POS	MILLIMETERS		INCHES	
	MAX	MIN	MAX	MIN
1				
A	1.70	1.50	.067	.060
A1	0.10	0.02	.004	.0008
B	3.15	2.95	.124	.116
B1	0.85	0.65	.033	.028
C	0.35	0.25	.014	.010
D	6.70	6.30	.264	.248
e	2.30 NOM		.0905 NOM	
e1	4.60 NOM		.181 NOM	
E	3.70	3.30	.146	.130
H	7.30	6.70	.287	.264
S	1.05	0.85	.041	.033
t	1.30	1.10	.051	.043
Θ	10° MAX		10° MAX	
Θ1	16°	10°	16°	10°
Θ2	16°	10°	16°	10°
L	0.75 MIN		0.0295 MIN	

NOTE:
 1. PACKAGE OUTLINE EXCLUSIVE OF ANY MOLD FLASHES DIMENSION.
 2. PACKAGE OUTLINE EXCLUSIVE OF BURR DIMENSION.



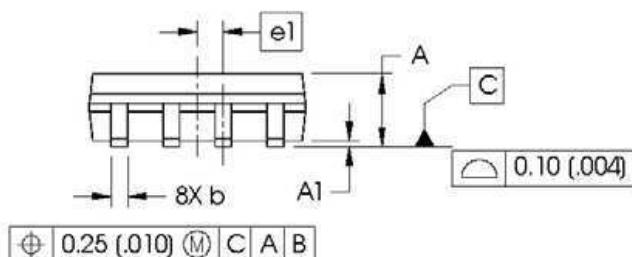
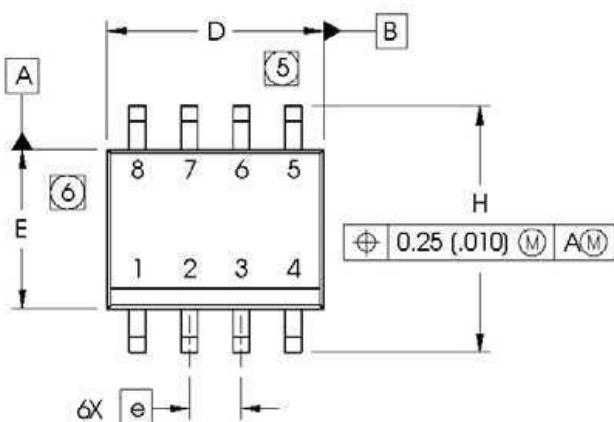
Leads and drain are plated with 100% Sn

Tape & Reel - SOT-223

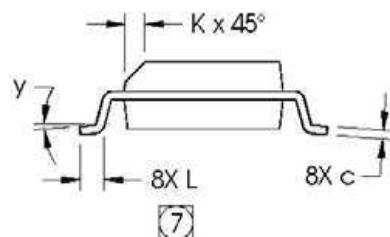


Case Outline - SO-8 - Automotive Q100 PbF MSL2 qualified

Dimensions are shown in millimeters (inches)



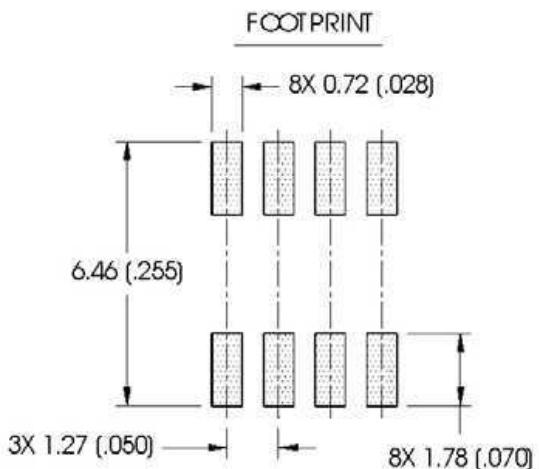
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050	BASIC	1.27	BASIC
e1	.025	BASIC	0.635	BASIC
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



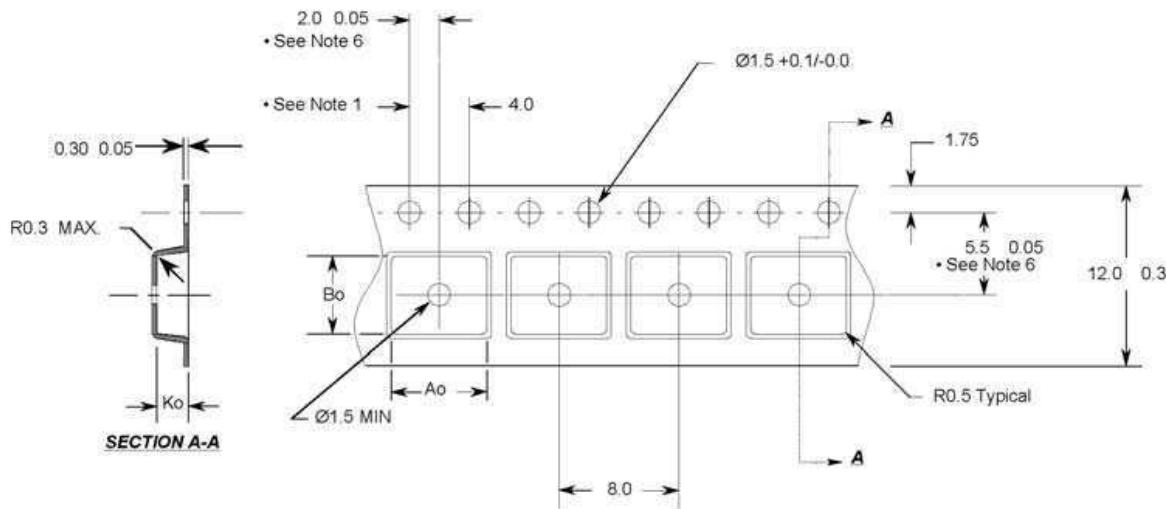
NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

Leads and drain are plated with 100% Sn



Tape & Reel - SO-8



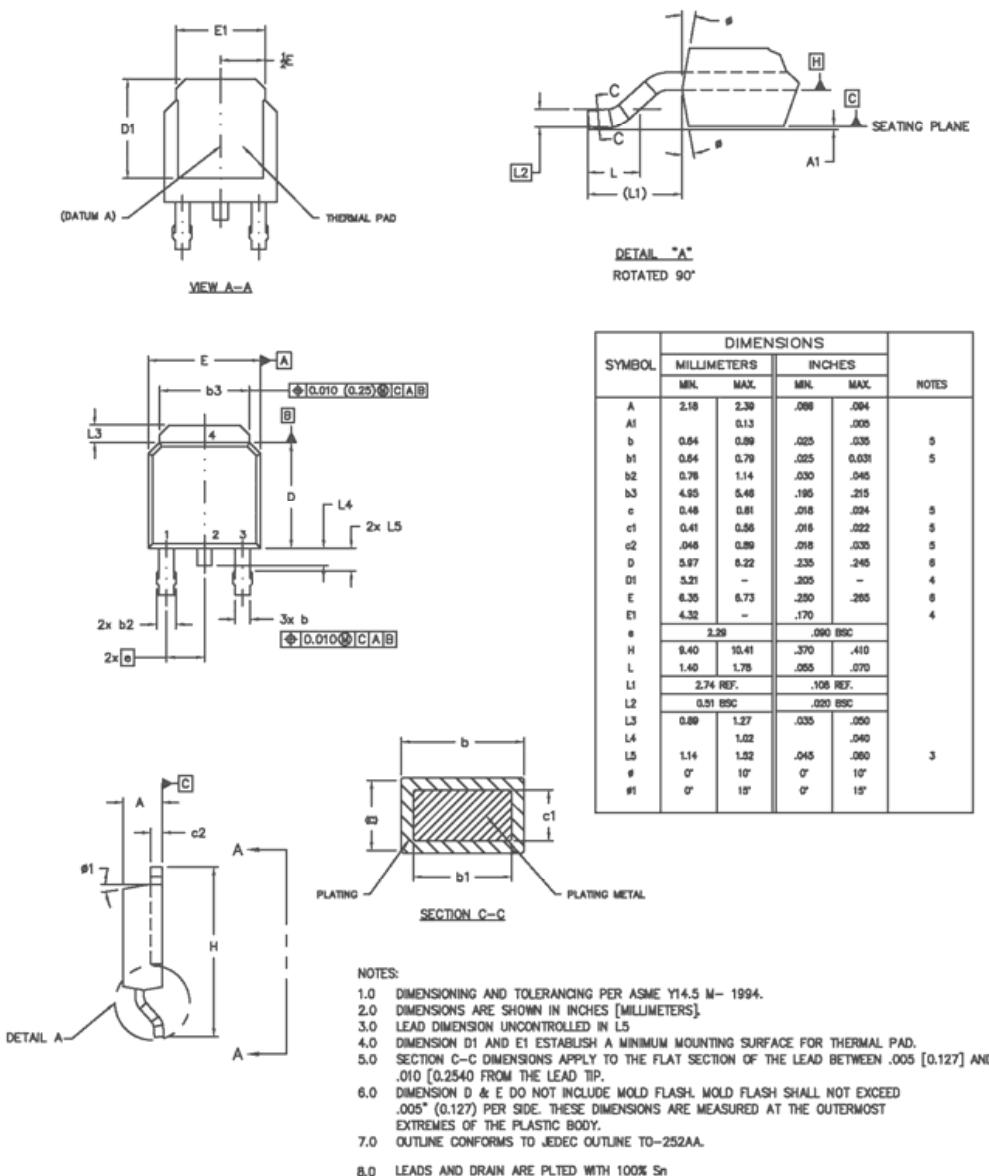
Notes:

1. 10 sprocket hole pitch cumulative tolerance 0.2
2. Camber not to exceed 1mm in 100mm
3. Material: Black Conductive Advantek Polystyrene
4. Ao and Bo measured on a plane 0.3mm above the bottom of the pocket
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

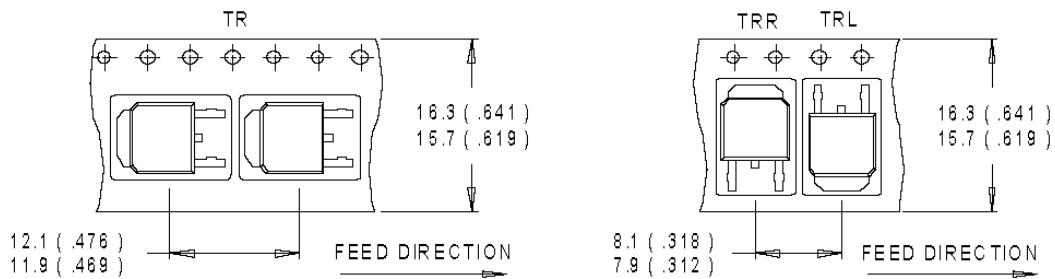
$Ao = 6.4 \text{ mm}$
 $Bo = 5.2 \text{ mm}$
 $Ko = 2.1 \text{ mm}$

- All Dimensions in Millimeters -

Case Outline – D-Pak - Automotive Q100 PbF MSL1 qualified

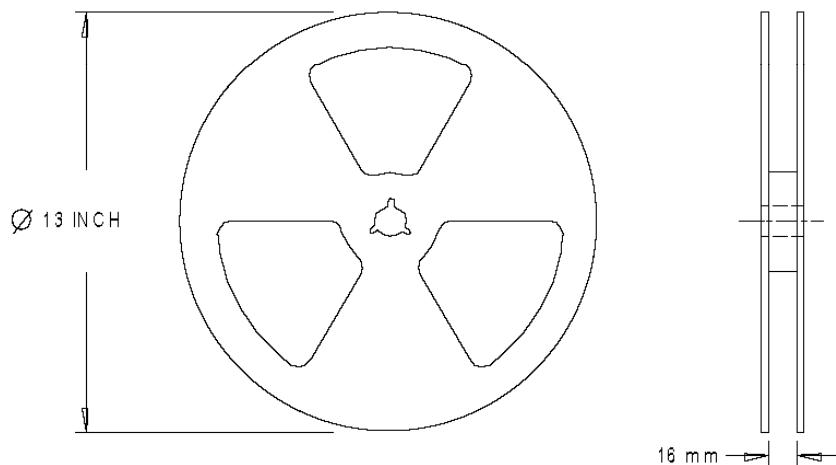


Tape & Reel - D-Pak



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.

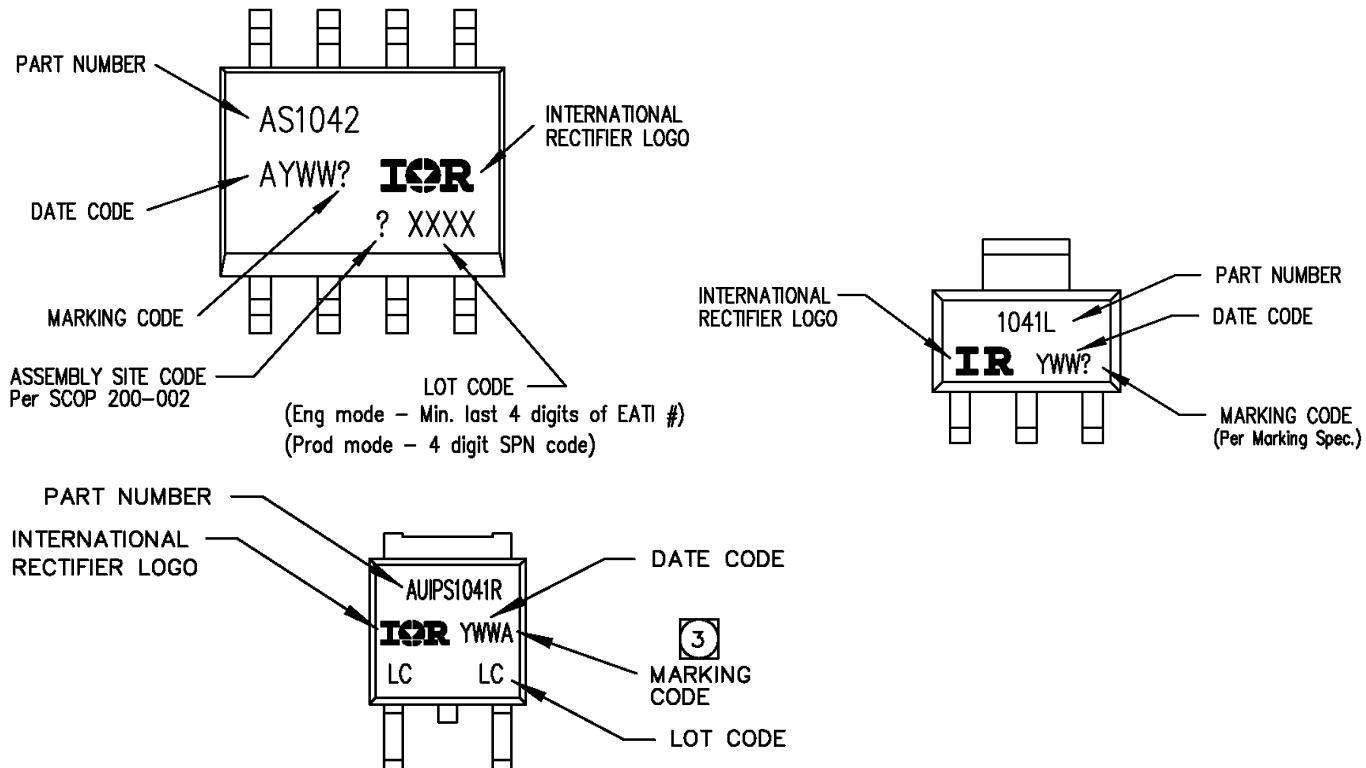


NOTES :

1. OUTLINE CONFORMS TO EIA-481.

Dimensions are shown in millimeters (inches)

Part Marking Information



Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS1042	SOIC-8	Tube	95	AUIPS1042G
		Tape and reel	2500	AUIPS1042GTR
AUIPS1041	SOT-223	Tube	95	AUIPS1041L
		Tape and reel	2500	AUIPS1041LTR
AUIPS1041	D-Pak-5-Lead	Tube	75	AUIPS1041R
		Tape and reel	2000	AUIPS1041RTR
		Tape and reel left	3000	AUIPS1041RTRL
		Tape and reel right	3000	AUIPS1041RTRR

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For technical support, please contact IR's Technical Assistance Center
<http://www.irf.com/technical-info/>

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Tel: (310) 252-7105

Revision History

Revision	Date	Notes/Changes
C1	November, 24 th , 2010	AU release
C2	December, 7 th 2010	Remove ESD section page 3
C3	December, 9 th 2010	Update qual page
C4	December, 14 th 2010	Update Tdon
D	February, 28 th 2011	Update Max rating