# FAIRCHILD January 2008 SEMICONDUCTOR FGPF50N30T **300V, 50A PDP IGBT General Description** Features • High current capability Using Novel Trench IGBT Technology, Fairchild's new sesries of trench IGBTs offer the optimum performance for PDP applica-+ Low saturation voltage: V\_{CE(sat)} =1.4V @ I\_C = 30A tions where low conduction and switching losses are essential. • High input impedance · Fast switching · RoHS compliant Applications PDP System TO-220F 1.Gate 2.Collector 3.Emitter

## **Absolute Maximum Ratings**

Symbol	Description		Ratings	Units
V <sub>CES</sub>	Collector to Emitter Voltage		300	V
V <sub>GES</sub>	Gate to Emitter Voltage		± 30	V
I <sub>CM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	120	А
P <sub>D</sub> Maximum Power Dissipation Maximum Power Dissipation	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	46.8	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100 <sup>o</sup> C	18.7	W
TJ	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case	-	2.67	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	-	62.5	°C/W

Notes:

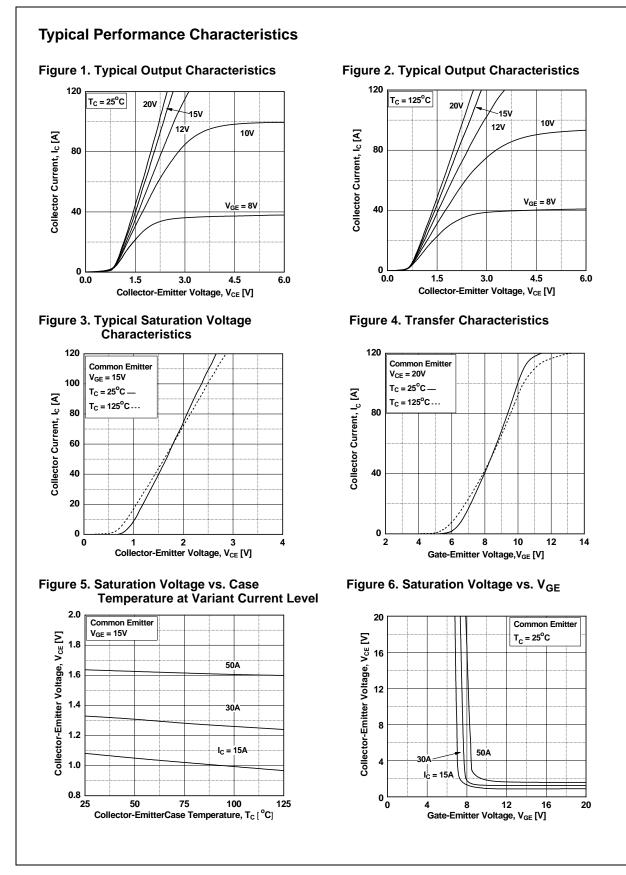
1: Repetitive test , Pulse width=100usec , Duty=0.1

\* I<sub>C</sub>\_pluse limited by max Tj

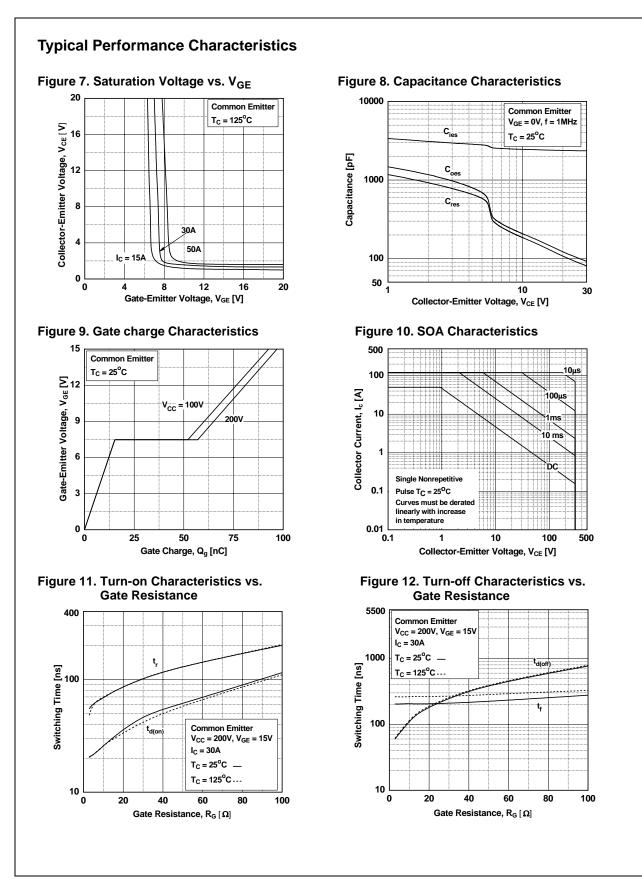
1

		Pack	Ackage Packaging Type O-220F Rail / Tube		Qty pe	Qty per Tube		Max Qty per Box	
		TO-22			50ea		-		
Electric	al Cha	racteristics of th	e IGB	$\mathbf{T}_{T_{c}=25}$	℃ unless otherwise noted				
Symbol		Parameter			Conditions	Min.	Тур.	Max.	Units
						1			
Off Charac				0) ( )	050 4	000			N
BV <sub>CES</sub>		to Emitter Breakdown Vol		<sub>E</sub> = 0V, I <sub>C</sub>	= 250μA	300	-	-	V
$\Delta BV_{CES}$ $\Delta T_{.1}$	Temperat Voltage	ture Coefficient of Breakdo	V <sub>G</sub>	$_{E}$ = 0V, $I_{C}$	= 250µA	-	0.3	-	V/ºC
I <sub>CES</sub>		Cut-Off Current	Vc	<sub>E</sub> = V <sub>CES</sub> ,	V <sub>GE</sub> = 0V	-	-	250	μA
I <sub>GES</sub>	G-E Leak	age Current		$V_{CE} = V_{CES}, V_{CE} = 0V$ $V_{GE} = V_{GES}, V_{CE} = 0V$		-	-	±400	nA
520	+	-	0	_ 010,			1	I	ļ
On Charac	teristics		·						I.
V <sub>GE(th)</sub>	G-E Threshold Voltage		I <sub>C</sub> =	$I_C = 250 \mu A, V_{CE} = V_{GE}$		3.0	4.5	5.5	V
V <sub>CE(sat)</sub> Collect		llector to Emitter Saturation Voltage		I <sub>C</sub> = 15A, V <sub>GE</sub> = 15V		-	1.1	1.5	V
	Collector			I <sub>C</sub> = 30A, V <sub>GE</sub> = 15V		-	1.4	-	V
				$I_{C} = 50A, V_{GE} = 15V,$ $T_{C} = 25^{\circ}C$		-	1.65	-	V
				= 50A, V <sub>GE</sub> = 125ºC	<u>=</u> = 15V,	-	1.60	-	V
Dynamic C	haracteris	stics							
C <sub>ies</sub>	Input Cap					-	2320	-	pF
C <sub>oes</sub>		apacitance		$V_{CE} = 30V$ , $V_{GE} = 0V$ , f = 1MHz		-	92	-	pF
C <sub>res</sub>	•	Transfer Capacitance	f =			-	80	-	pF
- 165									
Switching	Character	istics							
t <sub>d(on)</sub>	Turn-On	Delay Time		V <sub>CC</sub> = 200V, I <sub>C</sub> = 30A,		-	31	-	ns
t <sub>r</sub>	Rise Time	e	– Ro	<sub>C</sub> = 200V, = 15Ω, V	I <sub>C</sub> = 30A, ∽⊑ = 15V.	-	78	-	ns
t <sub>d(off)</sub>	Turn-Off	Delay Time	Re	$R_{G} = 15\Omega, V_{GE} = 15V,$ Resistive Load, $T_{C} = 25^{\circ}C$		-	156	-	ns
t <sub>f</sub>	Fall Time					-	200	300	ns
t <sub>d(on)</sub>	Turn-On	Delay Time				-	30	-	ns
t <sub>r</sub>	Rise Time	e	V <sub>C</sub> Ro	<sub>C</sub> = 200V, = 15Ω. V	I <sub>C</sub> = 30A, ₂₅ = 15V.	-	78	-	ns
t <sub>d(off)</sub>	Turn-Off	Delay Time		$R_G = 15\Omega$ , $V_{GE} = 15V$ , Resistive Load, $T_C = 125^{\circ}C$		-	163	-	ns
t <sub>f</sub>	Fall Time					-	260	-	ns
Qg	Total Gat	e Charge				-	97	-	nC
Q <sub>ge</sub>	Gate to E	mitter Charge		<sub>E</sub> = 200V,	I <sub>C</sub> = 30A,	-	15	-	nC
Q <sub>gc</sub>	Gate to C	Collector Charge	۲G	– V <sub>GE</sub> = 15V		-	41	-	nC

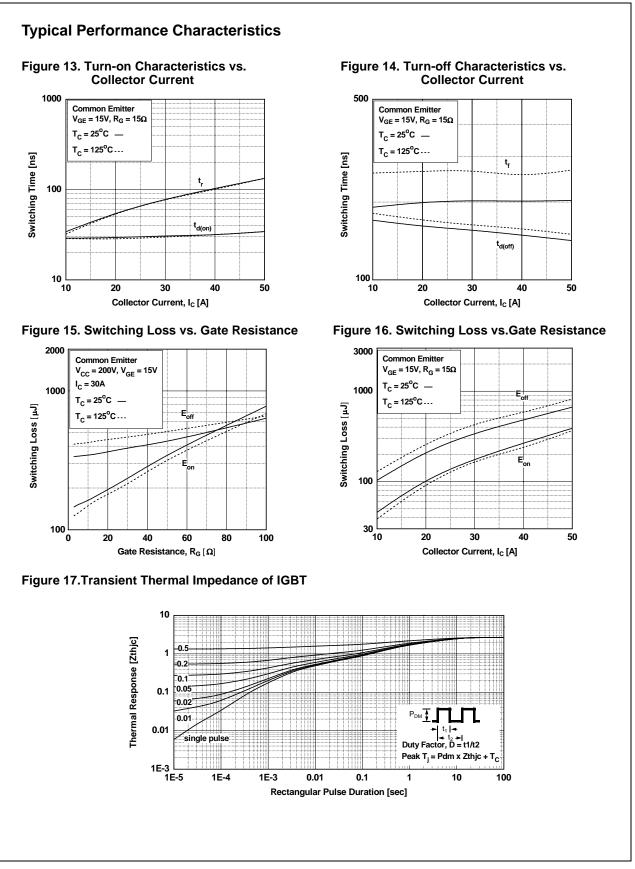
FGPF50N30T Rev. A



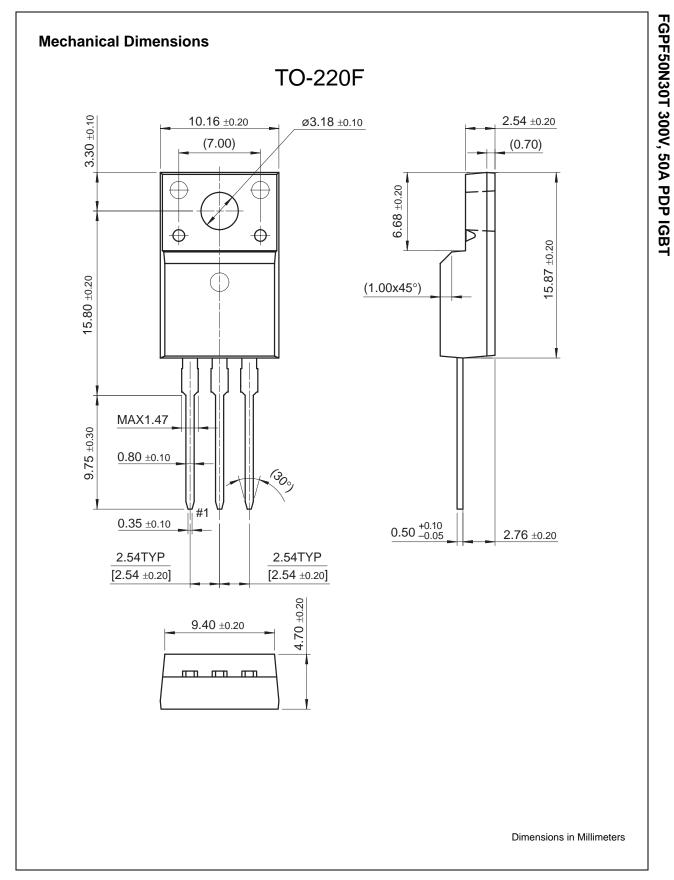
FGPF50N30T Rev. A



FGPF50N30T Rev. A



FGPF50N30T Rev. A



FGPF50N30T Rev. A



SEMICONDUCTOR

### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidianries, and is not intended to be an exhaustive list of all such trademarks.

ACEx <sup>®</sup> Build it Now <sup>TM</sup> CorePLUS <sup>TM</sup> <i>CROSSVOLT</i> <sup>TM</sup> CTL <sup>TM</sup> Current Transfer Logic <sup>TM</sup> EcoSPARK <sup>®</sup> EZSWITCH <sup>TM</sup> * Fairchild <sup>®</sup> Fairchild <sup>®</sup> Fairchild <sup>®</sup> Fairchild Semiconductor <sup>®</sup> FACT Quiet Series <sup>TM</sup> FACT <sup>®</sup> FAST <sup>®</sup> FastvCore <sup>TM</sup> FlashWriter <sup>®</sup> *	FPS™ FRFET <sup>®</sup> Global Power Resource <sup>SM</sup> Green FPS™ GTO™ <i>i-Lo</i> ™ IntelliMAX™ ISOPLANAR™ MegaBuck™ MICROCOUPLER™ MicroFET™ MicroFET™ MicroPak™ MillerDrive™ Motion-SPM™ OPTOLOGIC <sup>®</sup> OPTOPLANAR <sup>®</sup>	PDP-SPM <sup>™</sup> Power220 <sup>®</sup> POWEREDGE <sup>®</sup> Power-SPM <sup>™</sup> PowerTrench <sup>®</sup> Programmable Active Droop <sup>™</sup> QFET <sup>®</sup> QS <sup>™</sup> QT Optoelectronics <sup>™</sup> Quiet Series <sup>™</sup> RapidConfigure <sup>™</sup> SMART START <sup>™</sup> SMART START <sup>™</sup> SPM <sup>®</sup> STEALTH <sup>™</sup> SuperFET <sup>™</sup> SuperSOT <sup>™</sup> -3 SuperSOT <sup>™</sup> -6 SuperSOT <sup>™</sup> -8	SupreMOS <sup>™</sup> SyncFET <sup>™</sup> © SYSTEM <sup>®</sup> The Power Franchise <sup>®</sup> Definition TinyBoost <sup>™</sup> TinyBoost <sup>™</sup> TinyDerot <sup>™</sup> TinyOPTO <sup>™</sup> TinyOPTO <sup>™</sup> TinyPWM <sup>™</sup> TinyWire <sup>™</sup> µSerDes <sup>™</sup> UHC <sup>®</sup> Ultra FRFET <sup>™</sup> VCX <sup>™</sup>
---	--	---	---

\* EZSWITCH™ and FlashWriter<sup>®</sup> are trademarks of System General Corporation, used under license by Fairchild Semiconductor.

### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN: NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component in any component of a life support, device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be pub- lished at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserve the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontin- ued by Fairchild Semiconductor. The datasheet is printed for reference infor mation only.

FGPF50N30T Rev. A