

March 2013

# FGL35N120FTD 1200 V, 35 A Field Stop Trench IGBT

#### **Features**

- · Field Stop Trench Technology
- · High Speed Switching
- Low Saturation Voltage:  $V_{CE(sat)}$  = 1.68 V @  $I_C$  = 35 A
- · High Input Impedance

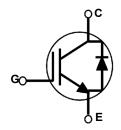
#### **Applications**

• Solar Inverter, UPS, Welder, PFC

#### **General Description**

Using advanced field stop trench IGBT technology, Fairchild®'s 1200V trench IGBTs offer the optimum performance for hard switching application such as solar inverter, UPS, welder applications.





# **Absolute Maximum Ratings**

Symbol	Description		Ratings	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		1200	V
V <sub>GES</sub>	Gate to Emitter Voltage		± 25	V
	Collector Current	@ T <sub>C</sub> = 25°C	70	Α
l <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 100°C	35	Α
I <sub>CM (1)</sub>	Pulsed Collector Current @ T <sub>C</sub> = 25°C		105	Α
IF	Diode Continuous Forward Current	@ T <sub>C</sub> = 100°C	40	Α
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	368	W
· D	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	147	W
T <sub>J</sub>	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

#### **Thermal Characteristics**

Symbol	Parameter	Ratings	Unit	
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	0.34	°C/W	
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	0.9	°C/W	

Notes:
1: Repetitive rating: Pulse width limited by max. junction temperature

$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	25	°C/W

# **Package Marking and Ordering Information**

Device Marking Device		Package Reel Size		Tape Width	Quantity	
FGL35N120FTD	FGL35N120FTDTU	TO-264	-	-	30	

# Electrical Characteristics of the IGBT $T_C = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 μA	1200	-	-	٧
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	-	-	1	mA
I <sub>GES</sub>	G-E Leakage Current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0 V	-	-	±250	nA
On Charac	teristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 35 mA, V <sub>CE</sub> = V <sub>GE</sub>	3.5	6.2	7.5	V
()		I <sub>C</sub> = 35 A, V <sub>GE</sub> = 15 V	-	1.68	2.2	٧
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 35 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 125°C	-	2.0	-	V
Dynamic C	haracteristics					
C <sub>ies</sub>	Input Capacitance		_	5090	_	pF
C <sub>oes</sub>	Output Capacitance	V <sub>CE</sub> = 30 V <sub>,</sub> V <sub>GE</sub> = 0 V, f = 1MHz	-	180	-	pF
C <sub>res</sub>	Reverse Transfer Capacitance	1 = 1WIDZ	-	95	-	pF
	Characteristics		_	34	_	no
t <sub>d(on)</sub>	Turn-On Delay Time Rise Time		-	63	-	ns
t <sub>r</sub>	Turn-Off Delay Time		-	172	-	ns
t <sub>d(off)</sub>	Fall Time	$V_{CC}$ = 600 V, $I_{C}$ = 35 A, $R_{G}$ = 10 $\Omega$ , $V_{GE}$ = 15 V, Inductive Load, $T_{C}$ = 25°C	<u> </u>	107	_	ns
E <sub>on</sub>	Turn-On Switching Loss		_	2.5	_	mJ
E <sub>off</sub>	Turn-Off Switching Loss		_	1.7	_	mJ
E <sub>ts</sub>	Total Switching Loss		_	4.2	_	mJ
t <sub>d(on)</sub>	Turn-On Delay Time		-	33	-	ns
t <sub>r</sub>	Rise Time		-	66	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 600 V, I <sub>C</sub> = 35 A,	-	180	-	ns
t <sub>f</sub>	Fall Time	$R_G = 10 \Omega, V_{GE} = 15 V,$	-	146	-	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 125°C	-	3.1	-	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	2.1	-	mJ
E <sub>ts</sub>	Total Switching Loss		-	5.2	-	mJ
Qg	Total Gate Charge		-	210	-	nC
Q <sub>ge</sub>	Gate to Emitter Charge	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 35 A, V <sub>GE</sub> = 15 V	-	42	-	nC
Q <sub>gc</sub>	Gate to Collector Charge	1 VGE - 13 V	-	101	-	nC

# Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Unit
V <sub>FM</sub>	V <sub>EM</sub> Diode Forward Voltage	I <sub>E</sub> = 35 A	T <sub>C</sub> = 25°C	-	2.7	3.4	V
Prode Forward Voltage	Blodd Forward Vollage		T <sub>C</sub> = 125°C	-	2.5	-	
t <sub>rr</sub>	Diode Reverse Recovery Time		T <sub>C</sub> = 25°C	-	337	-	ns
The Disagn Never Services reserving		T <sub>C</sub> = 125°C	-	520	-		
I	I <sub>rr</sub> Diode Peak Reverse Recovery Current	I <sub>F</sub> = 35 A,	T <sub>C</sub> = 25°C	-	7.6	-	Α
·m		di/dt = 200 A/μs	T <sub>C</sub> = 125°C	-	12.9	-	
O	Q <sub>rr</sub> Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	1292	-	nC
-II			T <sub>C</sub> = 125°C	-	3377	-	

**Figure 1. Typical Output Characteristics** 

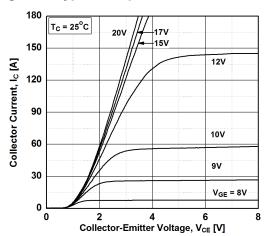


Figure 3. Typical Saturation Voltage Characteristics

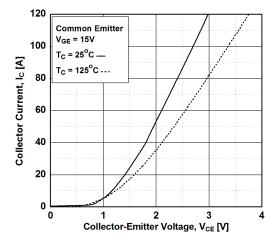
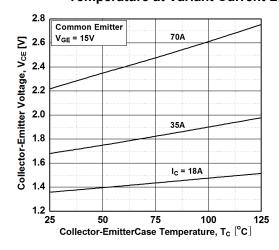
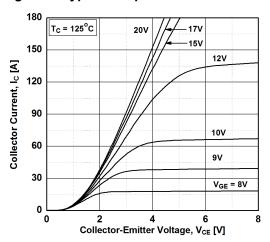


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level



**Figure 2. Typical Output Characteristics** 



**Figure 4. Transfer Characteristics** 

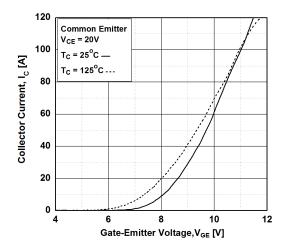


Figure 6. Saturation Voltage vs.  $V_{GE}$ 

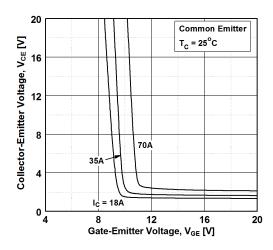


Figure 7. Saturation Voltage vs. V<sub>GE</sub>

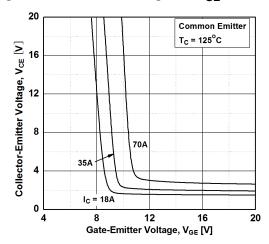


Figure 9. Capacitance Characteristics

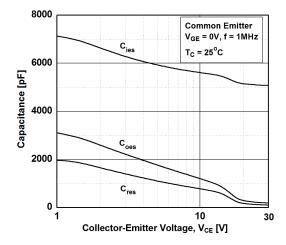


Figure 11. SOA Characteristics

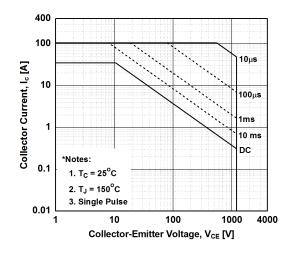


Figure 8. Load Current vs. Frequency

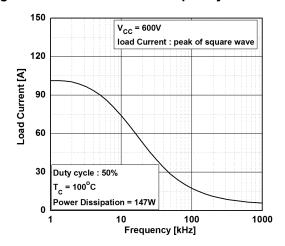


Figure 10. Gate Charge Characteristics

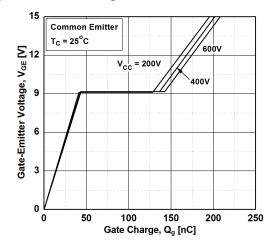


Figure 12. Turn-on Characteristics vs.
Gate Resistance

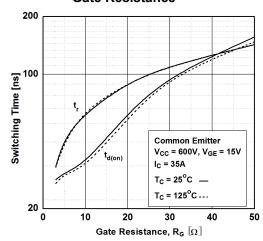


Figure 13. Turn-off Characteristics vs.
Gate Resistance

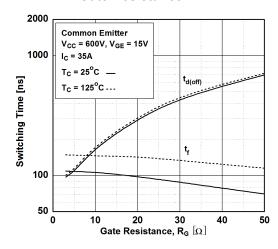


Figure 15. Turn-off Characteristics vs. Collector Current

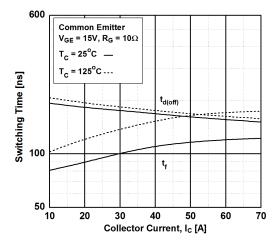


Figure 17. Switching Loss vs. Collector Current

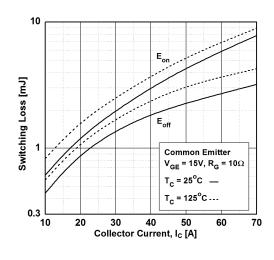


Figure 14. Turn-on Characteristics vs.
Collector Current

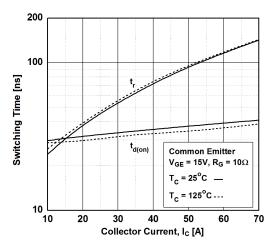


Figure 16.Switching Loss vs. Gate Resistance

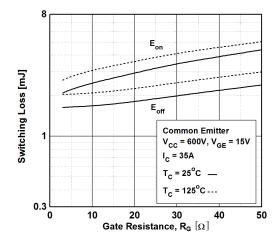


Figure 18. Turn off Switing SOA Characteristics

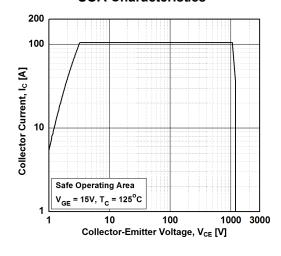


Figure 19. Forward Characteristics

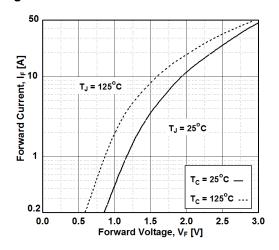


Figure 20. Reverse Recovery Current

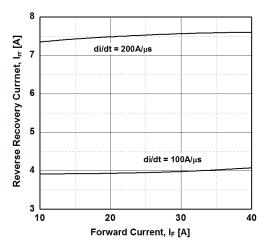


Figure 21. Stored Charge

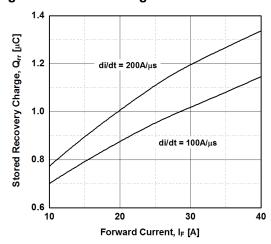


Figure 22. Reverse Recovery Time

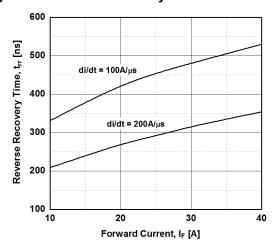
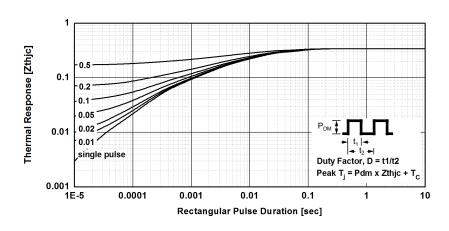


Figure 23. Transient Thermal Impedance of IGBT



#### **Mechanical Dimensions** TO-264 18.30 5.20 4.80 20.20 19.80 17.70 Α В 16.60 Ø<sup>7.40</sup> 7.00 (1.00)Ø 3.50 (2.00) 12.00) <u>€</u> 6.20 5.80 R2.00 3.10 0.254 A B 1.20 0.80 9.10 8.90 ( 0.50 21.62 21.02 19.80 R1.00 1.70 1.30 -( 1.50 ) 2.60 2.40 (4.05) -3.20 2.80 1.50 3.10 2.50 (1.50) <u>∕</u>c 2.70 2x 20.50 ⊕ 0.254 M A B 0.85 0.50 5.75 5.15 5.15 **FRONT VIEW** SIDE VIEW **BACK VIEW** NOTES: A. PACKAGE REFERENCE: JEDEC TO264 VARIATION AA. B. ALL DIMENSIONS ARE IN MILLIMETERS. 5.20 3.70 (0.15)4.80 3.30 OUT OF JEDEC STANDARD VALUE. D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994. E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS. F. THIS PACKAGE IS INTENDED ONLY FOR ш "FS PKG CODE AR" G. DRAWING FILE NAME: TO264A03REV1 **BOTTOM VIEW**

\* Front/Back Side Isolation Voltage: AC 2700V





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Datasheet Identification Product Status		Definition		
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary First Production		Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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