

## FEP16AT - FEP16JT

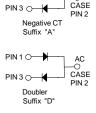
# PIN 1 O CASE PIN 2 Positive CT

PIN 1 O

### **Features**

- Low forward voltage drop.
- High surge current capacity.
- High current capability.
- High reliability.





## **Fast Rectifiers (Glass Passivated)**

Absolute Maximum Ratings\* T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value							Units	
		16AT	16BT	16CT	16DT	16FT	16GT	16HT	16JT	1
$V_{RRM}$	Maximum Repetitive Reverse Voltage		100	150	200	300	400	500	600	V
I <sub>F(AV)</sub>	Average Rectified Forward Current, .375 " lead length @ T <sub>A</sub> = 100°C		16							Α
I <sub>FSM</sub>	Non-repetitive Peak Forward Surge Current 200 8.3 ms Single Half-Sine-Wave			А						
T <sub>sta</sub>	Storage Temperature Range		-55 to +150							
TJ	Operating Junction Temperature	Temperature -55 to +150			°C					

 $<sup>{}^{\</sup>textstyle \star} \text{These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.}$ 

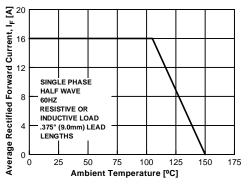
## **Thermal Characteristics**

Symbol	Parameter	Value	Units
P <sub>D</sub>	Power Dissipation	8.33	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	15	°C/W
$R_{\theta JL}$	Thermal Resistance, Junction to Lead	2.2	°C/W

## Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Device							Units	
		16AT	16BT	16CT	16DT	16FT	16GT	16HT	16JT	
V <sub>F</sub>	Forward Voltage @ 8.0A	0.95		1.3		1.5		V		
t <sub>rr</sub>	Reverse Recovery Time $I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{RR} = 0.25 \text{ A}$	35		50				ns		
I <sub>R</sub>	Reverse Current @ rated $V_R$ $T_A = 25^{\circ}C$ $T_A = 100^{\circ}C$	10 500			μΑ μΑ					
Ст	Total Capacitance V <sub>R</sub> = 4.0. f = 1.0 MHz	85 60		0	pF					

## **Typical Characteristics**



**Figure 1. Forward Current Derating Curve** 

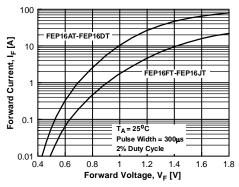


Figure 3. Forward Voltage Characteristics

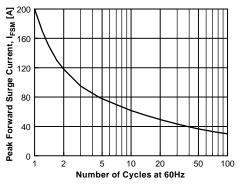


Figure 2. Non-Repetitive Surge Current Reverse Characteristics

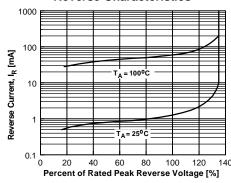


Figure 4. Reverse Current vs Reverse Voltage

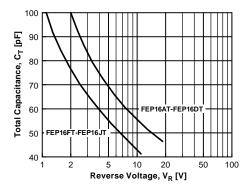
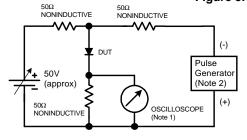
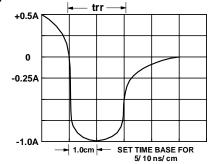


Figure 5. Total Capacitance





**Reverse Recovery Time Characterstic and Test Circuit Diagram** 

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