

# **Power line chokes**

Current-compensated ring core double chokes 250 V AC, 0.3 ... 2 A, 1.1 ... 22 mH, +40 °C

Series/Type: B82720A/K

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Power line chokes B82720A/K

## **Current-compensated ring core double chokes**

Rated voltage 250 V AC
Rated current 0.3 ... 2 A
Rated inductance 1.1 ... 22 mH

#### Construction

- Current-compensated ring core double choke
- Ferrite core with epoxy coating (UL 94 V-0)
- Polycarbonate case (UL 94 V-0)
- Polyurethane potting (UL 94 V-0)
- Sector winding

#### **Features**

- High resonance frequency due to special winding technique
- Approx. 0.7% stray inductance for symmetrical interference suppression
- Suitable for wave soldering
- Design complies with UL 1283 and EN 60938-2 (VDE 0565-2)
- RoHS-compatible

# **Applications**

- Suppression of common-mode interferences
- Compact electronic ballasts in lamps
- Compact switch-mode power applications

#### **Terminals**

- Base material CuNi18Zn20
- Layer composition Ni, Sn
- Hot-dipped
- Pins  $0.5 \times 0.5$  (mm),  $\varnothing 0.6$  mm
- Pins in the lead spacing  $10 \times 7.5$  (mm) or  $10 \times 10$  (mm)

#### Marking

Manufacturer, ordering code, rated inductance, rated current, graphic symbol, rated voltage, date of manufacture (YYWWD)

#### **Delivery mode**

■ Cardboard box



B82720A



B82720K

<sup>1)</sup> UL approval with 300 V AC

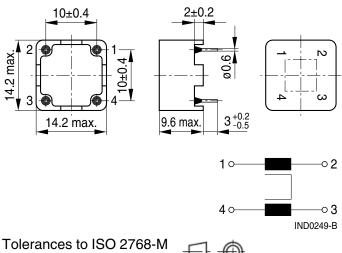


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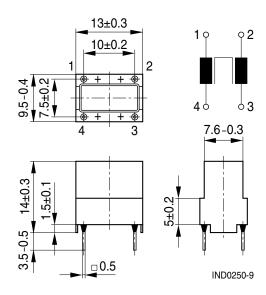
# **Current-compensated ring core double chokes**

# Dimensional drawings and pin configurations

Horizontal version (B82720A)



Vertical version (B82720K)



Tolerances to ISO 2768-W unless otherwise noted. Dimensions in mm.



Rated voltage V <sub>R</sub>	250 V AC (50/60 Hz)		
Test voltage V <sub>test</sub>	1500 V AC, 2 s (line/line)		
Rated temperature T <sub>R</sub>	+40 °C		
Rated current I <sub>R</sub>	Referred to 50 Hz and rated temperature		
Rated inductance L <sub>R</sub>	Measured with Agilent 4284A at 10 kHz, 0.1 mA, +20 °C, inductance is specified per winding.		
Inductance tolerance	−30/+50% at +20 °C		
Inductance decrease $\Delta L/L_0$	<10% at DC magnetic bias with I <sub>R</sub> , +20 °C		
Stray inductance L <sub>stray,typ</sub>	Measured with Agilent 4284A at 10 kHz, 5 mA, +20 °C, typical values		
DC resistance R <sub>typ</sub>	Measured at +20 °C, typical values, specified per winding		
Solderability (lead-free)	Sn96.5Ag3.0Cu0.5: $+(245 \pm 5)$ °C, $(3 \pm 0.3)$ s Wetting of soldering area $\geq 95\%$ (to IEC 60068-2-20, test Ta)		
Resistance to soldering heat (wave soldering)	+(260 ±5) °C, (10 ±1) s (to IEC 60068-2-20, test Tb)		
Climatic category	40/125/56 (to IEC 60068-1)		
Storage conditions (packaged)	–25 °C +40 °C, ≤ 75% RH		
Weight	Approx. 2.5 g		
Approvals	UL 1283, EN 60938-2		
	<del>-</del>		



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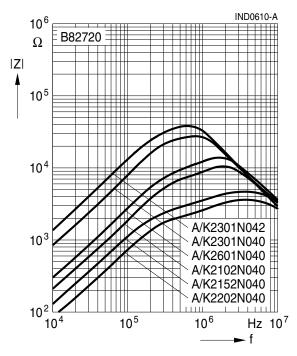
## **Characteristics and ordering codes**

$I_R$	L <sub>R</sub>	L <sub>stray,typ</sub>	R <sub>typ</sub>	Ordering code		Approvals	
Α	mH	μΗ	mΩ	Horizontal version	Vertical version	<b>₽</b>	<b>7.1</b>
0.3	22	130	1500	B82720A2301N042	B82720K2301N042	×	×
0.3	12	80	1100	B82720A2301N040	B82720K2301N040	×	×
0.6	4.4	30	400	B82720A2601N040	B82720K2601N040	×	×
1.0	3.0	20	220	B82720A2102N040	B82720K2102N040	×	×
1.5	1.6	10	110	B82720A2152N040	B82720K2152N040	×	×
2.0	1.1	6	65	B82720A2202N040	B82720K2202N040	×	×

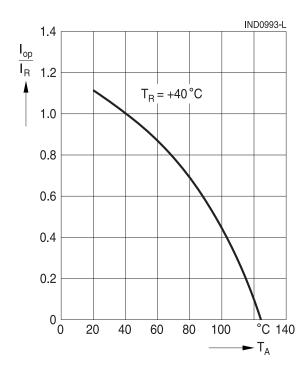
 $<sup>\</sup>times$  = approval granted

# Impedance |Z| versus frequency f

measured with windings in parallel at +20  $^{\circ}\text{C},$  typical values



# Current derating $I_{op}/I_R$ versus ambient temperature $T_A$





# **Cautions and warnings**

# **Current-compensated ring core double chokes**

which might lead to reduced reliability or lifetime.

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there. Derating must be applied
    in case the ambient temperature in the application exceeds the rated temperature of the
    component.
  - Ensure the operation temperature (which is the sum of the ambient temperature and the temperature rise caused by losses / self-heating) of the component in the application does not exceed the maximum value specified in the climatic category.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.

  Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts,
- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.
- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.



#### Important notes

## **Current-compensated ring core double chokes**

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