

ISSUE NUMBER

Downlo

ELECTRICAL CIRCUIT:

0.000 0.00 0.0 TOLERANCE UNLESS OTHERWISE SPECIFIED IN MM: ± 0.50 ± 0.38 ± 0.25 ± 0.10 ANGLE

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THIS IS A C.A.D. GENERATED DRAWING DO NOT MAKE MANUAL REVISIONS TO MASTER.



SSUE NUMBER ORIGINAL

CONNECTOR SOLDER SIDE P6 **Q** P5 **Q** P3 **Q** P2 **Q** P4 **Q** P1 **P** 1CT : 1CT RECEIVE 1CT: 1CT TRANSMIT $\frac{2}{2}$ $\frac{2}{3}$ RJ45 CONTACT SIDE 96 ا ا **-**0 J2 <u>Մ</u>

> 1.TR:(100KHz,0.1V), TEST NOTES:(25±5°C)

PINS:(P3-P6):(J3-J6)=1:1±3% PINS:(P1-P2):(J1-J2)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias) PINS: (P1,P2),(P3,P6)=350uH MINIMUM

3 DCR

PINS:(J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM

PINS(P3,P6)TO(J3,J6)=1500VAC PINS(P1,P2)TO(J1,J2)=1500VAC

5.INSERTION LOSS:

-1.2dB MINIMUM AT 100KHz TO 80MHz;

6 RETURN LOSS:

-18dB MINIMUM AT 1MHz TO 30MHz;

-12dB MINIMUM AT 30MHz TO 80MHz

7 CROSS TALK

-35dB TYPICAL AT 1MHz TO 100MHz

8.COMMON TO COMMON MODE REJECTION: -30dB TYPICAL AT 30MHz TO 100MHz

9.LEAKAGE INDUCTANCE:

PINS(P3,P6) WITH (J3,J6) SHORT=0.35uH MAX @ 1MHz PINS(P1,P2) WITH (J1,J2) SHORT=0.35uH MAX @ 1MHz

10.INTERWINDING CAPACITANCE: PINS(P1,P2)TO(J1,J2)=35pF MAX @ 1MHz

PINS(P3,P6)TO(J3,J6)=35pF MAX @ 1MHz

SHIELDED/10/100 Mbps FILTER

RJ45 MAGNETIC JACK WITHOUT LED, 8P, 8C

YOUR CONNECTION TO QUALITY & SERVICE TORONTO, ONTARIO **EDAC INC**

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DRAWING NUMBER A40-108-260-906 SEE NOTE SHEET 2 OF 2 ISSUE