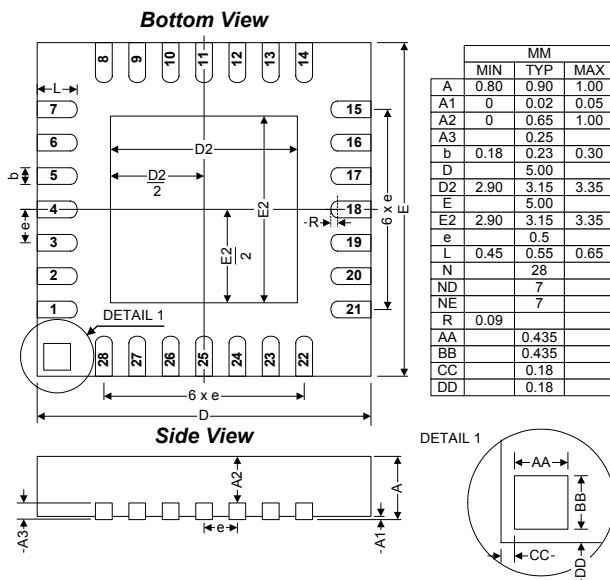


Selected Electrical Specifications

($T_A = -40$ to $+85$ °C, $V_{DD} = AV+ = 3.0$ V, $V_{REF} = 2.5$ V External, PGA Gain = 1x, MDCLK = 2.4567 MHz, Decimation Ratio = 1920 unless otherwise specified)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GLOBAL CHARACTERISTICS					
Supply Voltage		2.7		3.6	V
Supply Current (CPU active)	Clock = 50 MHz Clock = 1 MHz Clock = 32 kHz; V_{DD} Monitor Enabled		17 0.5 16		mA mA μ A
Supply Current (shutdown)	Oscillator not running; V_{DD} Monitor Disabled		0.1		μ A
Clock Frequency Range		DC		50	MHz
24-BIT A/D CONVERTER					
Resolution	(no missing codes)		24		bits
Integral Nonlinearity	Single-ended Mode Differential Mode			± 15	ppm FS
Offset Error			± 5		ppm
Gain Error			± 0.002		%
Common Mode Rejection Ratio (CMRR)			110		dB
Power Supply Rejection, DC		80			dB
Power Supply Current			230		μ A
8-BIT CURRENT-MODE D/A CONVERTERS					
Resolution			8		bits
Integral Nonlinearity			± 0.5		LSB
Differential Nonlinearity	Guaranteed Monotonic		± 0.5	± 1	LSB

Package Information



C8051F350DK Development Kit

