PD - 97623



AUTOMOTIVE GRADE

AUIRLR2905

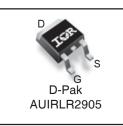
HEXFET[®] Power MOSFET

- Advanced Planar Technology
- Logic-Level Gate Drive
- Low On-Resistance
- Dynamic dV/dT Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- · Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified

Description

Specifically designed for Automotive applications, this cellular design of HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-resistance per silicon area. This benefit combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and a wide variety of other applications.

	V _{(BR)DSS}	55V	
	R _{DS(on)} max.	27m Ω	
s	I _D	42A	



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°	C Continuous Drain Current, V _{GS} @ 10V	42	
I _D @ T _C = 100	°C Continuous Drain Current, V _{GS} @ 10V	30	A
I _{DM}	Pulsed Drain Current ①	160	
P _D @T _C = 25°	C Power Dissipation	110	W
	Linear Derating Factor	0.71	W/°C
V _{GS}	Gate-to-Source Voltage	± 16	V
E _{AS}	Single Pulse Avalanche Energy (Thermally Limited)	210	mJ
I _{AR}	Avalanche Current 0	25	А
E _{AR}	Repetitive Avalanche Energy ①	11	mJ
dv/dt	Peak Diode Recovery ③	5.0	V/ns
ТJ	Operating Junction and	-55 to + 175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case ©		1.4	
R _{0JA}	Junction-to-Ambient (PCB mount) ^⑤		50	°C/W
$R_{\theta JA}$	Junction-to-Ambient		110	

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	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	55			V	$V_{GS} = 0V, I_{D} = 250\mu A$
$\Delta V_{(BR)DSS} / \Delta T_J$	Breakdown Voltage Temp. Coefficient	-	0.070		V/°C	Reference to 25°C, I _D = 1mA
				0.027		V _{GS} = 10V, I _D = 25A ④
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.030	Ω	V _{GS} = 5.0V, I _D = 25A ④
		_		0.040		V _{GS} = 4.0V, I _D = 21A ④
V _{GS(th)}	Gate Threshold Voltage	1.0		2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
gfs	Forward Transconductance	21			S	$V_{DS} = 25V, I_{D} = 25A$
I _{DSS}	Drain-to-Source Leakage Current	_		25	μA	$V_{DS} = 55V, V_{GS} = 0V$
				250		$V_{DS} = 44V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 16V
	Gate-to-Source Reverse Leakage			-100		V _{GS} = -16V

Static Electrical Characteristics @ $T_1 = 25^{\circ}C$ (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
Q _g	Total Gate Charge			48		I _D = 25A
Q _{gs}	Gate-to-Source Charge			8.6	nC	$V_{DS} = 44V$
Q_{gd}	Gate-to-Drain ("Miller") Charge			25	1	V _{GS} = 5.0V ④
t _{d(on)}	Turn-On Delay Time		11			$V_{DD} = 28V$
t _r	Rise Time		84		1	I _D = 25A
t _{d(off)}	Turn-Off Delay Time		26		ns	$R_{G} = 3.4\Omega$
t _f	Fall Time		15		1	$V_{GS} = 5.0V, R_D = 1.1\Omega$ (4)
L _D	Internal Drain Inductance		4.5			Between lead,
					nH	6mm (0.25in.)
L _S	Internal Source Inductance		7.5			from package
						and center of die contact
Ciss	Input Capacitance		1700			$V_{GS} = 0V$
Coss	Output Capacitance		400			$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		150		pF	f = 1.0MHz, See Fig. 5
Diode	Characteristics					
	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current		_	42		MOSFET symbol
	(Body Diode)				А	showing the
I _{SM}	Pulsed Source Current			160		integral reverse
	(Body Diode) ①					p-n junction diode.
V _{SD}	Diode Forward Voltage			1.3	V	T _J = 25°C, I _S = 25A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time		80	120	ns	T _J = 25°C, I _F = 25A
Q _{rr}	Reverse Recovery Charge		210	320	nC	di/dt = 100A/µs
t _{on}	Forward Turn-On Time	Intrinsio	Intrinsic turn-on time is negligible (turn-on is dominated by $L_{s}+L_{D}$)			

Notes:

- 0 Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11) $V_{DD} = 25V$, starting T_J = 25°C, L =470µH
- $R_G = 25\Omega$, $I_{AS} = 25A$. (See Figure 12)
- 3 I_{SD} \leq 25A, di/dt \leq 270A/µs, V_{DD} \leq V_{(BR)DSS}, $T_J \le 175^{\circ}C$
- ④ Pulse width \leq 300µs; duty cycle \leq 2%.

⑤ When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

 $\ensuremath{^{\circ}}\xspace \mathsf{R}_{ heta}$ is measured at Tj approximately 90°C.

Qualification Information[†]

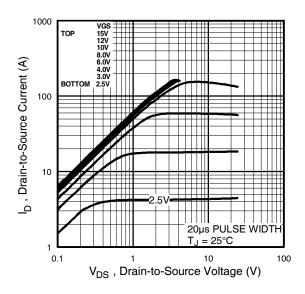
			Automotive			
		(per AEC-Q101) ^{††}				
Qualificat	ion Level	Comments: This part number(s) passed Automotive qualification. Industrial and Consumer qualification level is granted by extension of higher Automotive level.				
Moisture Sensitivity Level		D-PAK	MSL1			
	Machine Model	Class M4 (+/- 425V) ^{†††}				
		AEC-Q101-002				
	Human Body Model	Class H1B (+/- 1000V) ^{†††}				
ESD		AEC-Q101-001				
	Charged Device	Class C5 (+/- 1125V) ^{†††}				
	Model	AEC-Q101-005				
RoHS Compliant		Yes				

† Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

†† Exceptions to AEC-Q101 requirements are noted in the qualification report.

††† Highest passing voltage.

International **TOR** Rectifier





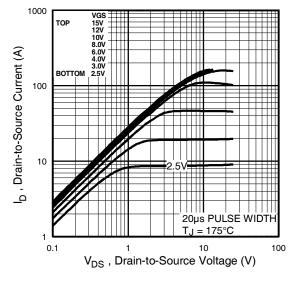


Fig 2. Typical Output Characteristics

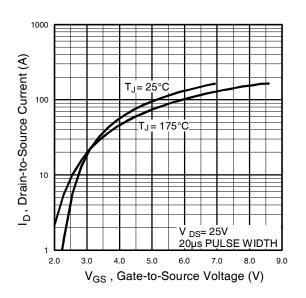


Fig 3. Typical Transfer Characteristics

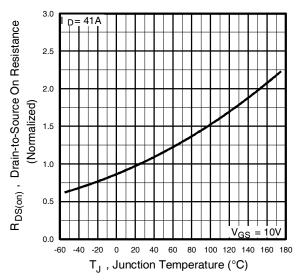


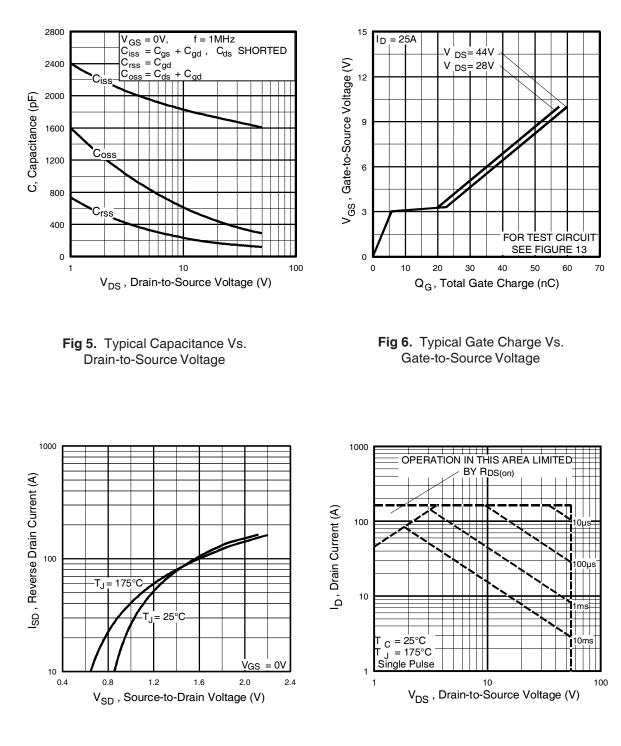
Fig 4. Normalized On-Resistance Vs. Temperature

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International **TOR** Rectifier

AUIRLR2905

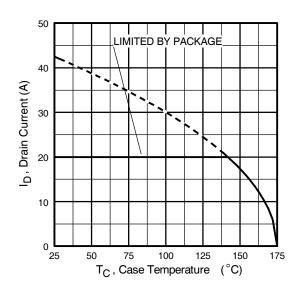


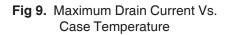


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Fig 8. Maximum Safe Operating Area

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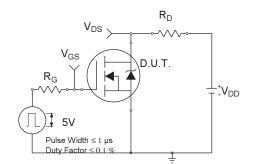


Fig 10a. Switching Time Test Circuit

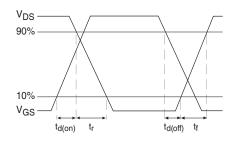


Fig 10b. Switching Time Waveforms

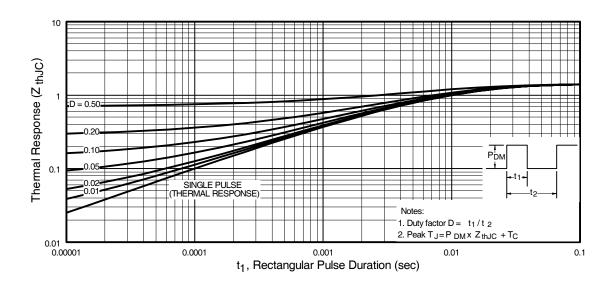


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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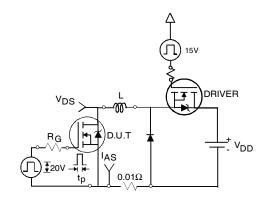


Fig 12a. Unclamped Inductive Test Circuit

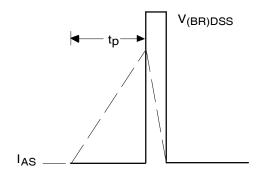


Fig 12b. Unclamped Inductive Waveforms

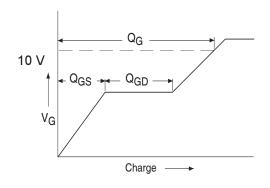


Fig 13a. Basic Gate Charge Waveform

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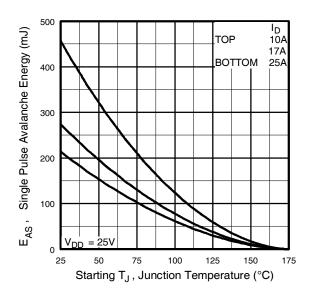


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

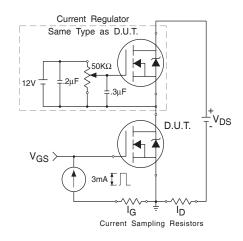
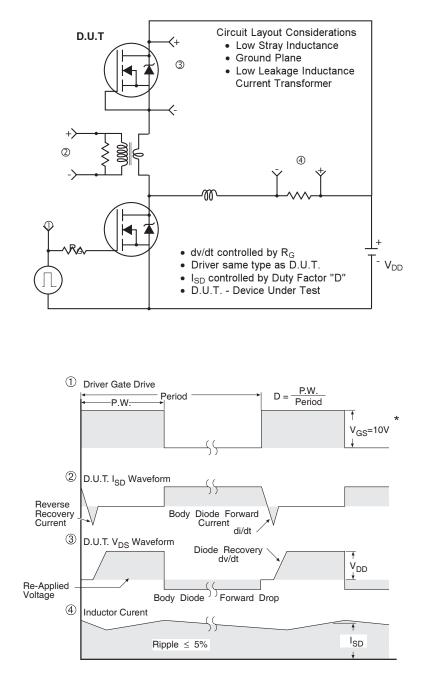


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



* V_{GS} = 5V for Logic Level Devices

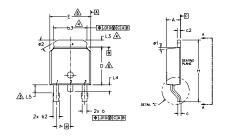
Fig 14. For N-Channel HEXFETS

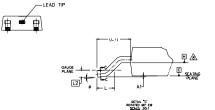
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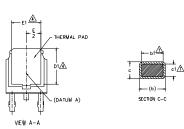
AUIRLR2905

D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)







NOTES

- 1,- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994 2.- DIMENSION ARE SHOWN IN INCHES [MILLIMETERS].
- A- LEAD DIMENSION UNCONTROLLED IN L5.
- Δ_{-} DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD. 5.- SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.
- DIMENSION D & E DO NOT INCLUDE WOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.

LEAD ASSIGNMENTS

HEXFET 1.- GATE 2.- DRAIN 3.- SOURCE 4.- DRAIN

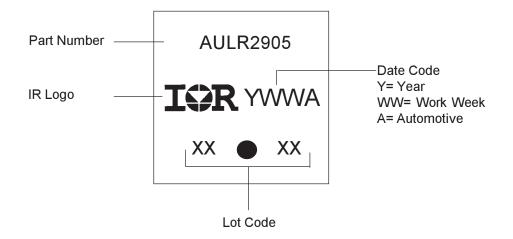
IGBT & CoPAK

1.- GATE 2.- COLLECTOR 3.- EMITTER 4.- COLLECTOR

- SDL. THESE DIMENSIONS ARE MEASURED AT THE OUTIM A- DIMENSION b1 & c1 APPLIED TO BASE WETAL ONLY. A- DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 9.- DUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.

S Y		N				
M B O	MILLIM	ETERS	INC	HES	0 T	
U L	MIN,	MAX.	MIN,	MAX.	Ė	
A	2.18	2.39	.086	.094		1
A1	-	0.13	-	.005		
ь	0.64	0.89	.025	.035		
ы	0.65	0.79	.025	.031	7	
b2	0.76	1.14	.030	.045		
b3	4,95	5,46	.195	.215	4	
с	0,46	0.61	.018	.024		
c1	0,41	0.56	.016	.022	7	
c2	0.46	0.89	.018	.035		
D	5,97	6.22	.235	.245	6	
D1	5.21	-	.205	-	4	
Е	6.35	6.73	.250	.265	6	
E1	4.32	-	.170	-	4	
е	2.29	BSC	.090	BSC		
н	9.40	10.41	.370	.410		
L	1.40	1.78	.055	.070		
L1	2.74	BSC	.108	.108 REF.		
L2	0.51	BSC	.020	.020 BSC		
L3	0,89	1.27	.035	.050	4	
L4	-	1.02	-	.040		
L5	1,14	1.52	.045	.060	3	
ø	0.	10"	0.	10*		
ø1	0"	15*	0*	15*		
ø2	25"	35*	25'	35*		

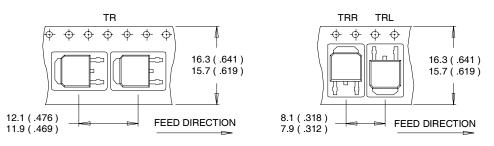
D-Pak Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/ www.irf.com

D-Pak (TO-252AA) Tape & Reel Information

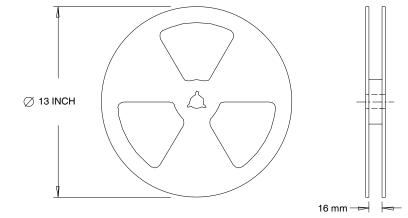
Dimensions are shown in millimeters (inches)



NOTES :

- 1. CONTROLLING DIMENSION : MILLIMETER.
- 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).

3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES : 1. OUTLINE CONFORMS TO EIA-481.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Ordering Information

Base part number	Package Type	Standard Pack	Standard Pack	
		Form	Quantity	
AUIRLR2905	Dpak	Tube	75	AUIRLR2905
		Tape and Reel	2000	AUIRLR2905TR
		Tape and Reel Left	3000	AUIRLR2905TRL
		Tape and Reel Right	3000	AUIRLR2905TRR



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