Microsemi
POWER PRODUCTS GROUP

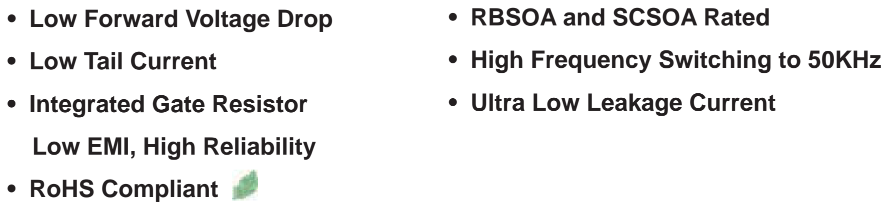
APT200GT60JRDQ4

600V, 200A, $V_{CE(ON)}$ = 2.0V Typical

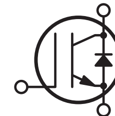
Thunderbolt IGBT®

The Thunderbolt IGBT® is a new generation of high voltage power IGBTs. Using Non-Punch-Through Technology, the Thunderbolt IGBT® offers superior ruggedness and ultrafast switching speed.

Features



- **Low Forward Voltage Drop**
- **Low Tail Current**
- **Integrated Gate Resistor**
  Low EMI, High Reliability
- **RoHS Compliant**
- **RBSOA and SCSOA Rated**
- **High Frequency Switching to 50KHz**
- **Ultra Low Leakage Current**



Maximum Ratings All Ratings: T_C = 25°C unless otherwise specified.

Symbol	Parameter	Ratings	Unit
V_{CES}	Collector-Emitter Voltage	600	Volts
V_{GE}	Gate-Emitter Voltage	±30	
I_{C1}	Continuous Collector Current @ T_C = 25°C	195	Amps
I_{C2}	Continuous Collector Current @ T_C = 100°C	100	
I_{CM}	Pulsed Collector Current ①	600	
SSOA	Switching Safe Operating Area @ T_J = 150°C	600A @ 600V	
P_D	Total Power Dissipation	595	Watts
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C

Static Electrical Characteristics

Symbol	Characteristic / Test Conditions	Min	Typ	Max	Unit
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage (V_{GE} = 0V, I_C = 1.0mA)	600	-	-	Volts
$V_{GE(TH)}$	Gate Threshold Voltage (V_{CE} = V_{GE}, I_C = 4.0mA, T_j = 25°C)	3	4	5	
$V_{CE(ON)}$	Collector Emitter On Voltage (V_{GE} = 15V, I_C = 200A, T_j = 25°C)	1.6	2.0	2.5	
	Collector Emitter On Voltage (V_{GE} = 15V, I_C = 200A, T_j = 125°C)	-	2.5	-	
I_{CES}	Collector Cut-off Current (V_{CE} = 600V, V_{GE} = 0V, T_j = 25°C) ②	-	-	50	μA
	Collector Cut-off Current (V_{CE} = 600V, V_{GE} = 0V, T_j = 125°C) ②	-	-	1500	
I_{GES}	Gate-Emitter Leakage Current (V_{GE} = ±30V)	-	-	600	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

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Dynamic Characteristic

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{ies}	Input Capacitance	V_{GE} = 0V, V_{CE} = 25V f = 1MHz	-	8650	-	pF
C_{oes}	Output Capacitance		-	546	-	
C_{res}	Reverse Transfer Capacitance		-	1180	-	
V_{GEP}	Gate-to-Emitter Plateau Voltage	Gate Charge V_{GE} = 15V V_{CE}= 300V I_C = 200A	-	7.5	-	V
Q_g	Total Gate Charge③		-	946	-	nC
Q_{ge}	Gate-Emitter Charge		-	58	-	
Q_{gc}	Gate-Collector Charge		-	430	-	
SSOA	Switching Safe Operating Area	T_J = 150°C, R_G = 2.2Ω , V_{GE} = 15V, L = 100μH, V_{CE}= 600V	600			A
$t_{d(on)}$	Turn-On Delay Time	Inductive Switching (25°C) V_{CC} = 400V V_{GE} = 15V I_C = 200A R_G = 2.2Ω T_J = +25°C	-	72	-	ns
t_r	Current Rise Time		-	160	-	
$t_{d(off)}$	Turn-Off Delay Time		-	952	-	
t_f	Current Fall Time		-	212	-	
E_{on1}	Turn-On Switching Energy④		-	-	-	μJ
E_{on2}	Turn-On Switching Energy⑤		-	9193	-	
E_{off}	Turn-Off Switching Energy⑥		-	19290	-	
$t_{d(on)}$	Turn-On Delay Time	Inductive Switching (125°C) V_{CC} = 400V V_{GE} = 15V I_C = 200A R_G = 2.2Ω T_J = +125°C	-	71	-	ns
t_r	Current Rise Time		-	157	-	
$t_{d(off)}$	Turn-Off Delay Time		-	1030	-	
t_f	Current Fall Time		-	202	-	
E_{on1}	Turn-On Switching Energy④		-	-	-	μJ
E_{on2}	Turn-On Switching Energy⑤		-	10460	-	
E_{off}	Turn-Off Switching Energy⑥		-	20210	-	

Thermal and Mechanical Characteristics

Symbol	Characteristic / Test Conditions	Min	Typ	Max	Unit
$R_{θJC}$	Junction to Case **(IGBT)**	-	-	0.21	°C/W
$R_{θJC}$	Junction to Case **(DIODE)**	-	-	0.48	
W_T	Package Weight	-	29.2	-	g
Torque	Terminals and Mounting Screws	-	-	10	in·lbf
		-	-	1.1	N·m
$V_{Isolation}$	RMS Voltage (50-60Hz Sinusoidal Waveform from Terminals to Mounting Base for 1 Min.)	2500	-	-	Volts

① Repetitive Rating: Pulse width limited by maximum junction temperature.

② For Combi devices, I_{ces} includes both IGBT and FRED leakages.

③ See MIL-STD-750 Method 3471.

④ E_{on1} is the clamped inductive turn-on energy of the IGBT only, without the effect of a commutating diode reverse recovery current adding to z a the IGBT turn-on loss. Tested in inductive switching test circuit shown in figure 21, but with a Silicon Carbide diode.

⑤ E_{on2} is the clamped inductive turn-on energy that includes a commutating diode reverse recovery current in the IGBT turn-on switching loss. (See Figures 21, 22.)

⑥ E_{off} is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1. (See Figures 21, 23.)

⑦ R_G is external gate resistance not including gate driver impedance.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

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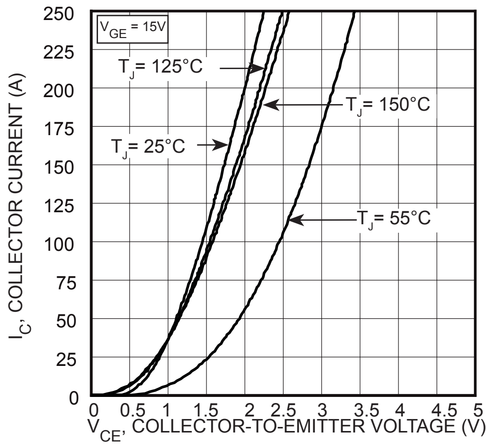

FIGURE 1, Output Characteristics ($T_J = 25°C$)

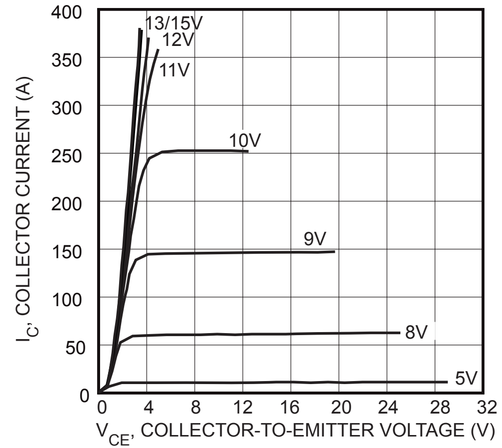

FIGURE 2, Output Characteristics ($T_J = 25°C$)

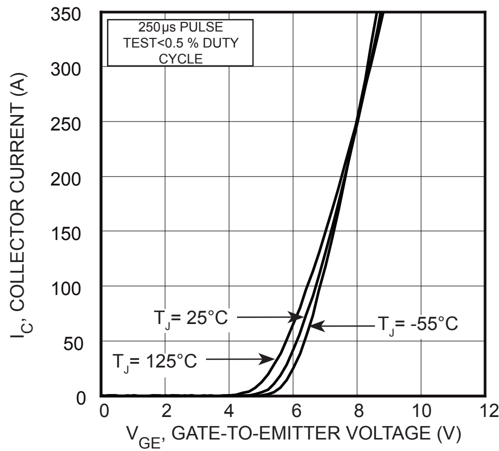

FIGURE 3, Transfer Characteristics

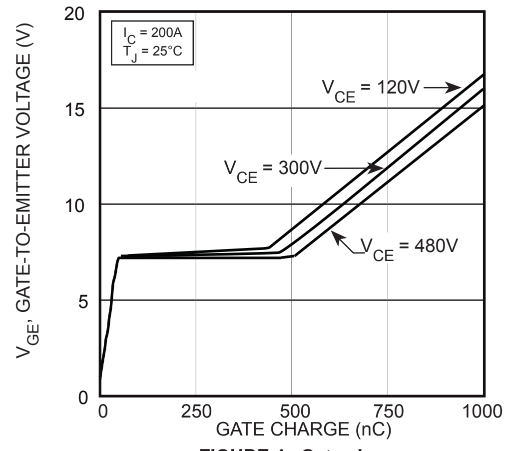

FIGURE 4, Gate charge

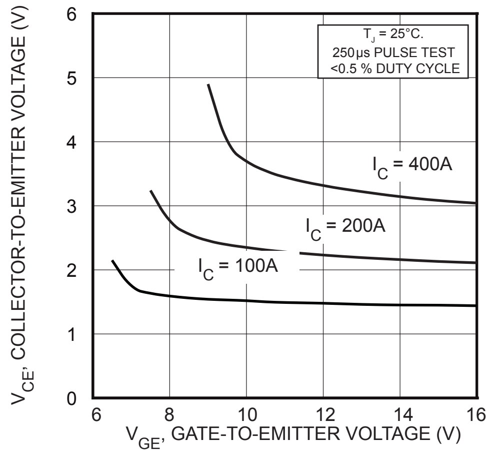

FIGURE 5, On State Voltage vs Gate-to-Emitter Voltage

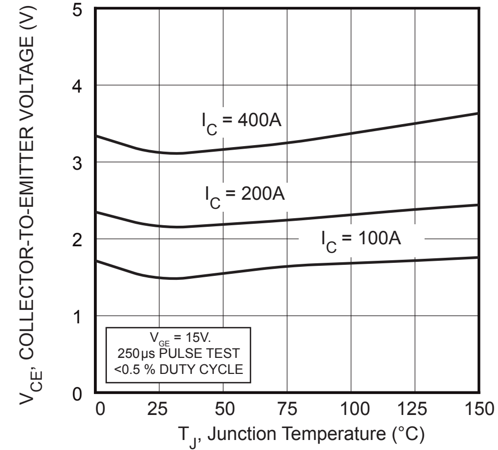

FIGURE 6, On State Voltage vs Junction Temperature

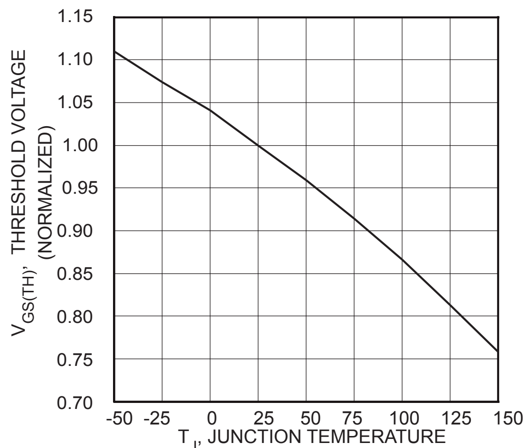

FIGURE 7, Threshold Voltage vs Junction Temperature

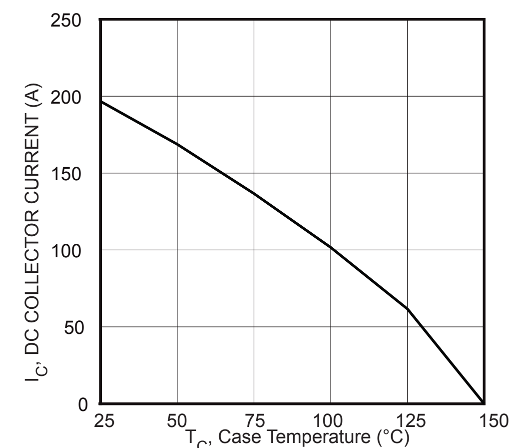

FIGURE 8, DC Collector Current vs Case Temperature

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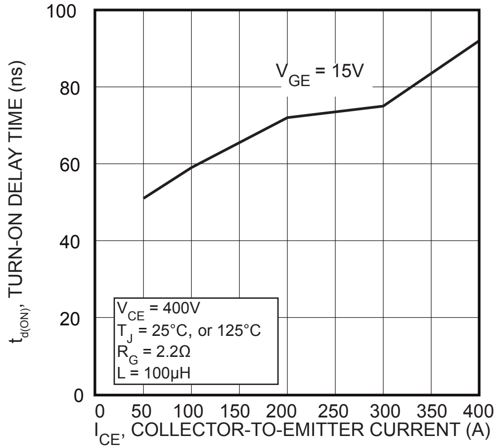

FIGURE 9, Turn-On Delay Time vs Collector Current

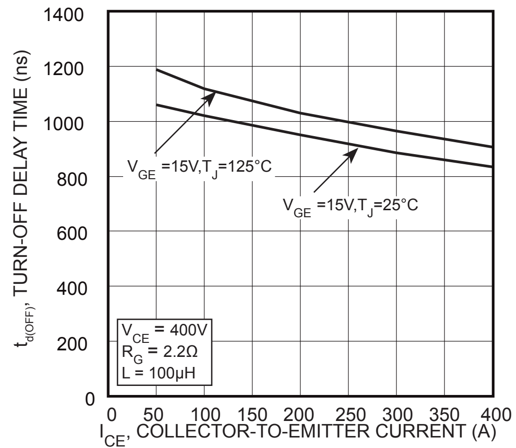

FIGURE 10, Turn-Off Delay Time vs Collector Current

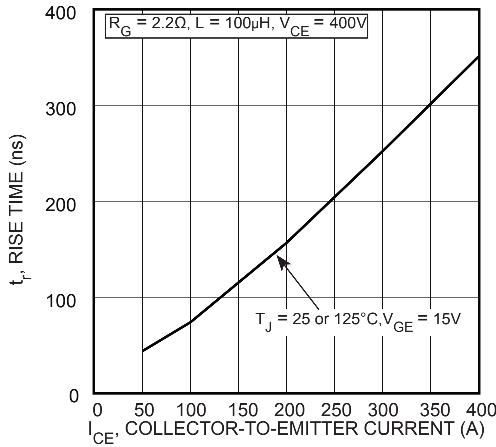

FIGURE 11, Current Rise Time vs Collector Current

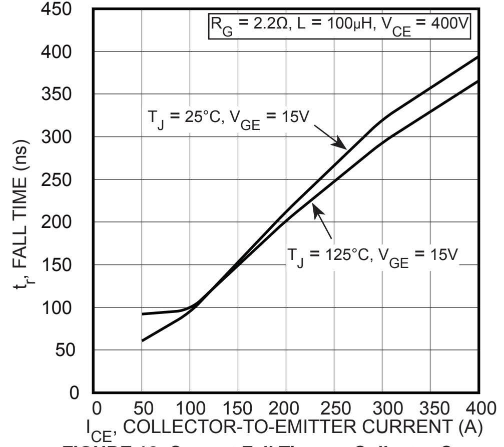

FIGURE 12, Current Fall Time vs Collector Current

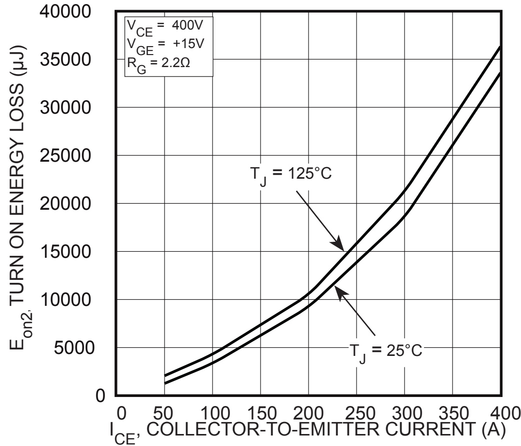

FIGURE 13, Turn-On Energy Loss vs Collector Current

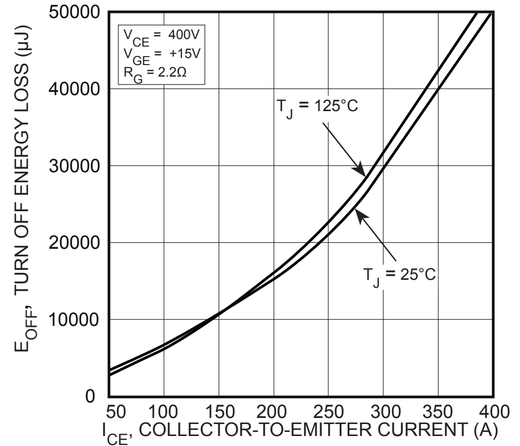

FIGURE 14, Turn-Off Energy Loss vs Collector Current

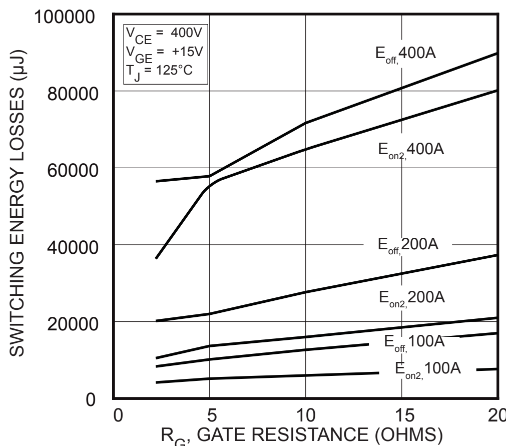

FIGURE 15, Switching Energy Losses vs Gate Resistance

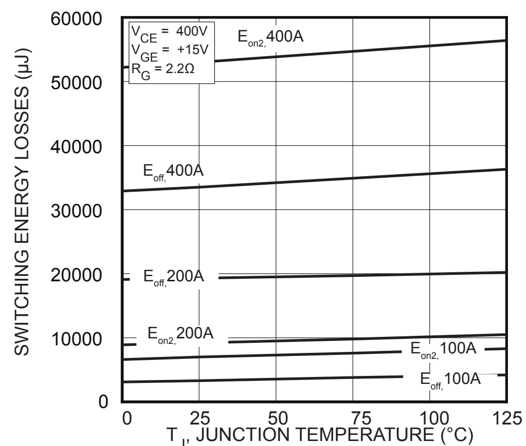

FIGURE 16, Switching Energy Losses vs Junction Temperature

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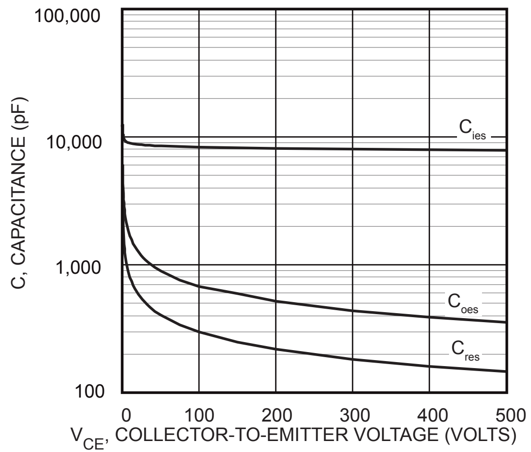

FIGURE 17, Capacitance vs Collector-To-Emitter Voltage

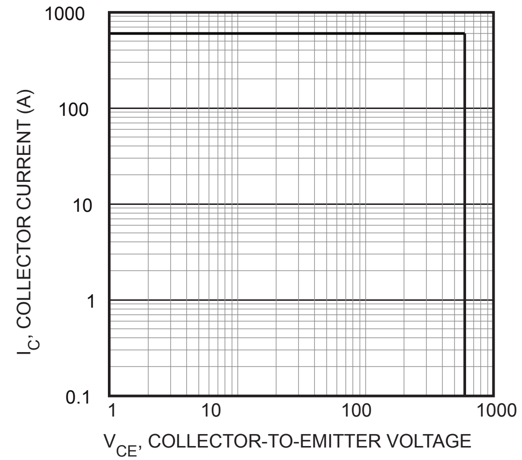

FIGURE 18, Minimum Switching Safe Operating Area

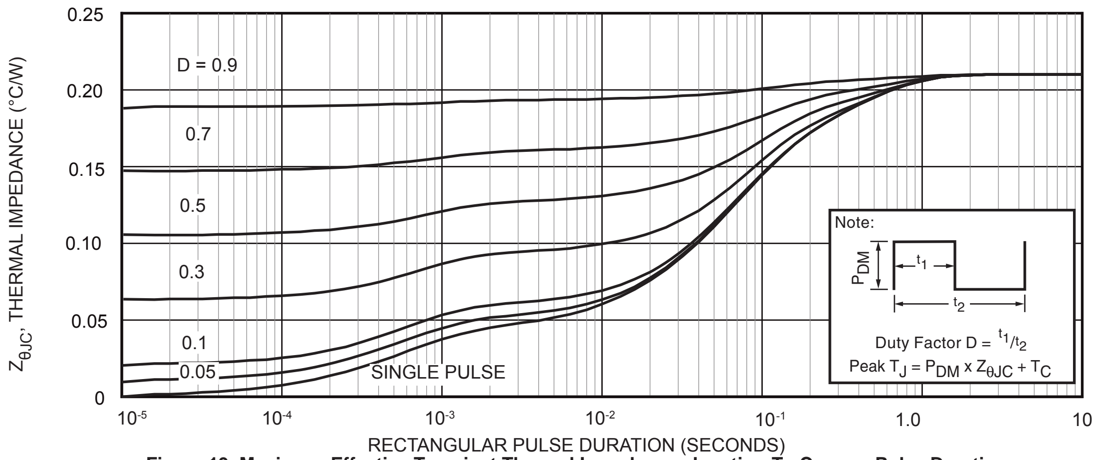

Figure 19, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

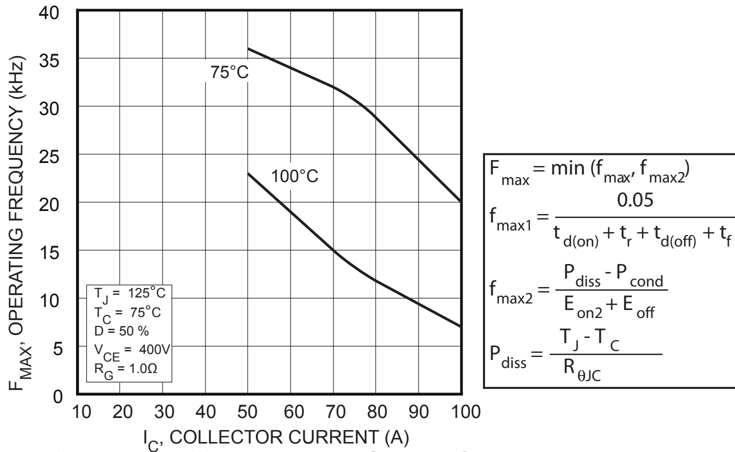

Figure 20, Operating Frequency vs Collector Current

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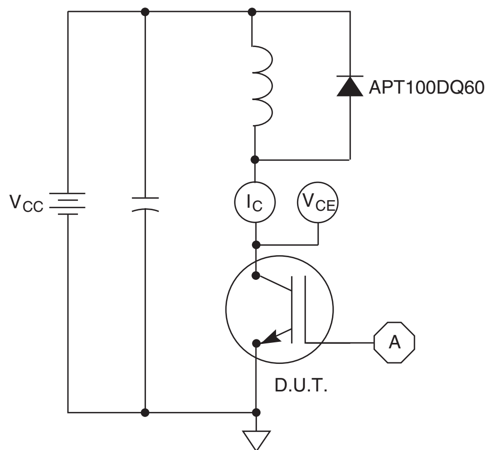

Figure 21, Inductive Switching Test Circuit

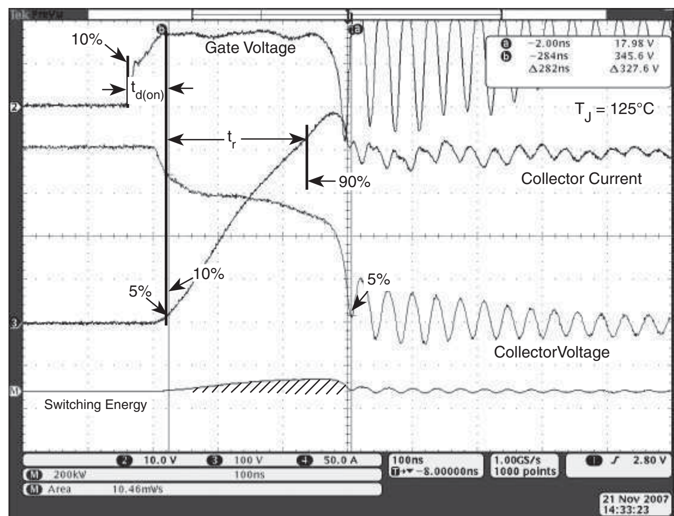

Figure 22, Turn-on Switching Waveforms and Definitions

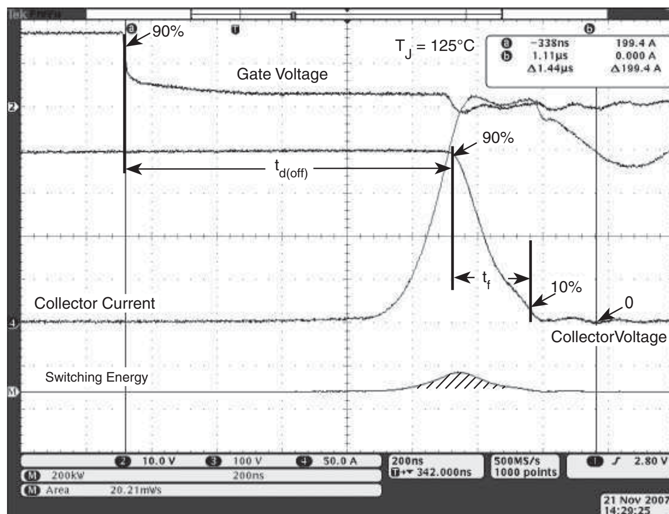

Figure 23, Turn-off Switching Waveforms and Definitions

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ULTRAFAST SOFT RECOVERY ANTI-PARALLEL DIODE

MAXIMUM RATINGS

All Ratings: T_C = 25°C unless otherwise specified.

Symbol	Characteristic / Test Conditions	APT200GT60JRDQ4	Unit
$I_{F(AV)}$	Maximum Average Forward Current (T_C = 100°C, Duty Cycle = 0.5)	60	Amps
$I_{F(RMS)}$	RMS Forward Current (Square wave, 50% duty)	73	
I_{FSM}	Non-Repetitive Forward Surge Current (T_J = 45°C, 8.3 ms)	540	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions		Min	Type	Max	Unit
V_F	Forward Voltage	I_F = 60A		1.7		Volts
		I_F = 120A		2.0		
		I_F = 60A, T_J = 125°C		1.4		

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
t_{rr}	Reverse Recovery Time	I_F = 1A, di_F/dt = -100A/µs, V_R = 30V, T_J = 25°C	-	60	-	ns
t_{rr}	Reverse Recovery Time	I_F = 60A, di_F/dt = -200A/µs V_R = 400V, T_C = 25°C	-	95	-	
Q_{rr}	Reverse Recovery Charge		-	135	-	nC
I_{RRM}	Maximum Reverse Recovery Current		-	3	-	Amps
t_{rr}	Reverse Recovery Time	I_F = 60A, di_F/dt = -200A/µs V_R = 400V, T_C = 125°C	-	130	-	ns
Q_{rr}	Reverse Recovery Charge		-	722	-	nC
I_{RRM}	Maximum Reverse Recovery Current		-	10	-	Amps
t_{rr}	Reverse Recovery Time	I_F = 60A, di_F/dt = -1000A/µs V_R = 400V, T_C = 125°C	-	140	-	ns
Q_{rr}	Reverse Recovery Charge		-	2776	-	nC
I_{RRM}	Maximum Reverse Recovery Current		-	38	-	Amps

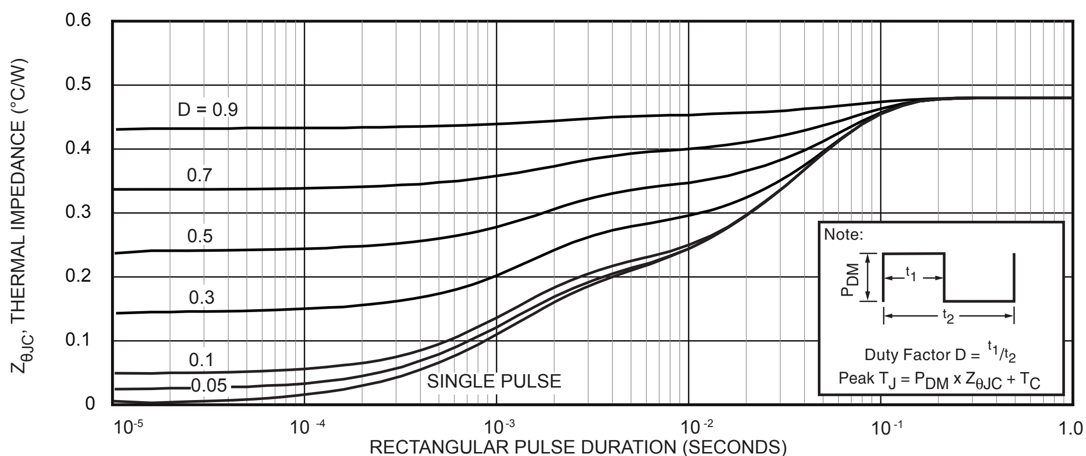

Figure 24, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

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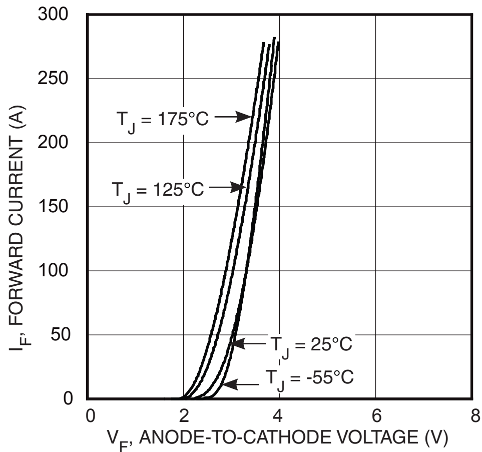

Figure 25. Forward Current vs. Forward Voltage

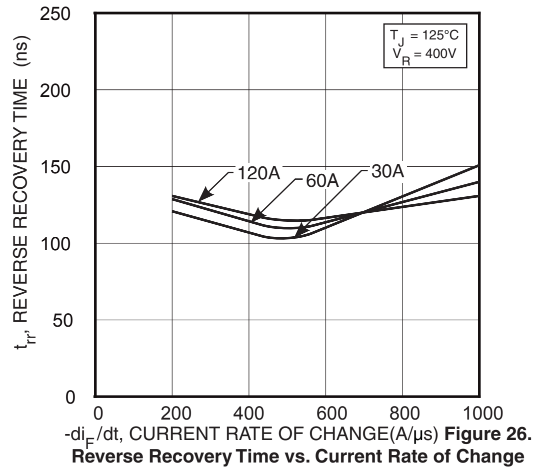

Figure 26. Reverse Recovery Time vs. Current Rate of Change

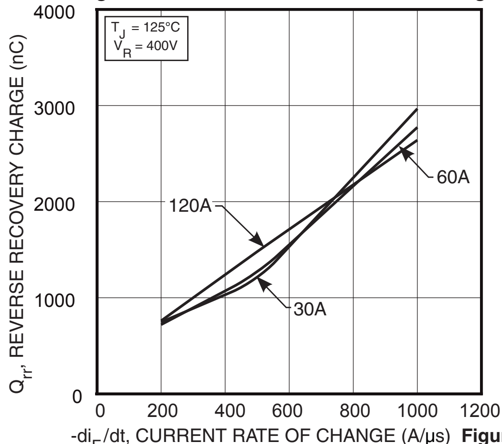

Figure 27. Reverse Recovery Charge vs. Current Rate of Change

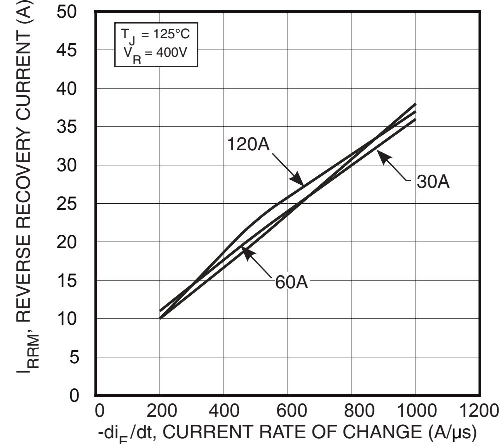

Figure 28. Reverse Recovery Current vs. Current Rate of Change

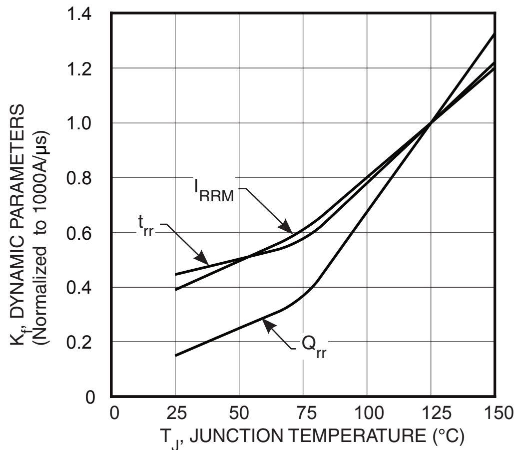

Figure 29. Dynamic Parameters vs. Junction Temperature

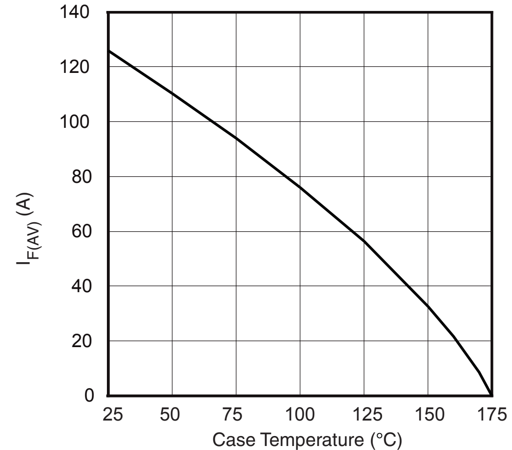

Figure 30. Maximum Average Forward Current vs. CaseTemperature

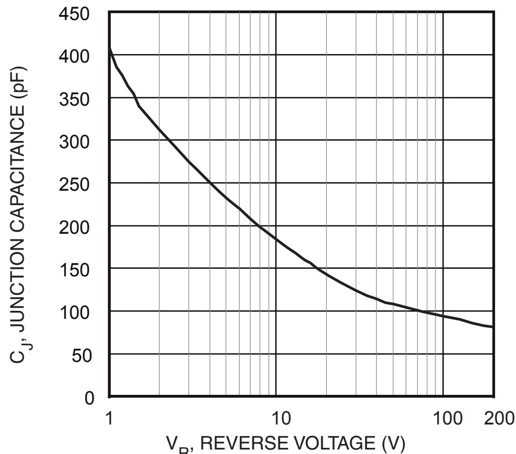

Figure 31. Junction Capacitance vs. Reverse Voltage

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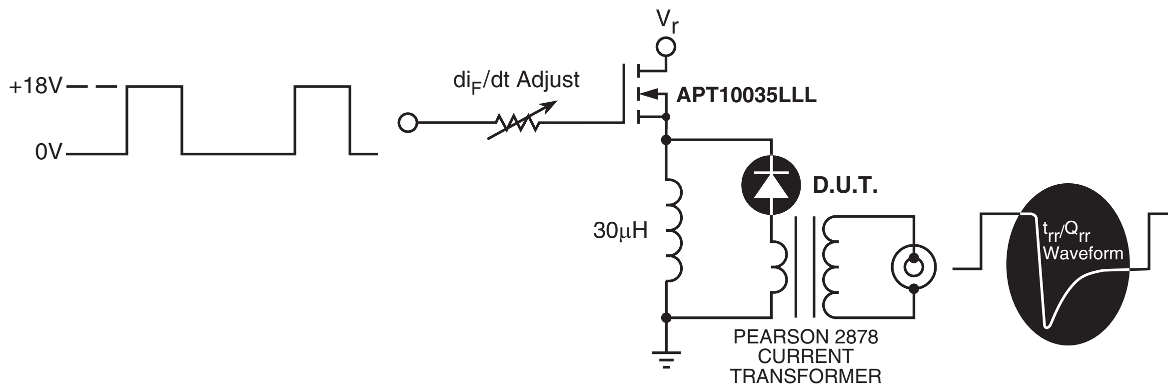


Figure 32, Diode Test Circuit

1. I_F - Forward Conduction Current
2. di_F/dt - Rate of Diode Current Change Through Zero Crossing.
3. I_{RRM} - Maximum Reverse Recovery Current.
4. t_{rr} - Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and $0.25 \bullet I_{RRM}$ passes through zero.
5. Q_{rr} - Area Under the Curve Defined by I_{RRM} and t_{rr}.

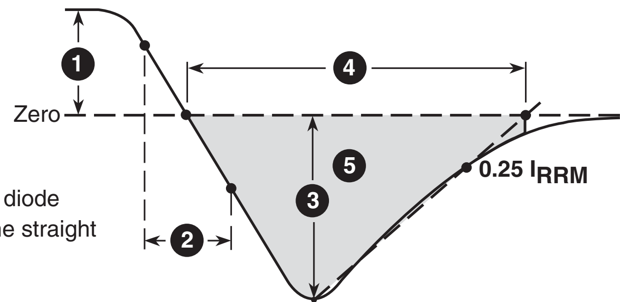
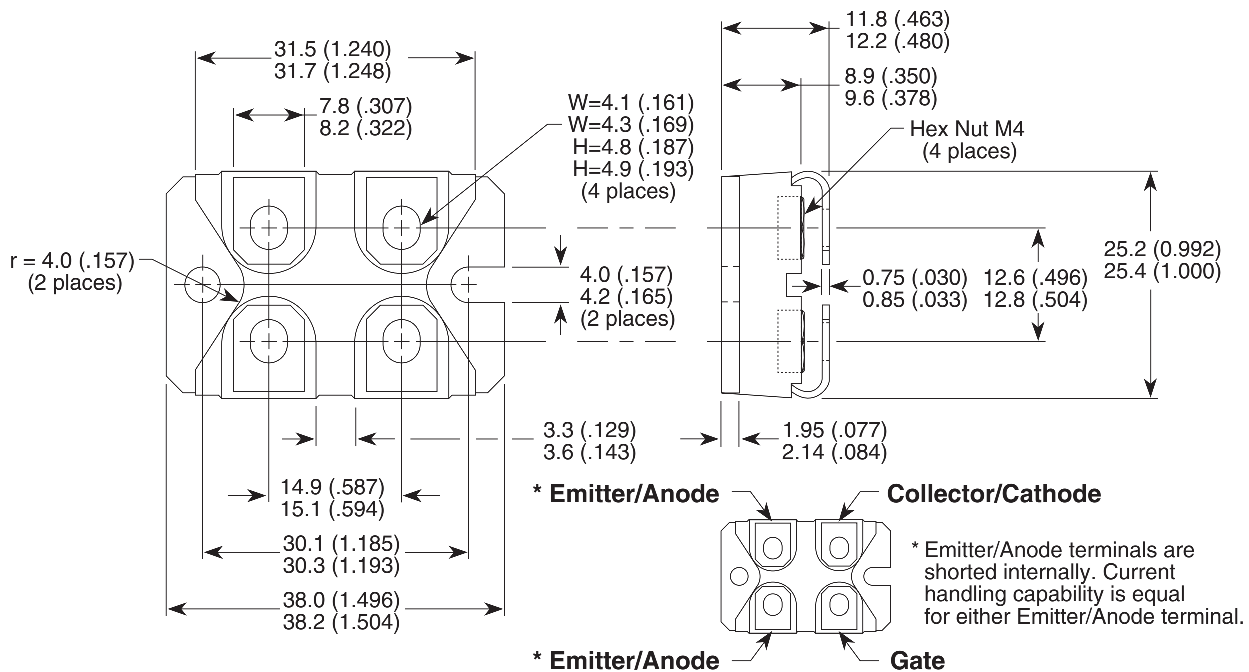


Figure 33, Diode Reverse Recovery Waveform and Definitions

SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)

Microsemi's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 6,939,743, 7,352,045 5,283,201 5,801,417 5,648,283 7,196,634 6,664,594 7,157,886 6,939,743 7,342,262 and foreign patents. US and Foreign patents pending. All Rights Reserved.

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