



---

## ***PWM Control 5A Step-Down Converter***

### **❖ GENERAL DESCRIPTION**

APE1809 consists of step-down switching regulator with PWM control. These device include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

APE1809 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to vary the duty ratio linearly from 0 up to 100%. This converter also contains an error amplifier circuit as well as a soft-start circuit that prevents overshoot at startup. An enable function, an over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. Also, an internal compensation block is built in to minimum external component count.

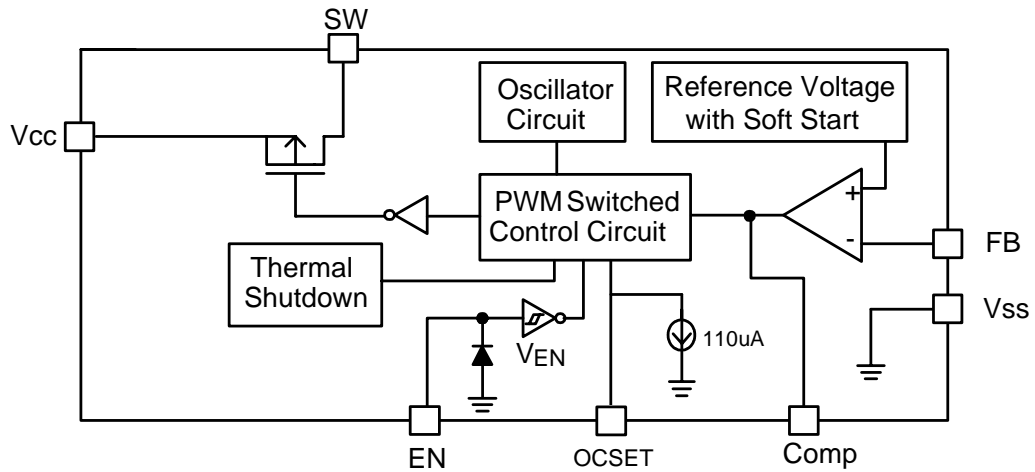
With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 23V, it is also suitable for the operation via an AC adapter.

### **❖ FEATURES**

- Input voltage : 4.0V to 23V
- Output voltage : 0.8V to Vcc
- Duty ratio : 0% to 100% PWM control
- Oscillation frequency : 330KHz typ.
- Soft-start(SS), Current Limit(CL), Enable function.
- Thermal Shutdown function.
- Short Circuit Protect (SCP).
- Built-in internal SW P-channel MOS.
- Low ESR output capacitor (Multi-layer chip capacitor (MLCC)) application.
- PDIP-8L Pb-Free package.

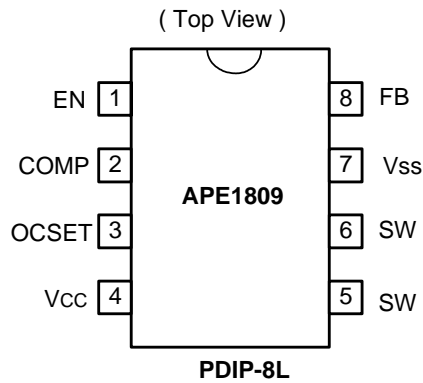


❖ Block Diagram



❖ PIN ASSIGNMET

The package of APE1809 is PDIP-8L; the pin assignment is given by:



Name	Description
EN	Power-off pin H : normal operation(Step-down) L : Step-down operation stopped (All circuits deactivated)
Comp	Compensation pin
OCSET	Add an external resistor to set max switch output current.
V <sub>cc</sub>	IC power supply pin
SW	Switch pin. Connect external inductor/diode here.
V <sub>ss</sub>	GND pin
FB	Feedback pin

❖ ORDER/MARKING INFORMATION

Order Information	Top Marking
<p><b>APE1809X</b></p> <p>Package Type D: PDIP-8L</p>	<p>1809D ▶ Part number</p> <p>YWWSSS ▶ ID code: internal</p> <p>▶ WW: 01~52</p> <p>▶ Year: 6 = 2006</p>



❖ **Absolute Maximum Ratings** (at Ta=25°C)

Characteristics	Symbol	Rating	Unit
VCC Pin Voltage	V <sub>CC</sub>	V <sub>SS</sub> - 0.3 to V <sub>SS</sub> + 25	V
Feedback Pin Voltage	V <sub>FB</sub>	V <sub>SS</sub> - 0.3 to V <sub>CC</sub>	V
ON/OFF Pin Voltage	V <sub>EN</sub>	V <sub>SS</sub> - 0.3 to V <sub>CC</sub> + 0.3	V
Switch Pin Voltage	V <sub>SW</sub>	V <sub>SS</sub> - 0.3 to V <sub>CC</sub> + 0.3	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	T <sub>ST</sub>	-40 to +150	°C
Operating Temperature Range	T <sub>OP</sub>	-20 to +125	°C
Operating Supply Voltage	V <sub>OP</sub>	+3.6 to +23	V
Thermal Resistance from Junction to case	θ <sub>JC</sub>	20	°C/W
Thermal Resistance from Junction to ambient	θ <sub>JA</sub>	45	°C/W

Note : θ<sub>JA</sub> is measured with the PCB copper area(need connect to SW pins) of approximately 1 in<sup>2</sup>(Multi-layer).

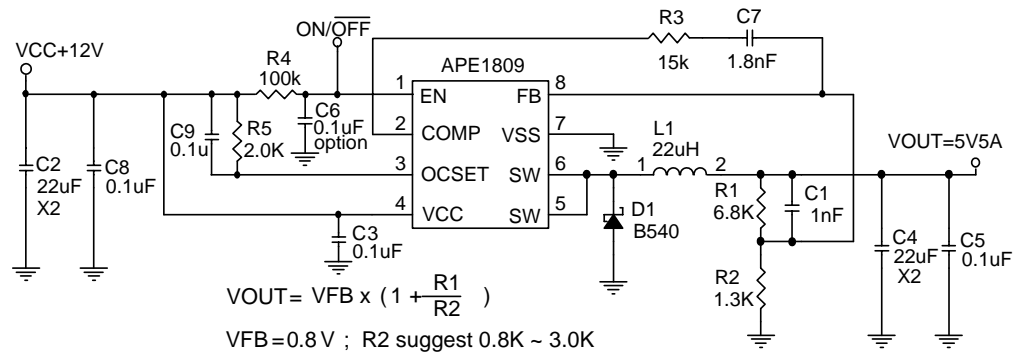
❖ **Electrical Characteristics** (VIN = 12V, Ta=25°C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units	
Feedback Voltage	V <sub>FB</sub>	I <sub>OUT</sub> =0.2A	0.784	0.8	0.816	V	
Quiescent Current	I <sub>CCQ</sub>	V <sub>FB</sub> =1.2V force driver off		3	5	mA	
Feedback Bias Current	I <sub>FB</sub>	I <sub>OUT</sub> =0.2A	-	0.1	0.5	uA	
Shutdown Supply Current	I <sub>SD</sub>	V <sub>EN</sub> =0V	-	2	10	uA	
OCSET pin bias current	I <sub>OCSET</sub>		95	110	125	uA	
Switch Current	I <sub>SW</sub>		6.0	-	-	A	
Line Regulation	ΔV <sub>OUT</sub> /V <sub>OUT</sub>	V <sub>CC</sub> = 5V~23V, I <sub>OUT</sub> =0.2A	-	0.6	1.2	%	
Load Regulation	ΔV <sub>OUT</sub> /V <sub>OUT</sub>	I <sub>OUT</sub> = 0.2 to 5A	-	0.3	0.5	%	
Oscillation Frequency	F <sub>OSC</sub>	SW pin	260	330	400	KHz	
EN Pin Logic input threshold voltage	V <sub>SH</sub>	High (regulator ON)	2.0	-	-	V	
	V <sub>SL</sub>	Low (regulator OFF)	-	-	0.8		
EN Pin Input Current	I <sub>SH</sub>	V <sub>EN</sub> =2.5V (ON)	-	20	-	uA	
	I <sub>SL</sub>	V <sub>EN</sub> =0.3V (OFF)	-	-10	-	uA	
Soft-Start Time	T <sub>SS</sub>		0.3	4	8	ms	
Internal MOSFET R <sub>DSON</sub>	R <sub>DSON</sub>	V <sub>CC</sub> =5V, V <sub>FB</sub> =0V	-	80	140	mΩ	
		V <sub>CC</sub> =12V, V <sub>FB</sub> =0V	-	50	90		
Efficiency	EFFI	V <sub>OUT</sub> = 5V	I <sub>OUT</sub> = 3A	-	91	-	%
			I <sub>OUT</sub> = 4A	-	90	-	
Thermal shutdown Temp	TSD			125		°C	



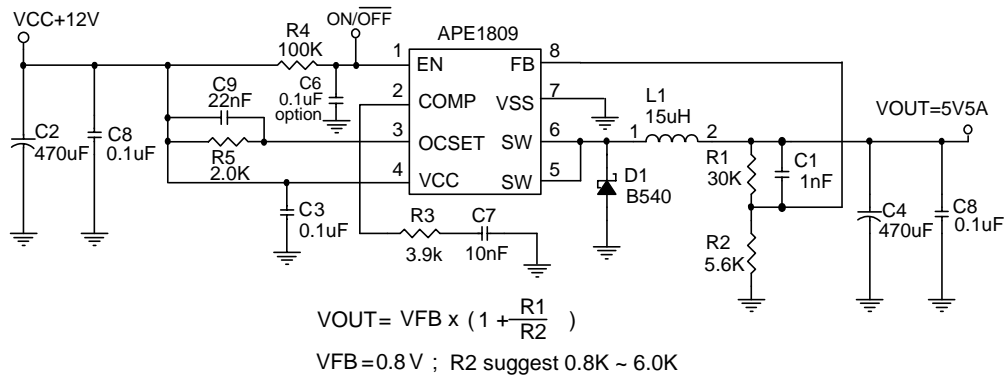
❖ Application Circuit

1. MLCC



Compensation Capacitor Selection(MLCC)					
V <sub>IN</sub>	V <sub>OUT</sub>	R <sub>3</sub>	C <sub>7</sub>	C <sub>1</sub>	C <sub>9</sub>
12V	2.5/3.3/ 5.0V	15K	1800pF	1nF	Open
5V	3.3/2.5/1.8V	15K	1800pF	1nF	Open

2. EL CAP



Compensation Capacitor Selection(AL CAP)					
V <sub>IN</sub>	V <sub>OUT</sub>	R <sub>3</sub>	C <sub>7</sub>	C <sub>1</sub>	C <sub>9</sub>
5-16V	5/3.3/2.5/1.8V	3.9K	10nF	1nF	Open

L1 recommend value (V <sub>IN</sub> =12V)				
V <sub>OUT</sub>	1.8 V	2.5V	3.3V	5V
I <sub>OUT</sub> =3A	12uH	15uH	18uH	22uH
I <sub>OUT</sub> =5A	8uH	10uH	12uH	15uH



❖ **Function Descriptions**

PWM Control

The APE1809 consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the APE1809, the pulse width varies in a range from 0 to 100%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

RDS(ON) Current Limiting

The current limit threshold is setting by the external resistor (R5) connecting from V<sub>CC</sub> supply to OCSET pin. The internal 110uA sink current crossing the resistor sets the voltage at pin of OCSET. When the PWM voltage is less than the voltage at OCSET, an over-current condition is triggered. Please refer to the formula for setting the current limit value:

$$I_{SW(MAX)} = \frac{I_{OCSET} \times R3 + 0.11}{R_{DS(ON)}}$$

( Normally, The I<sub>SW(MAX)</sub> setting more than I<sub>OUT</sub> 1.5~2.5A ).

Example:

$$I_{SW} = (110\mu A * 2.0k + 0.11) / 50m\Omega = 6.6A$$

Setting the Output Voltage

Application circuit item shows the basic application circuit with APE1809 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.8V \times \left( 1 + \frac{R1}{R2} \right)$$

Table 1 Resistor select for output voltage setting

V <sub>OUT</sub>	R2	R1
5V	1.3K	6.8K
	5.6K	30K
3.3V	1.5K	4.7K
	5.6K	18K
2.5V	2.2K	4.7K
	5.6K	12K
1.8V	2K	2.5K



PCB Layout Guide

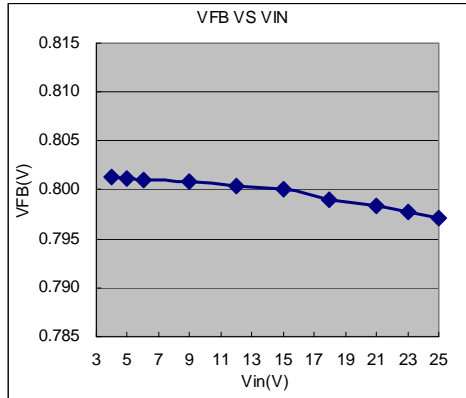
If you need low  $T_c$  &  $T_j$  or large PD(Power Dissipation), The dual SW pins(5&6) on the PDIP-8L package are internally connected to die pad, The PCB layout should allow for maximum possible copper area at the SW pins.

1. Connect C3 to  $V_{CC}$  pin as closely as possible to get good power filter effect.
2. Connect R5 to  $V_{CC}$  pin as closely as possible.
3. Connect ground side of the C2 & D1 as closely as possible.

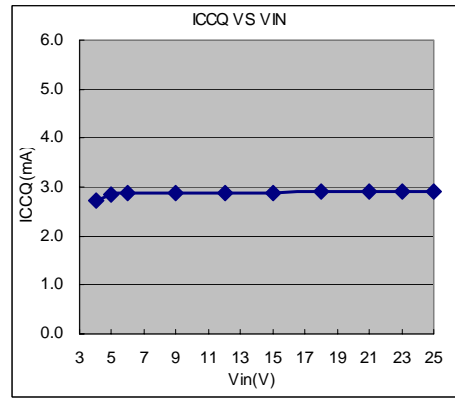


❖ Typical Characteristics

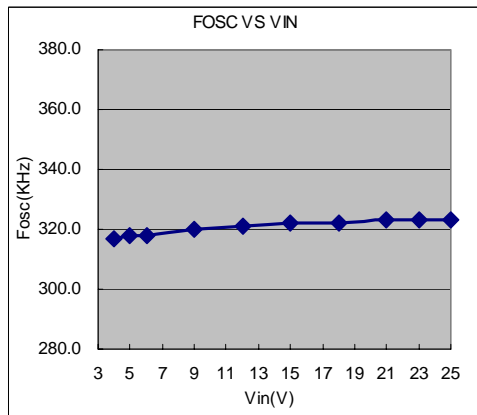
VFB VS VIN



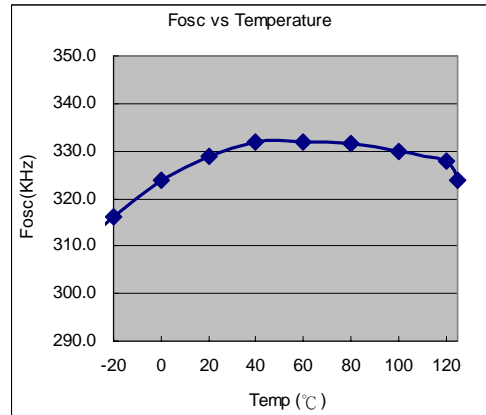
ICCQ VS VIN



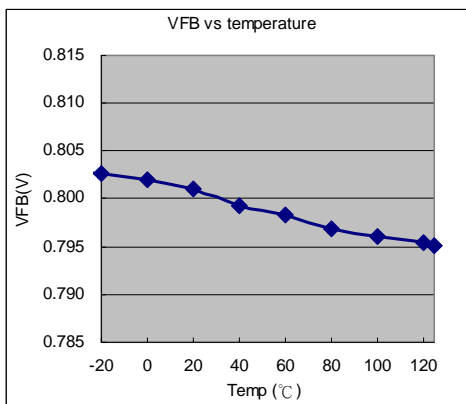
FOSC VS VIN



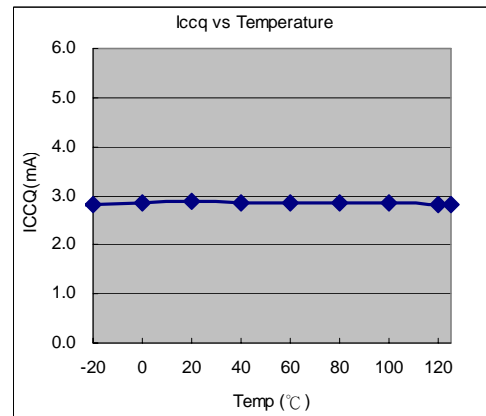
FOSC VS TEMPERATURE



VFB VS TEMPERATURE



ICCQ VS TEMPERATURE

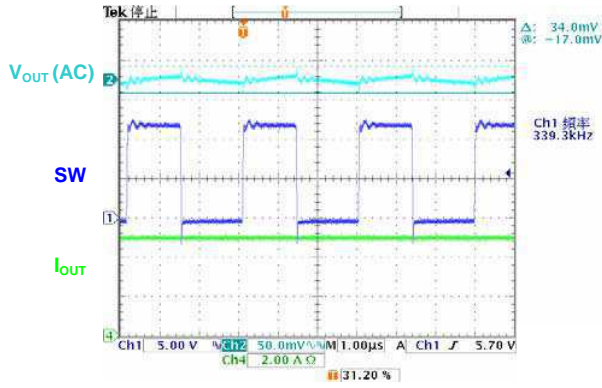




❖ Typical Characteristics

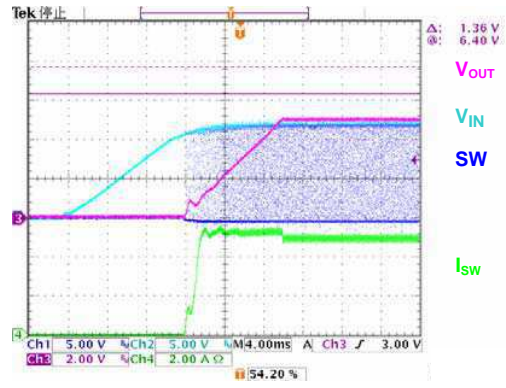
Output Ripple

( $V_{IN}=12V, V_{OUT}=5.0V, I_{OUT}=5A$ )



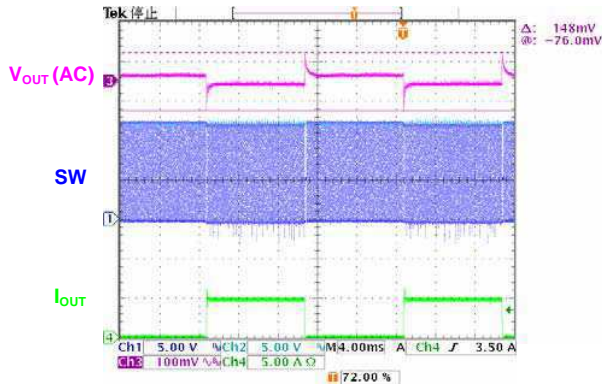
Power on test wave

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=5A$ )



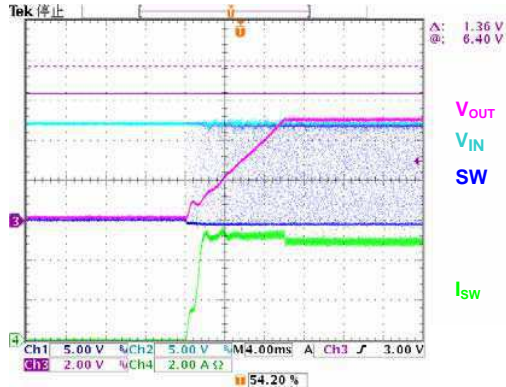
Load Transient Response

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0.2\sim 5A$ )



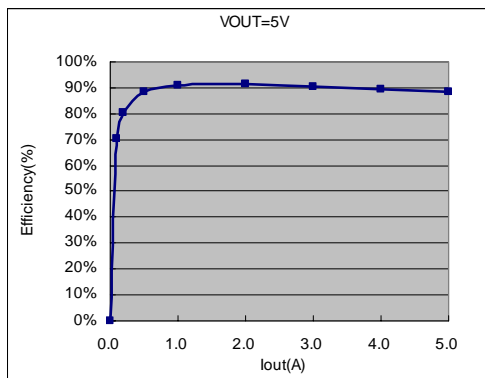
EN on test wave

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=5A$ )



Efficiency

( $V_{IN}=12V, V_{OUT}=5V$ )



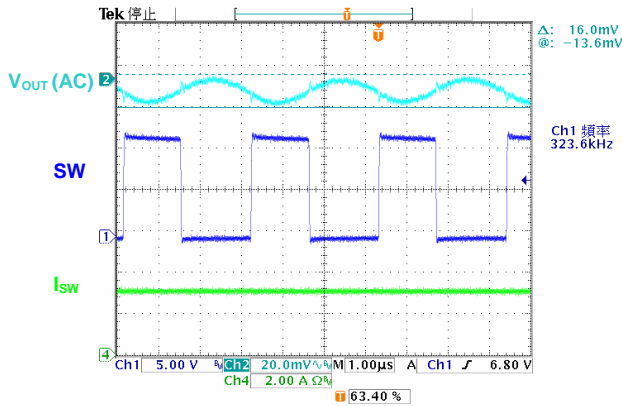




❖ Typical Characteristics (MLCC)

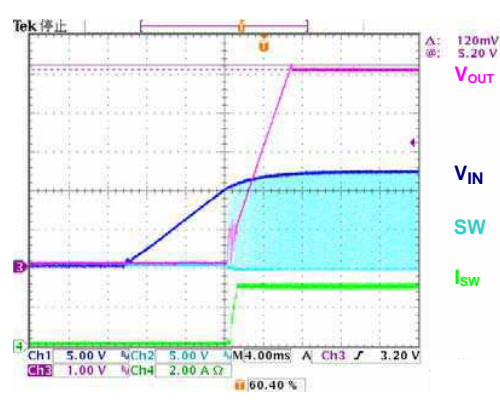
Output Ripple

( $V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=3A$ )



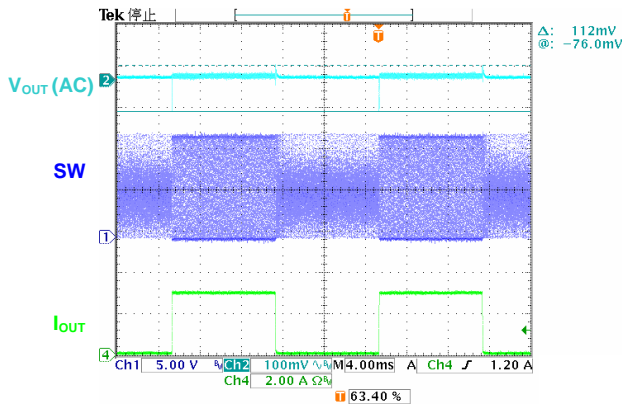
Power on test wave

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=3A$ )



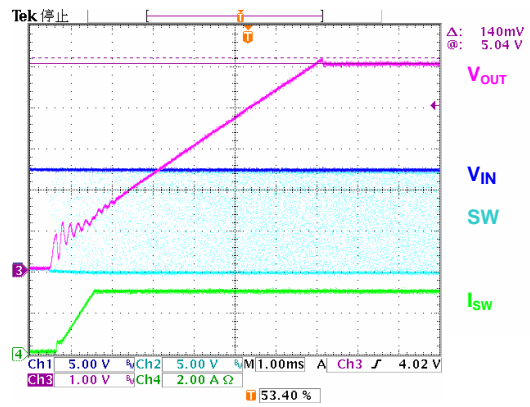
Load Transient Response

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0.1\sim 3A$ )



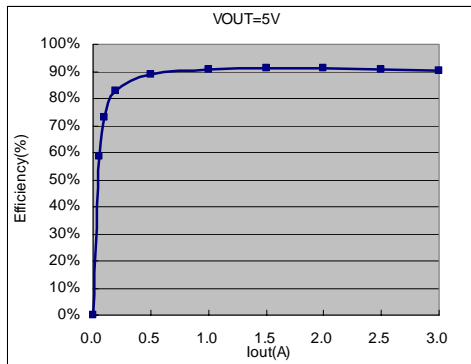
EN on test wave

( $V_{IN}=12V, V_{OUT}=5V, I_{OUT}=3A$ )



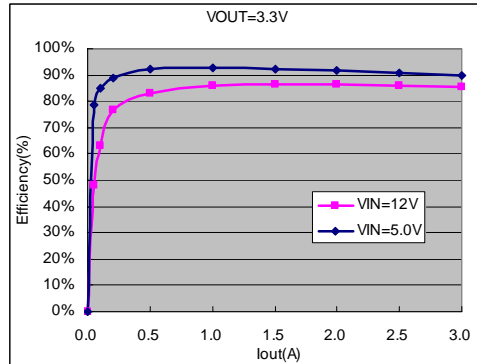
Efficiency

( $V_{IN}=12V, V_{OUT}=5V$ )



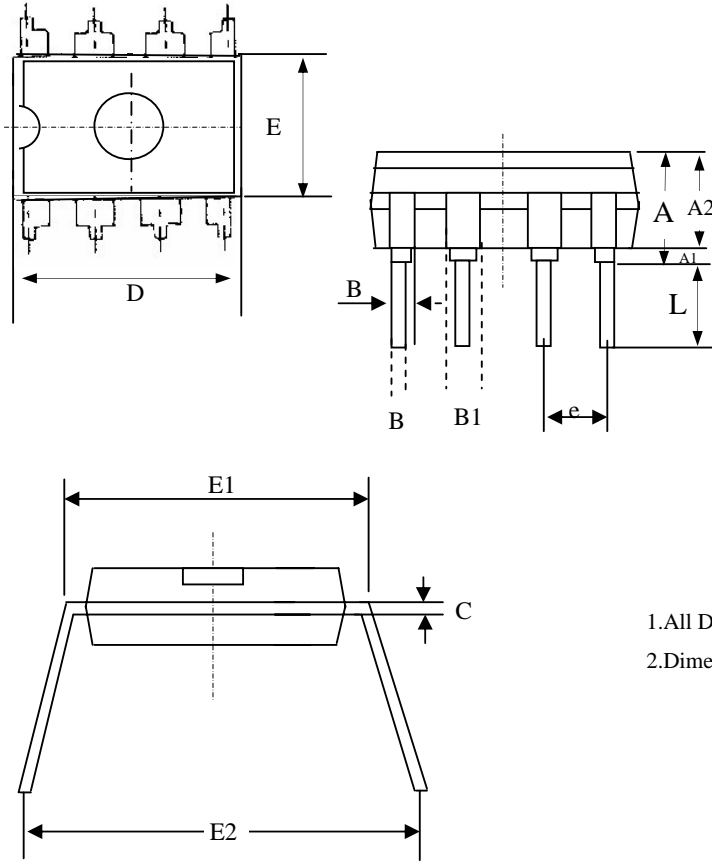
Efficiency

( $V_{OUT}=3.3V$ )





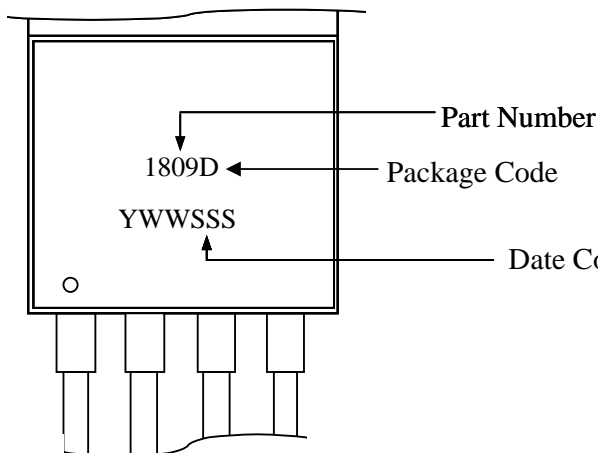
## Package Outline : PDIP-8L



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	3.60	4.50	5.40
A1	0.38	----	----
A2	2.90	3.95	5.00
B	0.36	0.46	0.56
B1	1.10	1.45	1.80
B2	0.76	0.98	1.20
C	0.20	0.28	0.36
D	9.00	9.60	10.20
E	6.10	6.65	7.20
E1	7.62	7.94	8.26
E2	8.3	9.65	11
e	2.540BSC		
L	3.18	----	----

1. All Dimensions Are in Millimeters.
2. Dimension Does Not Include Mold Protrusions.

## Part Marking Information & Packing : PDIP-8L



**Y :** Last Digit Of The Year  
**WW :** Week  
**SSS :** Sequence