

CD771060 Thyristors

Half-Controlled Negative Thyristor Doubler

V(DRM) Max. (V) 1.0k

V(RRM) Max. (V)

I(T) Rated Maximum (A) 80

@Temp. (°C) (Test Condition) 125

I(TSM) Max. (A) 1.1k

@ t(w) (s) (Test Condition)

I(GT) Max. (A)

V(GT) Max.(V)

I(H) Max. (A) Holding Current

I(D) Max. (A) Leakage Current

@Temp. (°C) (Test Condition)

V(T) Max. (V) 1.7

@I(T) (A) (Test Condition) 250

t(q) Typ. (s)

dv/dt Min. (V/us)

Minimum Operating Temp (°C)

Maximum Operating Temp (°C) 125

Thermal Resistance Junc-Case

& Name of Screw Terminals

& Name of Quick-Connects