7MBR35VM120-50

IGBT Modules

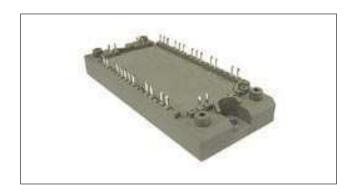
IGBT MODULE (V series) 1200V / 50A / PIM

■ Features

Low VcE(sat)
Compact Package
P.C.Board Mount Module
Converter Diode Bridge Dynamic Brake Circuit
RoHS compliant product

■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply



■ Maximum Ratings and Characteristics

■ Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions	Conditions		Units	
Collector-Emit	ter voltage	Vces				V	
Gate-Emitter v	oltage	V _{GES}			±20	V	
- a	Collector current	Ic	Continuous	Tc=80°C	35		
E Collector curre		Icp	1ms	Tc=80°C	70	Δ.	
E Collector curre		-lc				Α	
		-lc pulse	1ms		70		
Collector power	er dissipation	Pc	1 device		210	W	
Collector-Emit	ter voltage	Vces			1200	V	
Gate-Emitter v	oltage	V _{GES}			±20	V	
© Collector curre	Collector current	Ic	Continuous	Tc=80°C	25	٨	
Collector curre		Іср	1ms	Tc=80°C	50	Α	
Collector power	Collector power dissipation		1 device		170	W	
Repetitive pea	Repetitive peak reverse voltage (Diode)				1200	V	
Repetitive pea	Repetitive peak reverse voltage					V	
Average outpu	Average output current		50Hz/60Hz, sii	50Hz/60Hz, sine wave		Α	
Average output Surge current	Surge current (Non-Repetitive)		10ms, Tj=150°C		260	Α	
I²t (Non-Repeti	I²t (Non-Repetitive)		half sine wave	half sine wave		A²s	
Junction temperature		T :	Inverter, Brake		175		
		Tj	Converter		150		
Operating junciton temperature (under switching conditions)		Inverter, Brake		150	*0		
		Tjop	Converter		150	°C	
Case temperature		Tc			125		
Storage temperature		Tstg					
solation voltage	olation voltage between terminal and copper base (*1) between thermistor and others (*2)		AC : 1min.	AC : 1min.		VAC	
Screw torque	Mounting (*3)	-	M5		3.5	N m	

Note *1: All terminals should be connected together during the test.

Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note *3: Recommendable value: 2.5-3.5 Nm (M5)

● Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items		Symbolo	Canditions		Characteristics			Units		
		Symbols	Conditions	min.	typ.	max.	Units			
	Zero gate voltage collector current	Ices	V _{GE} = 0V, V _{CE} = 1200V		-	-	1.0	mA		
	Gate-Emitter leakage current	I _{GES}	V _{GE} = 0V, V _{GE} = ±20V		-	-	200	nA		
	Gate-Emitter threshold voltage	V _{GE (th)}	V _{CE} = 20V, I _C = 35mA		6.0	6.5	7.0	V		
	Collector-Emitter saturation voltage	V _{CE (sat)} (terminal)	V _{GE} = 15V I _C = 35A	Tj=25°C	-	2.15	2.60	V		
				Tj=125°C	-	2.50	-			
				Tj=150°C	-	2.55	-			
		VcE (sat) (chip)	V _{GE} = 15V I _C = 35A	Tj=25°C	-	1.85	2.30			
				Tj=125°C	-	2.20	-			
				Tj=150°C	-	2.25	-			
_	Input capacitance	Cies	V _{CE} = 10V, V _{GE} = 0V, f = 1MHz		-	2.9	-	nF		
nverter	Turn-on time	ton			-	0.39	1.20			
nve		tr	Vcc = 600V		-	0.09	0.60	μs		
		tr (i)	I _c = 35A V _{GE} = +15 / -15V		-	0.03	-			
	Turn-off time	toff	$R_G = 27\Omega$		-	0.53	1.00	1		
	Turn-oπ time	tf			-	0.06	0.30	1		
		V _F (terminal)		Tj=25°C	-	2.00	2.45	V		
	Forward on voltage		I _F = 35A	Tj=125°C	-	2.15	-			
				Tj=150°C	-	2.10	-			
		V _F (chip)	I _F = 35A	Tj=25°C	-	1.70	2.15			
				Tj=125°C	-	1.85	-			
				Tj=150°C	-	1.80	-			
	Reverse recovery time	trr	I _F = 35A		-	-	0.1	μs		
	Zero gate voltage collector current	Ices	V _{GE} = 0V V _{CE} = 1200V		-	-	1.0	mA		
	Gate-Emitter leakage current	Iges	V _{CE} = 0V V _{GE} = +20 / -20V		-	-	200	nA		
	Collector-Emitter saturation voltage	V _{CE (sat)} (terminal)	V _{GE} = 15V I _C = 25A	Tj=25°C	-	2.05	2.50	- V		
Brake				Tj=125°C	-	2.40	-			
			IC - 25A	Tj=150°C	-	2.45	-			
		V _{CE (sat)} (chip)	V _{GE} = 15V I _C = 25A	Tj=25°C	-	1.85	2.30			
				Tj=125°C	-	2.20	-			
				Tj=150°C	-	2.25	-]		
	-	ton	V _{CE} = 600V	'	-	0.39	1.20			
	Turn-on time	tr	Ic = 25A		-	0.09	0.60	μs		
		toff	V _{GE} = +15 / -15V		-	0.53	1.00			
	Turn-off time	tf	$R_{\rm G} = 39\Omega$		-	0.06	0.30	1		
	Reverse current	IRRM V _R = 1200V		-	-	1.00	mA			
Converter		V _{FM} (chip)	I _F = 35A	terminal	-	1.65	2.10	,,,		
	Forward on voltage			chip	-	1.35	-	V		
	Reverse current	IRRM	V _R = 1600V		-	-	1.0	mA		
ō		R	T = 25°C T = 100°C		-	5000	-	- Ω K		
Thermistor	Resistance				465	495	520			
The	B value	В	T = 25 / 50°C		3305	3375	3450			

● Thermal resistance characteristics

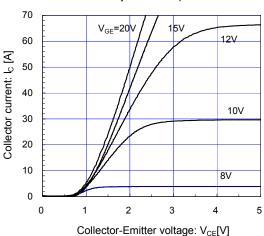
Items	Symbols	Conditions	Characteristics			Units		
items		Conditions	min.	typ.	max.	Units		
	Rth(j-c)	Inverter IGBT	-	-	0.72	°C/W		
Thermal resistance (1device)		Inverter FWD	-	-	0.91			
Thermal resistance (Tuevice)		Brake IGBT	-	-	0.89			
		Converter Diode	-	-	0.88			
Contact thermal resistance (1device) (*4) Rth(c-f		with Thermal Compound	-	0.05	-			

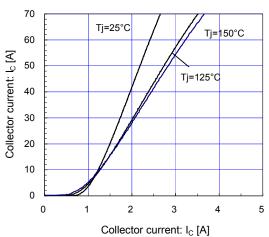
Note *4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

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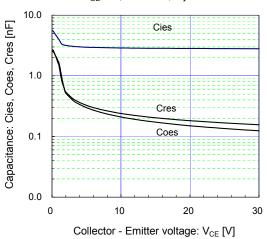
■ Characteristics (Representative)

 $\label{eq:continuous} \begin{tabular}{l} [Inverter] \\ Collector current vs. Collector-Emitter voltage (typ.) \\ Tj= 25^{\circ}C\ /\ chip \end{tabular}$

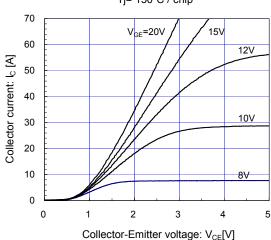




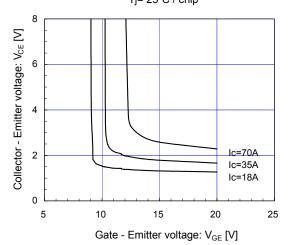
 $\label{eq:continuous} \begin{tabular}{ll} [Inverter] \\ Capacitance vs. Collector-Emitter voltage (typ.) \\ V_{GE}=0V, f= 1MHz, Tj= 25°C \\ \end{tabular}$



[Inverter] Collector current vs. Collector-Emitter voltage (typ.) $Ti=150^{\circ}C \text{ / chip}$



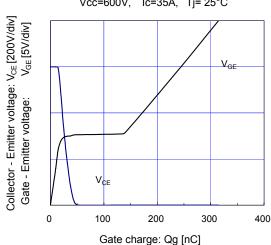
 $[Inverter] \\ Collector-Emitter voltage \ vs. \ Gate-Emitter voltage \ (typ.) \\ Tj= 25^{\circ}C \ / \ chip$

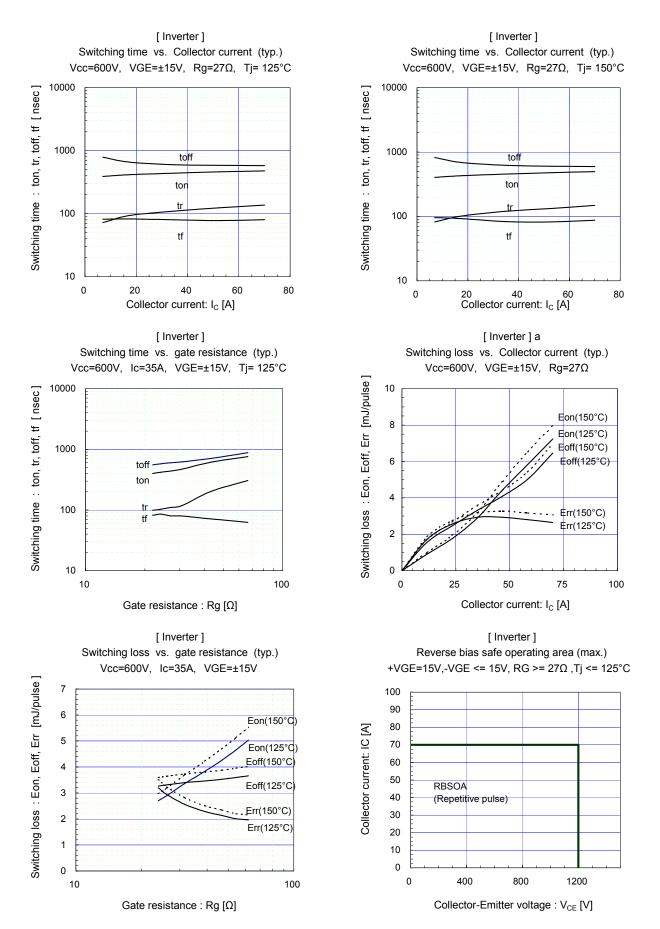


[Inverter]

Dynamic gate charge (typ.)

Vcc=600V, Ic=35A, Tj= 25°C

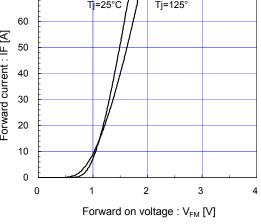


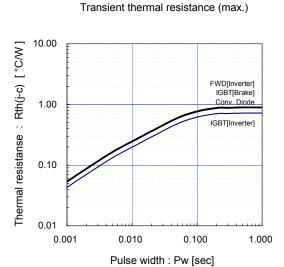


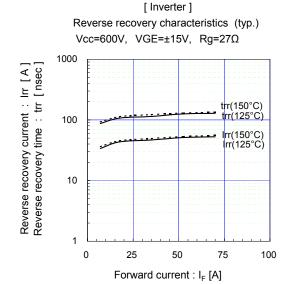
[Inverter] Forward current vs. forward on voltage (typ.) chip 70 Tj=25°C 60 Forward current : IF [A] 50 40 Tj=150°C 30 Tj=125°C 20 10 0 2 3 0

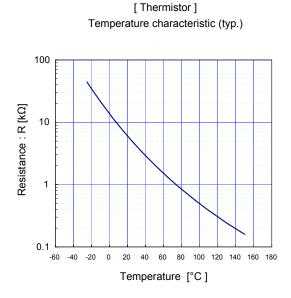
5 Forward on voltage : V_F [V] [Converter] Forward current vs. forward on voltage (typ.)

chip 70 Tj=25°C Tj=125° 60 Forward current : IF [A] 50 40 30 20 10 0 0 2

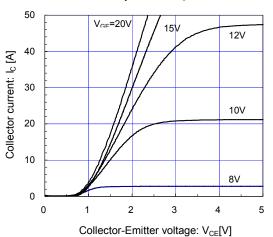




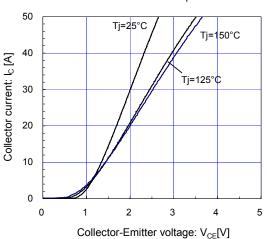




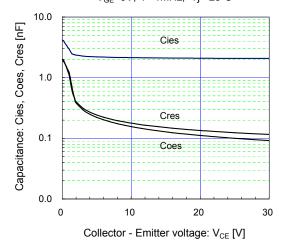
[Brake] Collector current vs. Collector-Emitter voltage (typ.) $Tj=25^{\circ}C\ /\ chip$



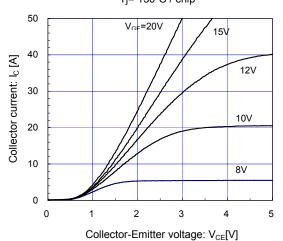
[Brake]
Collector current vs. Collector-Emitter voltage (typ.)
VGE=15V / chip



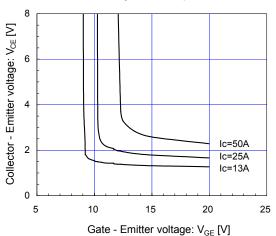
 $[\mbox{ Brake }] \label{eq:capacitance vs. Collector-Emitter voltage (typ.)} $$V_{GF}=0V, \mbox{ f= 1MHz}, \mbox{ Tj= }25^{\circ}\mbox{C}$



[Brake] Collector current vs. Collector-Emitter voltage (typ.) $Tj = 150^{\circ}C \text{ / chip}$



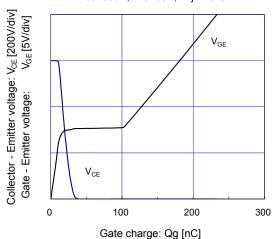
[Brake]
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)
Tj= 25 C / chip



[Brake]

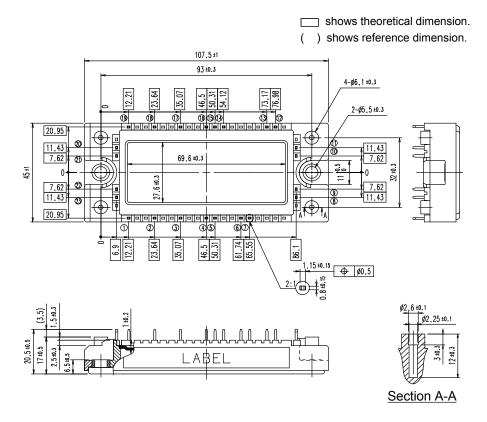
Dynamic gate charge (typ.)

Vcc=600V, Ic=35A, Tj= 25°C

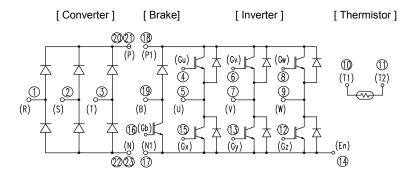


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■ Outline Drawings, mm



■ Equivalent Circuit Schematic



WARNING

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- Measurement equipment

- Machine tools
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