1. General description

The 74AUP1G17 provides the single Schmitt-trigger buffer. It is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The inputs switch at different points for positive and negative-going signals. The difference between the positive voltage V_{T+} and the negative voltage V_{T-} is defined as the input hysteresis voltage V_{H} .

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
 - ◆ HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \ \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from –40 °C to +85 °C and –40 °C to +125 °C





3. Ordering information

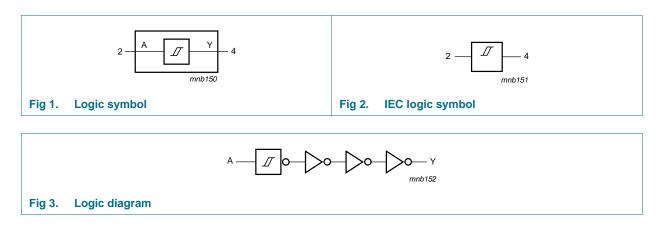
Table 1. Ordering information											
Type number	Package										
	Temperature range	Name	Description	Version							
74AUP1G17GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1							
74AUP1G17GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886							
74AUP1G17GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1 \times 0.5 mm	SOT891							
74AUP1G17GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115							
74AUP1G17GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202							

4. Marking

Table 2. Marking	
Type number	Marking code ^[1]
74AUP1G17GW	pJ
74AUP1G17GM	pJ
74AUP1G17GF	pJ
74AUP1G17GN	pJ
74AUP1G17GS	pJ

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

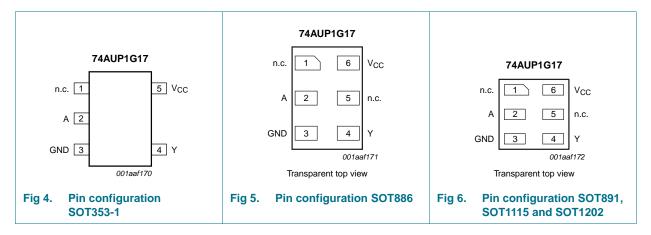
5. Functional diagram





6. Pinning information

6.1 Pinning



6.2 Pin description

Table 3.PinSymbol	description Pin		Description
	TSSOP5	XSON6	
n.c.	1	1	not connected
A	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

7. Functional description

Table 4. Function table^[1]

Input	Output
A	Y
L	L
н	Н

[1] H = HIGH voltage level; L = LOW voltage level.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

				10	,
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage		<u>[1]</u> –0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode	<u>[1]</u> –0.5	+4.6	V
lo	output current	$V_{O} = 0 V$ to V_{CC}	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[2] _	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP5 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K. For XSON6 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6.	Recommended operating co	indutions			
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; $V_{CC} = 0 V$	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

Table 6. Recommended operating conditions



10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Uni
T _{amb} = 2	5 °C					
V _{он}	HIGH-level output voltage	$V_I = V_{T+} \text{ or } V_{T-}$				
		I_{O} = –20 $\mu A;$ V_{CC} = 0.8 V to 3.6 V	$V_{CC}-0.1$	-	-	V
		$I_0 = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75\times V_{CC}$	-	-	V
		$I_0 = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_0 = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_0 = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_0 = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{T+} \text{ or } V_{T-}$				
		$I_0 = 20 \ \mu\text{A}; \ V_{CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 imes V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
I	input leakage current	$V_{I} = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.1	μA
OFF	power-off leakage current	V_{1} or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μA
∆l _{OFF}	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
СС	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
∆I _{CC}	additional supply current	$\label{eq:VI} \begin{array}{l} V_{I} = V_{CC} - 0.6 \; V; \; I_{O} = 0 \; A; \\ V_{CC} = 3.3 \; V \end{array}$	-	-	40	μA
CI	input capacitance	$V_I = GND \text{ or } V_{CC}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	1.1	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF
r _{amb} = -4	40 °C to +85 °C					
√он	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	$V_{CC}-0.1$	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	$0.7 \times V_{CC}$	-	-	V
		$I_0 = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		$I_0 = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_0 = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_0 = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V

NXP Semiconductors

74AUP1G17

Low-power Schmitt trigger

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_I = V_{T+} \text{ or } V_{T-}$				
		I_O = 20 $\mu\text{A};V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3\times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.33	V
		$I_0 = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.45	V
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.5	μΑ
ΔI_{OFF}	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
l _{cc}	supply current			-	0.9	μA
∆l _{CC}	additional supply current	$\label{eq:VI} \begin{array}{l} V_{I} = V_{CC} - 0.6 \; V; \; I_{O} = 0 \; A; \\ V_{CC} = 3.3 \; V \end{array}$	-	-	50	μA
T _{amb} = -	40 °C to +125 °C					
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_O = –20 $\mu A; V_{CC}$ = 0.8 V to 3.6 V	$V_{CC}-0.11$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_O = 20 $\mu A; V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		I_{O} = 1.7 mA; V_{CC} = 1.4 V	-	-	0.41	V
		I_{O} = 1.9 mA; V_{CC} = 1.65 V	-	-	0.39	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.36	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.50	V
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.75	μΑ
	power-off leakage current	$V_{\rm I}~{\rm or}~V_{\rm O}$ = 0 V to 3.6 V; $V_{\rm CC}$ = 0 V	-	-	±0.75	μΑ

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).											
Symbol	Parameter	Conditions	Min	Тур	Max	Unit					
ΔI_{OFF}	additional power-off leakage current		-	-	±0.75	μA					
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA					
ΔI_{CC}	additional supply current		-	-	75	μA					

Table 7. Static characteristics ...continued

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 8

Symbol	Parameter	Conditions			25 °C		-4	0 °C to +1	25 °C	Unit
				Min	Typ <mark>[1]</mark>	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 5 p	F									
t _{pd}	propagation delay	A to Y; see Figure 7	[2]							
		$V_{CC} = 0.8 V$		-	19.0	-	-	-	-	ns
		V_{CC} = 1.1 V to 1.3 V		2.6	5.7	10.6	2.5	10.9	11.1	ns
		V_{CC} = 1.4 V to 1.6 V		2.4	4.2	6.5	2.3	7.1	7.4	ns
		V _{CC} = 1.65 V to 1.95 V		2.0	3.6	5.5	1.9	6.1	6.3	ns
		V_{CC} = 2.3 V to 2.7 V		1.9	3.0	4.2	1.8	4.6	4.8	ns
		V_{CC} = 3.0 V to 3.6 V		1.8	2.7	3.6	1.5	3.8	4.0	ns
C _L = 10	pF									
t _{pd}	propagation delay	A to Y; see Figure 7	[2]							
		$V_{CC} = 0.8 V$		-	22.5	-	-	-	-	ns
		V_{CC} = 1.1 V to 1.3 V		2.9	6.6	12.4	2.7	12.9	13.0	ns
		V_{CC} = 1.4 V to 1.6 V		2.6	4.8	7.8	2.4	8.3	8.7	ns
		V _{CC} = 1.65 V to 1.95 V		2.5	4.2	6.3	2.4	6.8	7.1	ns
		V_{CC} = 2.3 V to 2.7 V		2.3	3.5	4.8	2.1	5.3	5.6	ns
		V_{CC} = 3.0 V to 3.6 V		2.1	3.3	4.4	2.0	4.6	4.8	ns
C _L = 15	pF									
t _{pd}	propagation delay	A to Y; see Figure 7	[2]							
		$V_{CC} = 0.8 V$		-	26.0	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V		3.2	7.4	14.1	3.1	14.7	14.9	ns
		V_{CC} = 1.4 V to 1.6 V		3.1	5.4	8.7	2.8	9.5	9.9	ns
		V_{CC} = 1.65 V to 1.95 V		2.7	4.7	7.1	2.7	7.8	8.2	ns
		V_{CC} = 2.3 V to 2.7 V		2.6	4.0	5.6	2.5	6.0	6.3	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.5	3.7	4.9	2.2	5.2	5.5	ns

74AUP1G17

Low-power Schmitt trigger

Symbol	Parameter	Conditions		25 °C		–40 °C to +125 °C			Unit	
				Min	Typ <mark>[1]</mark>	Мах	Min	Max (85 °C)	Max (125 °C)	
C _L = 30	pF									
t _{pd}	propagation delay	A to Y; see Figure 7	[2]							
		$V_{CC} = 0.8 V$		-	36.3	-	-	-	-	ns
		V_{CC} = 1.1 V to 1.3 V		3.9	9.7	19.0	3.7	19.8	20.1	ns
		V_{CC} = 1.4 V to 1.6 V		3.5	7.0	11.2	3.6	12.4	13.0	ns
		V_{CC} = 1.65 V to 1.95 V		3.5	6.0	9.2	3.4	10.1	10.7	ns
		V_{CC} = 2.3 V to 2.7 V		3.4	5.1	7.0	3.2	7.5	7.9	ns
		V_{CC} = 3.0 V to 3.6 V		3.3	4.8	6.2	3.1	7.1	7.5	ns
C _L = 5 pl	F, 10 pF, 15 pF and	30 pF								
C _{PD}	power dissipation	f = 1 MHz; V_I = GND to V_{CC}	[3]							
	capacitance	$V_{CC} = 0.8 V$		-	2.5	-	-	-	-	pF
		V_{CC} = 1.1 V to 1.3 V		-	2.7	-	-	-	-	pF
		V_{CC} = 1.4 V to 1.6 V		-	2.8	-	-	-	-	pF
		V_{CC} = 1.65 V to 1.95 V		-	3.0	-	-	-	-	pF
		V_{CC} = 2.3 V to 2.7 V		-	3.5	-	-	-	-	pF
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		-	4.0	-	-	-	-	рF

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 8

[1] All typical values are measured at nominal V_{CC}.

[2] t_{pd} is the same as t_{PLH} and t_{PHL}

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $\mathsf{P}_{D}=C_{PD}\times V_{CC}{}^{2}\times f_{i}\times N$ + $\Sigma(C_{L}\times V_{CC}{}^{2}\times f_{o})$ where:

 $f_i = input frequency in MHz;$

 f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

$$\begin{split} N &= number \mbox{ of inputs switching;} \\ \Sigma(C_L \times V_{CC}{}^2 \times f_o) &= sum \mbox{ of the outputs.} \end{split}$$



12. Waveforms

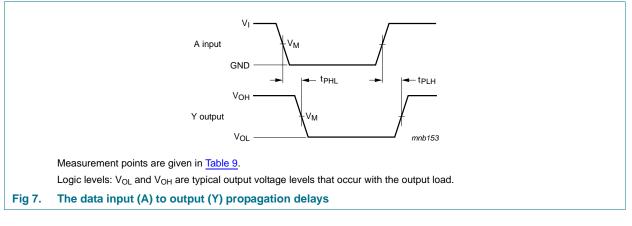


Table 9.Measurement points

Supply voltage	Output	Input					
V _{CC}	V _M	V _M	VI	$t_r = t_f$			
0.8 V to 3.6 V	$0.5 imes V_{CC}$	$0.5 imes V_{CC}$	V _{CC}	\leq 3.0 ns			

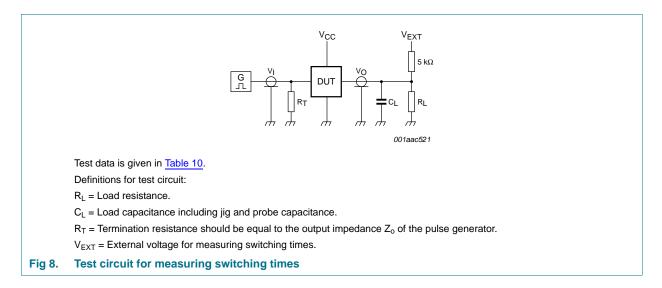


Table 10. Test data

Supply voltage	Load		V _{EXT}		
V _{cc}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 $M\Omega$	open	GND	$2\times V_{CC}$

[1] For measuring enable and disable times $R_L = 5 \text{ k}\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 \text{ M}\Omega$.

13. Transfer characteristics

Table 11. Transfer characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
۳ _{amb} = 25 °	O					
/ _{T+}	positive-going	see Figure 9 and Figure 10				
three	threshold voltage	$V_{CC} = 0.8 V$	0.30	-	0.60	V
		V _{CC} = 1.1 V	0.53	-	0.90	V
		$V_{CC} = 1.4 V$	0.74	-	1.11	V
		V _{CC} = 1.65 V	0.91	-	1.29	V
		$V_{CC} = 2.3 V$	1.37	-	1.77	V
		$V_{CC} = 3.0 V$	1.88	-	2.29	V
′T–	negative-going	see Figure 9 and Figure 10				
	threshold voltage	$V_{CC} = 0.8 V$	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		$V_{CC} = 1.4 V$	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	V
		$V_{CC} = 3.0 V$	0.88	-	1.24	V
′н	hysteresis voltage	see <u>Figure 9,</u> Figure 10, Figure 11 and <u>Figure 12</u>				
		$V_{CC} = 0.8 V$	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		$V_{CC} = 2.3 V$	0.53	-	0.92	V
		$V_{CC} = 3.0 V$	0.79	-	1.31	V
amb = -40	°C to +85 °C					
V _{T+} positive-going threshold voltage	see Figure 9 and Figure 10					
	threshold voltage	$V_{CC} = 0.8 V$	0.30	-	0.60	V
		V _{CC} = 1.1 V	0.53	-	0.90	V
		$V_{CC} = 1.4 V$	0.74	-	1.11	V
		V _{CC} = 1.65 V	0.91	-	1.29	V
		$V_{CC} = 2.3 V$	1.37	-	1.77	V
		$V_{CC} = 3.0 V$	1.88	-	2.29	V
Т–	negative-going threshold voltage	see Figure 9 and Figure 10				
		$V_{CC} = 0.8 V$	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		$V_{CC} = 1.4 V$	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		$V_{CC} = 2.3 V$	0.69	-	1.04	V
		$V_{CC} = 3.0 V$	0.88	-	1.24	V
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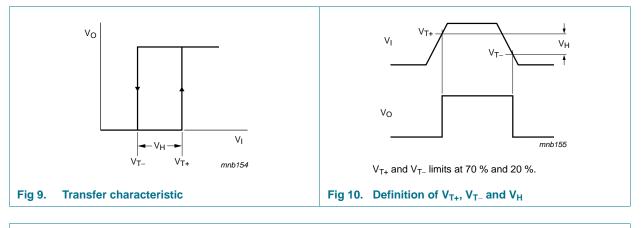
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _H hyster	hysteresis voltage	see <u>Figure 9, Figure 10,</u> <u>Figure 11</u> and <u>Figure 12</u>				
		$V_{CC} = 0.8 V$	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		$V_{CC} = 1.4 V$	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		$V_{CC} = 2.3 V$	0.53	-	0.92	V
		$V_{CC} = 3.0 V$	0.79	-	1.31	V
T _{amb} = -40	°C to +125 °C					
V _{T+}	positive-going	see Figure 9 and Figure 10				
	threshold voltage	$V_{CC} = 0.8 V$	0.30	-	0.62	V
		V _{CC} = 1.1 V	0.53	-	0.92	V
		$V_{CC} = 1.4 V$	0.74	-	1.13	V
		V _{CC} = 1.65 V	0.91	-	1.31	V
		$V_{CC} = 2.3 V$	1.37	-	1.80	V
		$V_{CC} = 3.0 V$	1.88	-	2.32	V
	negative-going	see Figure 9 and Figure 10				
	threshold voltage	$V_{CC} = 0.8 V$	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		$V_{CC} = 1.4 V$	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		$V_{CC} = 2.3 V$	0.69	-	1.04	V
		$V_{CC} = 3.0 V$	0.88	-	1.24	V
V _H	hysteresis voltage	see <u>Figure 9, Figure 10,</u> Figure 11 and <u>Figure 12</u>				
		$V_{CC} = 0.8 V$	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		$V_{CC} = 1.4 V$	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		$V_{CC} = 2.3 V$	0.53	-	0.92	V
		$V_{CC} = 3.0 V$	0.79	-	1.31	V

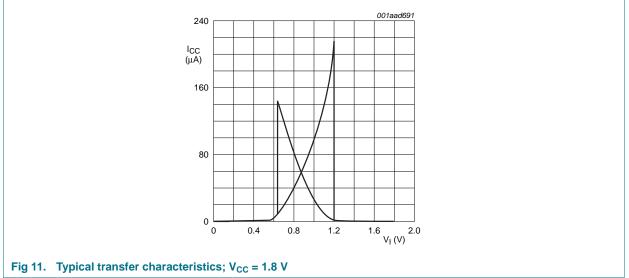
Table 11. Transfer characteristics ... continued

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14. Waveforms transfer characteristics



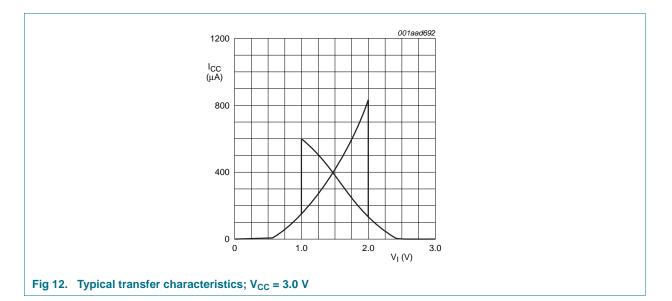


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15. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i \times (t_r \times I_{CC(AV)} + t_f \times I_{CC(AV)}) \times V_{CC}$ where:

 P_{ad} = additional power dissipation (μ W);

 $f_i = input frequency (MHz);$

 t_r = input rise time (ns); 10 % to 90 %;

 t_f = input fall time (ns); 90 % to 10 %;

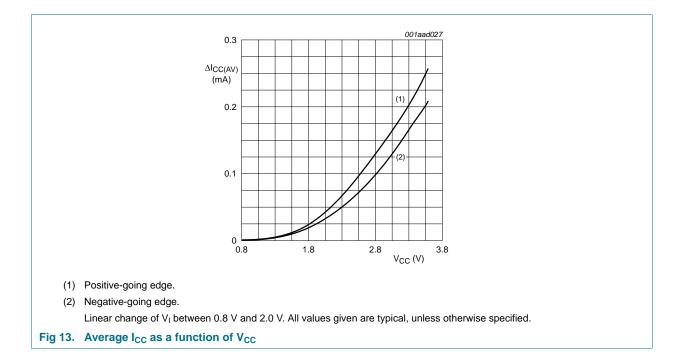
 $I_{CC(AV)}$ = average additional supply current (µA).

Average I_{CC} differs with positive or negative input transitions, as shown in Figure 13.

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16. Package outline

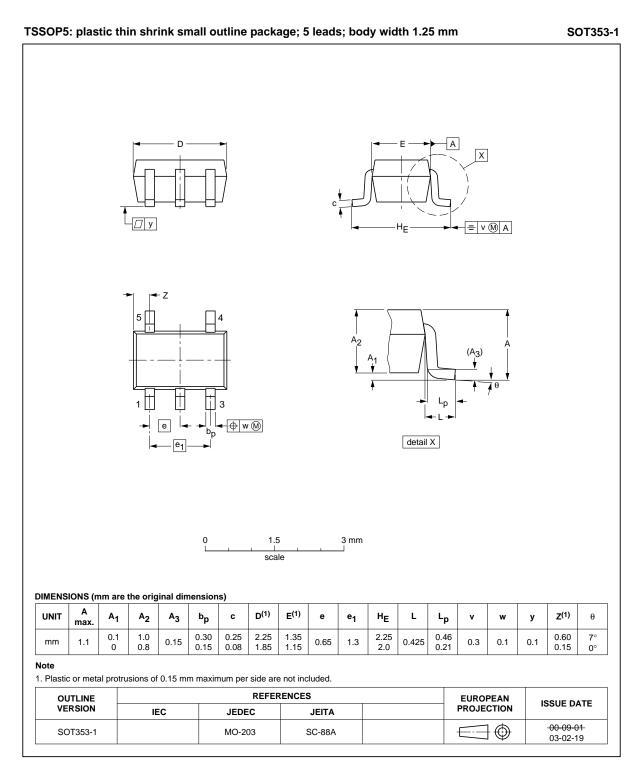


Fig 14. Package outline SOT353-1 (TSSOP5)

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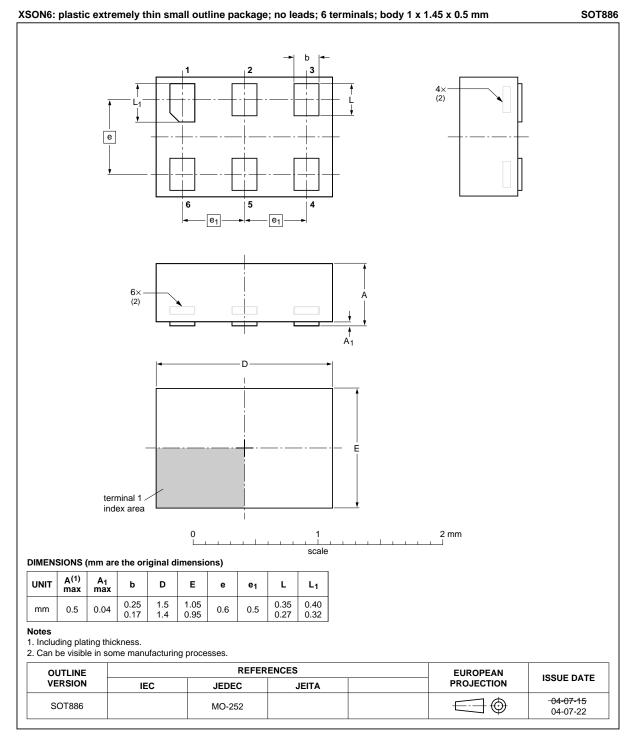


Fig 15. Package outline SOT886 (XSON6)



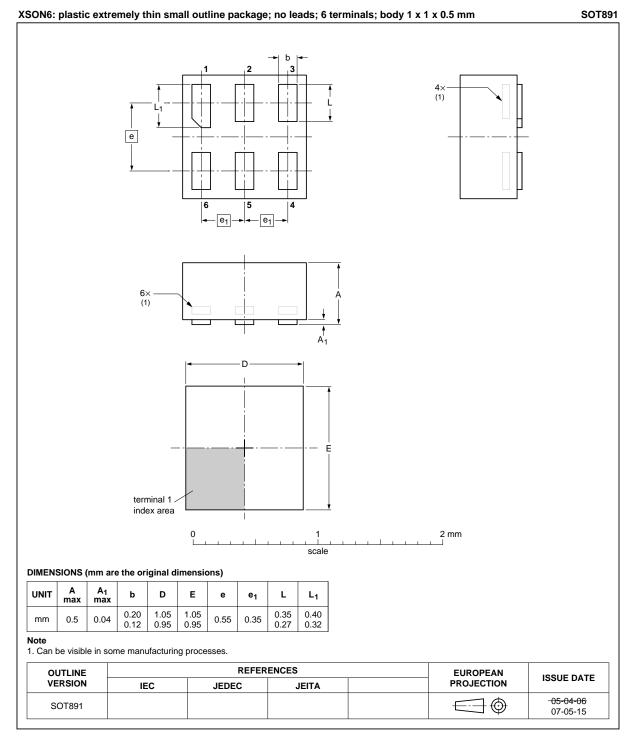
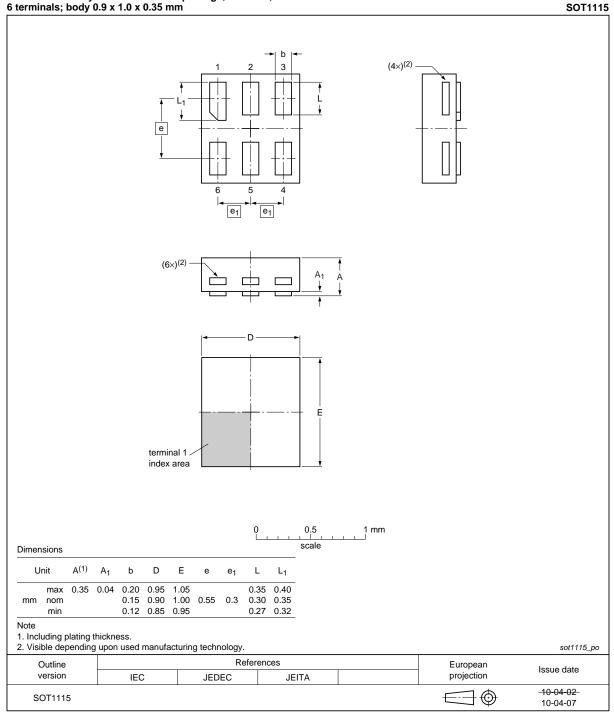


Fig 16. Package outline SOT891 (XSON6)

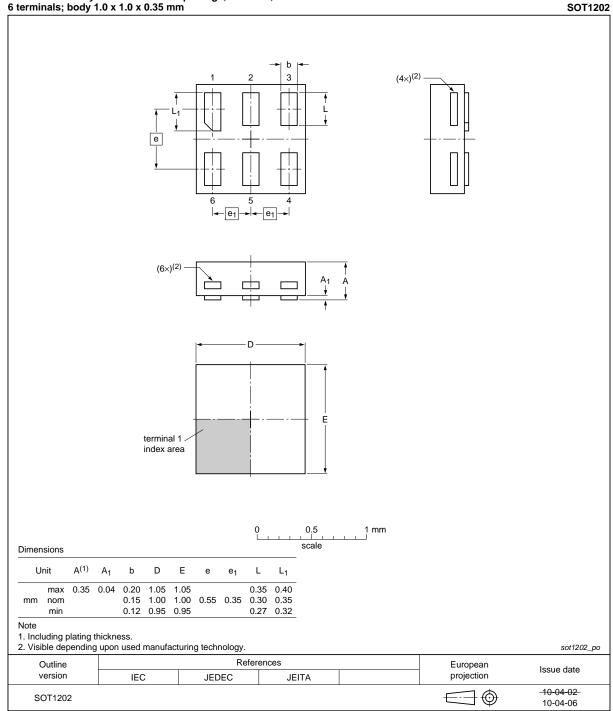




XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 17. Package outline SOT1115 (XSON6)





XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 18. Package outline SOT1202 (XSON6)



17. Abbreviations

Table 12. Abbro	eviations
Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

18. Revision history

Table 13. Revision histo	Ul y			
Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G17 v.4	20100715	Product data sheet	-	74AUP1G17 v.3
Modifications:	 Added type numl 	ber 74AUP1G17GN (SOT1115/X	SON6 package).	
	 Added type numl 	ber 74AUP1G17GS (SOT1202/X	SON6 package).	
74AUP1G17 v.3	20090710	Product data sheet	-	74AUP1G17 v.2
74AUP1G17 v.2	20060727	Product data sheet	-	74AUP1G17 v.1
74AUP1G17 v.1	20050726	Product data sheet	-	-



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19.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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