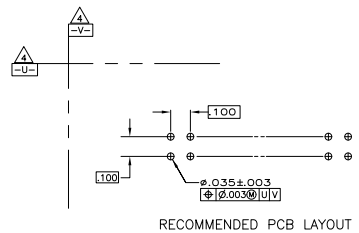
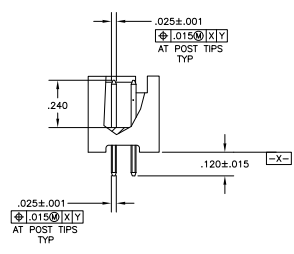
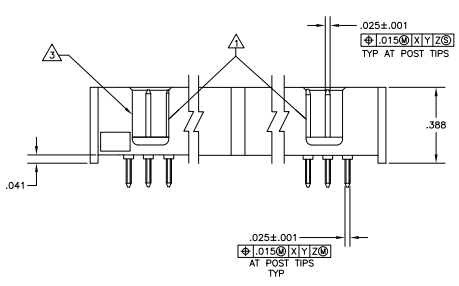
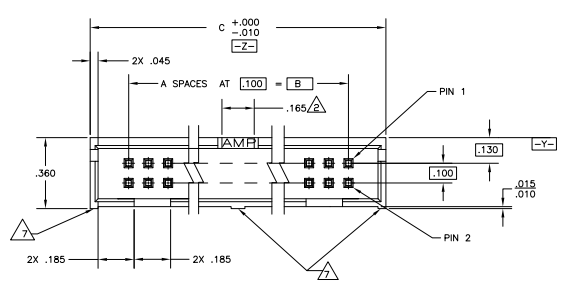


THIS DRAWING IS UNPUBLISHED
 ALL RIGHTS RESERVED

REVISES		DATE	BY	APP'D
GP	00			
ET				

- △ DOUBLE BAR POLARIZATION SLOTS.
- △ CENTER POLARIZATION SLOT.
- △ FOR 10 POSN ASSEMBLY, SLOT OMITTED IN THIS LOCATION.
- △ DATUMS U AND V TO BE ESTABLISHED BY THE CUSTOMER.
- △ HOUSING: GLASS FILLED NYLON OR PBT, BLACK, UL 94V-0 RATED.
POSTS: BRASS.
- △ POSTS: GOLD FLASH OVER PALLADIUM-NICKEL PLATE, .000030 MIN TOTAL ON THE MATING SURFACES, .000100 MIN TIN PLATE ON THE SOLDER-TAILS, .000050 MIN NICKEL UNDERPLATE ON THE ENTIRE POST.
-OR-
.000030 MIN GOLD PLATE ON THE MATING SURFACES, .000100 MIN TIN PLATE ON THE SOLDER-TAILS, .000050 MIN NICKEL UNDERPLATE ON THE ENTIRE POST.
- △ RIBS:
ALL POSITION SIZES: 2 LOCATED AT ENDS OF HOUSING.
20 THRU 34 POSITION SIZES: 1 ADDITIONAL RIB CENTERED ON HOUSING.
40 THRU 50 POSITION SIZES: 2 ADDITIONAL RIBS EVENLY SPACED WITHIN REMAINING LENGTH OF HOUSING.



RECOMMENDED PCB LAYOUT

2.800	2.400	24	50	1-5103309-0
2.300	1.900	19	40	5103309-8
2.000	1.600	16	34	5103309-7
1.600	1.200	12	26	5103309-6
1.300	.900	9	20	5103309-5
1.100	.700	7	16	5103309-3
1.000	.600	6	14	5103309-2
.800	.400	4	10	5103309-1

THIS DRAWING IS A CONTROLLED DOCUMENT.

DESIGNED BY	DATE	REVISED BY	DATE
108-40018			

TE Connectivity
 HEADER ASSY, VERTICAL, LOW PROFILE, AMP-LATCH

SIZE: A1 DATE: 00779 DRAWING NO: 5103309

CUSTOMER DRAWING