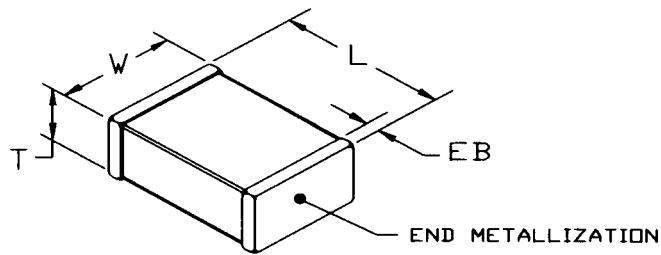


ROHS COMPLIANT



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	X7R
RATED VOLTAGE	50 VDC
CAPACITANCE RANGE (EIA)	10 nFd (102)
CAPACITANCE TOLERANCE	K (± 10%)
TEST PARAMETERS	1 kHz ±50 Hz @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	±15% (-55°C TO +125°C)
DISSIPATION FACTOR	< 2.5% @ 1kHz, 25°C
INSULATION RESISTANCE	> 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	2.5 x WVDC, 50 mA max

MECHANICAL CHARACTERISTICS

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.040±.004	.020±.004	.025 MAX	.008±.004

TERMINATION DESCRIPTION:


TERMINATION: V - NICKEL BARRIER WITH 100% TIN OVERPLATE

PACKAGING DESCRIPTION:

PACKAGE: T - 7" PAPER TAPE/REEL

MARKING DESCRIPTION:

4 - UNMARKED

						15191 BLEDSOE ST SYLMAR, CA 91342	
				TITLE			
				MULTILAYER CHIP CAPACITOR			
REV	DESCRIPTION	APPROVED	DATE	PART NO.			DWG SCALE
				500R07W103KV4T			NONE
ENG. MGR.	DATE	Q.A. MGR.	DATE	DWG. NO.			SHEET
				500R07W103KV4T			RE V
C. ENGLAND	11/24/09						1 OF 1