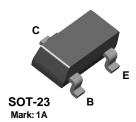


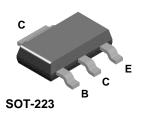
2N3904

MMBT3904

PZT3904







NPN General Purpose Amplifier

This device is designed as a general purpose amplifier and switch. The useful dynamic range extends to 100 mA as a switch and to 100 MHz as an amplifier.

Absolute Maximum Ratings* $T_{\Delta} = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	40	V
V _{CBO}	Collector-Base Voltage	60	V
V _{EBO}	Emitter-Base Voltage	6.0	V
I _C	Collector Current - Continuous	200	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics $T_A = 25$ °C unless otherwise noted

Symbol	Characteristic	Max		Units	
		2N3904	*MMBT3904	**PZT3904	
P_D	Total Device Dissipation	625	350	1,000	mW
	Derate above 25°C	5.0	2.8	8.0	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

¹⁾ These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

^{**}Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm².

(continued)

Electrical Characteristics	T, = 25°C unless otherwise noted
----------------------------	----------------------------------

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHAP	RACTERISTICS				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C = 1.0 \text{ mA}, I_B = 0$	40		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = 10 \mu A, I_E = 0$	60		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	6.0		V
I _{BL}	Base Cutoff Current	$V_{CE} = 30 \text{ V}, V_{EB} = 3 \text{V}$		50	nA
I _{CEX}	Collector Cutoff Current	V _{CE} = 30 V, V _{EB} = 3V		50	nA

ON CHARACTERISTICS*

h _{FE}	DC Current Gain	$I_C = 0.1 \text{ mA}, V_{CE} = 1.0 \text{ V}$	40		
		$I_C = 1.0 \text{ mA}, V_{CE} = 1.0 \text{ V}$	70		
		$I_C = 10 \text{ mA}, V_{CE} = 1.0 \text{ V}$	100	300	
		$I_C = 50 \text{ mA}, V_{CE} = 1.0 \text{ V}$	60		
		$I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}$	30		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$		0.2	V
		$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		0.3	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$	0.65	0.85	V
	_	$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		0.95	V

SMALL SIGNAL CHARACTERISTICS

f _T	Current Gain - Bandwidth Product	$I_C = 10 \text{ mA}, V_{CE} = 20 \text{ V},$ f = 100 MHz	300		MHz
C _{obo}	Output Capacitance	$V_{CB} = 5.0 \text{ V}, I_{E} = 0,$ f = 1.0 MHz		4.0	pF
C _{ibo}	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_{C} = 0,$ f = 1.0 MHz		8.0	pF
NF	Noise Figure	$I_C = 100 \mu A$, $V_{CE} = 5.0 V$, $R_S = 1.0 k \Omega$. $f = 10 Hz$ to 15.7 kHz		5.0	dB

SWITCHING CHARACTERISTICS

t _d	Delay Time	$V_{CC} = 3.0 \text{ V}, V_{BE} = 0.5 \text{ V},$	35	ns
t _r	Rise Time	$I_C = 10 \text{ mA}, I_{B1} = 1.0 \text{ mA}$	35	ns
t _s	Storage Time	$V_{CC} = 3.0 \text{ V}, I_{C} = 10 \text{mA}$	200	ns
t _f	Fall Time	$I_{B1} = I_{B2} = 1.0 \text{ mA}$	50	ns

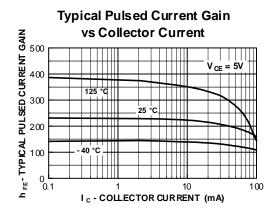
^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

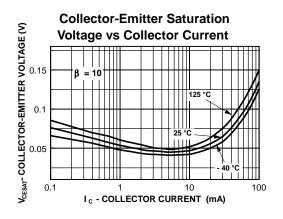
Spice Model

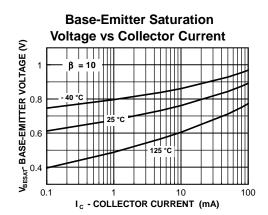
 $NPN \ (Is=6.734f \ Xti=3 \ Eg=1.11 \ Vaf=74.03 \ Bf=416.4 \ Ne=1.259 \ Is=6.734 \ Ikf=66.78m \ Xtb=1.5 \ Br=.7371 \ Nc=2 \ Isc=0 \ Ikr=0 \ Rc=1 \ Cjc=3.638p \ Mjc=.3085 \ Vjc=.75 \ Fc=.5 \ Cje=4.493p \ Mje=.2593 \ Vje=.75 \ Tr=239.5n \ Tf=301.2p \ Itf=.4 \ Vtf=4 \ Vtf=4 \ Xtf=2 \ Rb=10)$

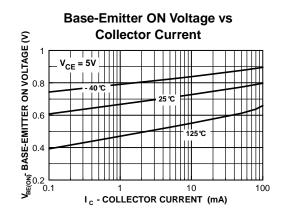
(continued)

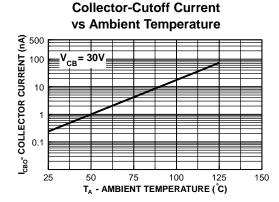
Typical Characteristics

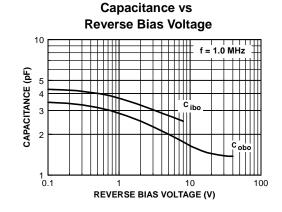






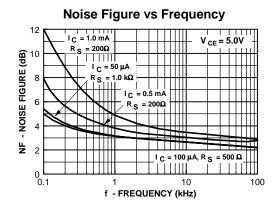


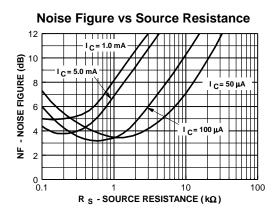


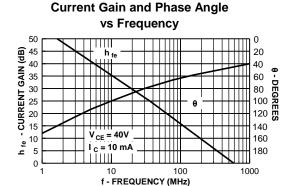


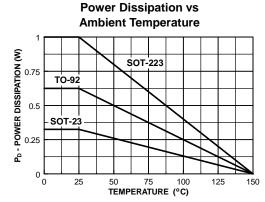
(continued)

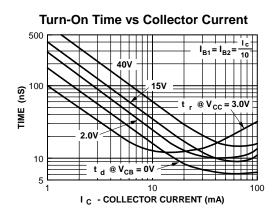
Typical Characteristics (continued)

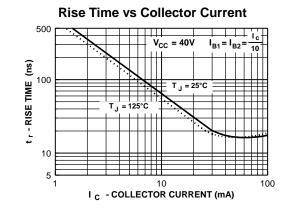








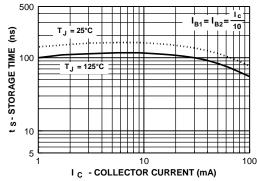




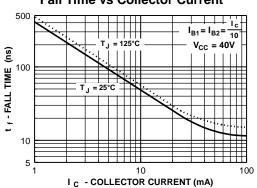
(continued)

Typical Characteristics (continued)

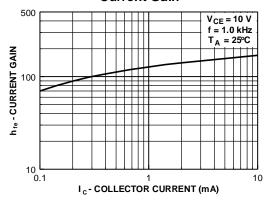




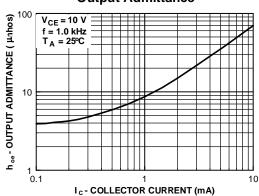
Fall Time vs Collector Current



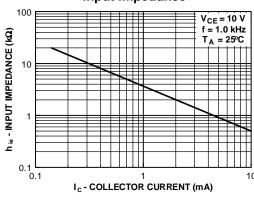
Current Gain



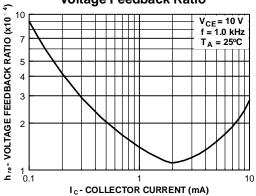
Output Admittance



Input Impedance



Voltage Feedback Ratio



(continued)

Test Circuits

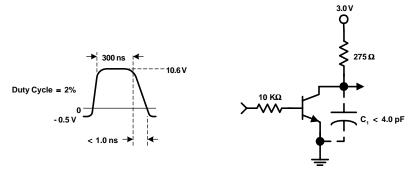


FIGURE 1: Delay and Rise Time Equivalent Test Circuit

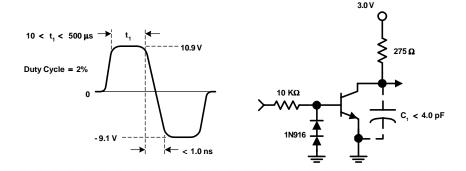


FIGURE 2: Storage and Fall Time Equivalent Test Circuit

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $ACEx^{TM}$ FASTr™ PowerTrench® SyncFET™ Bottomless™ QFET™ TinyLogic™ GlobalOptoisolator™ QSTM UHC™ CoolFET™ GTO™ **VCX**TM $CROSSVOLT^{TM}$ QT Optoelectronics™ HiSeC™

DOME™ ISOPLANAR™ Quiet Series™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

 A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. G