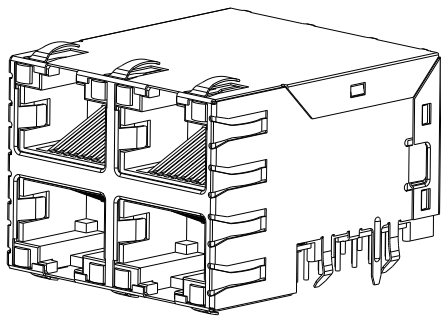
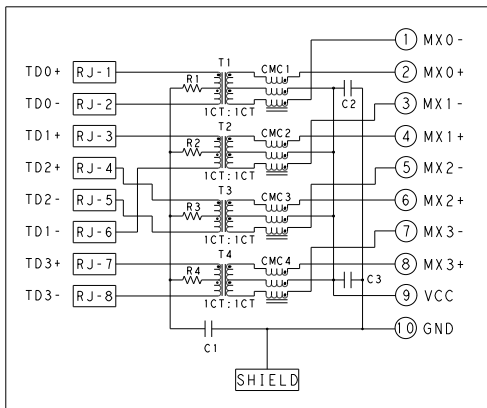


REV	DATE	DESCRIPTION	BY	CHK	APP
B	ECO-10-021273		ZHANG	JC	RZ
C	ECO-11-090404		ZHANG	GA	RZ
D	ECO-11-011433		ZHANG	YZ	LJ



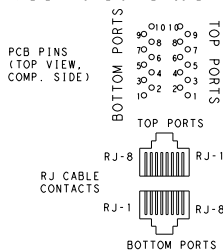
S8G56 GIGABIT CIRCUIT  $\Delta \Delta$   
TOP AND BOTTOM PORTS



C1 = 1000pF, 2kV CAPACITOR  
C2 - C3 = 10nF, 50V CAPACITORS  
R1-R4 = 75 Ohms, 1/16 W, RESISTORS

PIN DESIGNATIONS

(REPEAT FOR EACH VERTICAL PAIR OF PORTS)



- 1 MATERIALS:
- HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
  - SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
  - CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
  - LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME LEADS, PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL OVER 1.02um MIN COPPER UNDERPLATE, POST PLATED WITH 2.54um MIN MATTE TIN AND/OR 2.54um MIN SAC SOLDER DIP OR PURE TIN SOLDER DIP.

- $\Delta$  MAGNETICS
- APPLICATION: 10/100/1000 BASE-T
  - IMPEDANCE: 100 OHMS
  - TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
  - OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
  - ALL FOUR PAIRS BI-DIRECTIONAL
  - PERFORMANCE @ 25 °C:
    - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
    - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
    - 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
    - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
    - 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
    - COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
  - ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND WITH ALL PORTS CONNECTED..

- $\Delta$  PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK, WITH SUNDAY=1

- $\Delta$  TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.

- $\Delta$  RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.

- $\Delta$  LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20mA.
- DOMINANT WAVELENGTH (LD)
  - GREEN, 568nm TYP AT IF=20mA
  - YELLOW, 588nm TYP AT IF=20mA
  - FORWARD VOLTAGE (VF)
  - GREEN, 2.2V TYP AT IF=20mA
  - YELLOW, 2.1V TYP AT IF=20mA

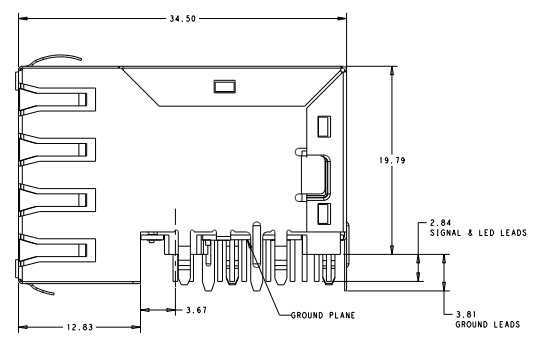
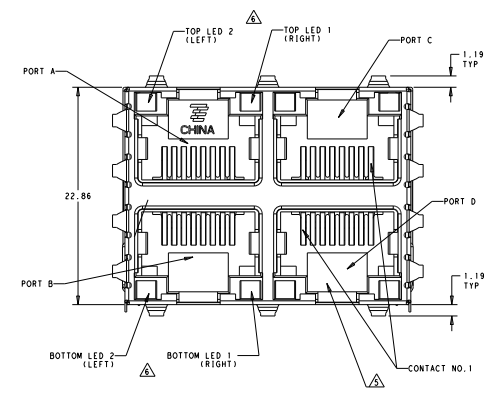
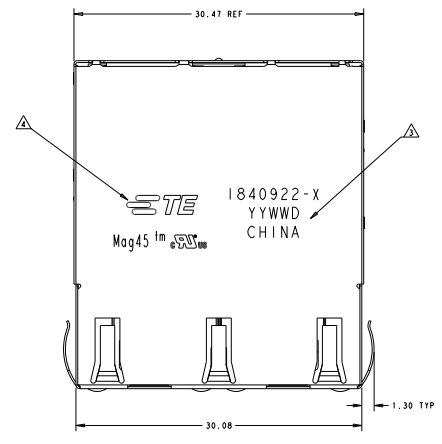
- $\Delta$  INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

- 8 OPERATING TEMPERATURE: FROM 0 °C TO 70 °C

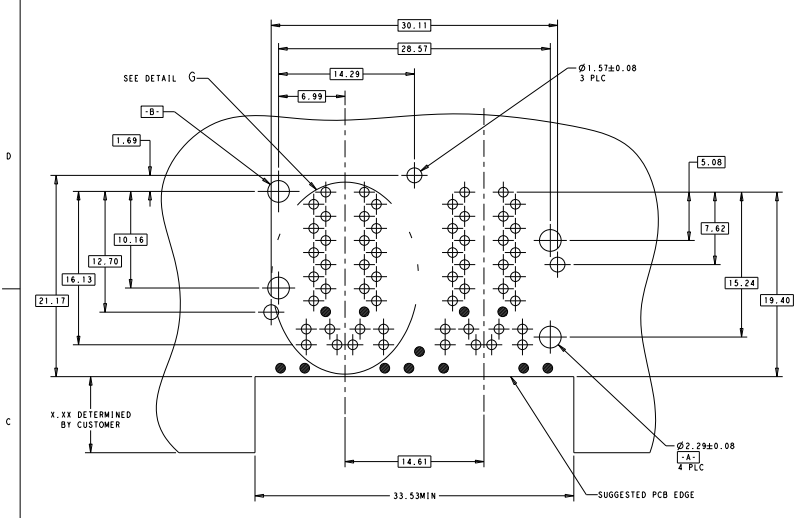
- 9 THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260 °C MAX, 10 SECONDS MAX.

GREEN	YELLOW	GREEN	YELLOW	1	1840922-3
GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	1	1840922-1
BOTTOM LED 2 (LEFT)	BOTTOM LED 1 (RIGHT)	TOP LED 2 (LEFT)	TOP LED 1 (RIGHT)	REAR PCB GND TABS	PART NUMBER

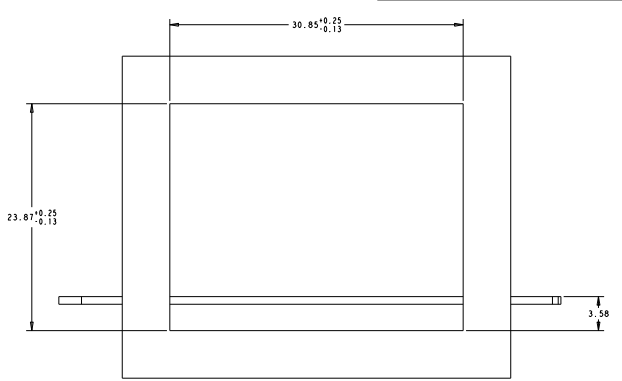
THIS DRAWING IS A CONTROLLED DOCUMENT.		REV: 000001	DATE: 20080801	BY: RICHARD GUN	CHK: ZHANG	APP: ZHANG
FUNCTIONAL	DESIGNED BY: ZHANG	DESIGNED DATE: 20080801	DESIGNED BY: ZHANG	DESIGNED DATE: 20080801	DESIGNED BY: ZHANG	DESIGNED DATE: 20080801
REV	DATE	DESCRIPTION	BY	CHK	APP	
1	20080801	2X2 MAG451(M) MODULAR JACK, OFFSET S8G56 GIGABIT CIRCUIT, W/ LED'S SHIELDED, WAVE PANEL GROUND TABS	ZHANG	JC	RZ	
TITLE: S8G56 GIGABIT CIRCUIT, W/ LED'S SHIELDED, WAVE PANEL GROUND TABS						
PART NUMBER: 1840922-3						
SCALE: 1:1						
CUSTOMER DRAWING						



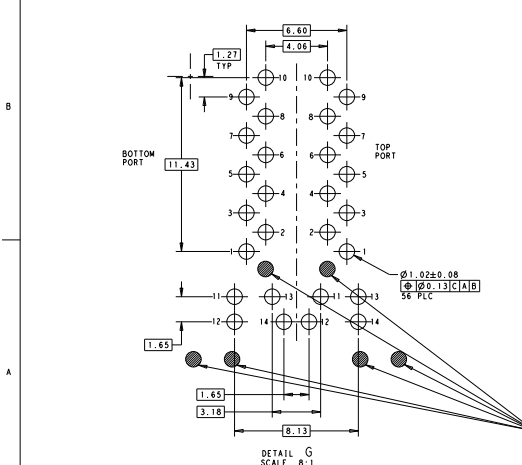
THIS DRAWING IS A CONTROLLED DOCUMENT.		REV: RICHARD GLO (000000)	DATE: 11/01/2007	REV: RICHARD GLO (000000)	DATE: 11/01/2007
DRAWN BY: RICHARD GLO (000000)		DESIGNED BY: RICHARD GLO (000000)	CHECKED BY: RICHARD GLO (000000)	DATE: 11/01/2007	DATE: 11/01/2007
MATERIAL: 1840922-X		PART NAME: 2X2 MAG45(1M) MODULAR JACK, OFFSET SHIELDED, WAVE PANEL GROUND TABS			
QUANTITY: 1		TOTAL QTY FOR THIS DRAWING: 1			
SCALE: 4:1		SHEET: 2 OF 4			



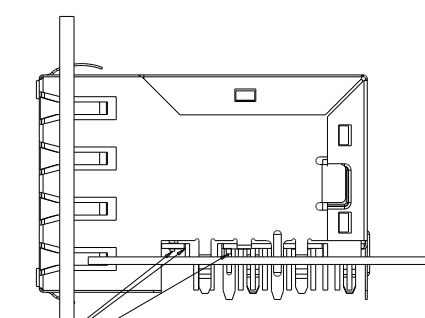
RECOMMENDED PC BOARD LAYOUT VIEW FROM COMPONENT SIDE



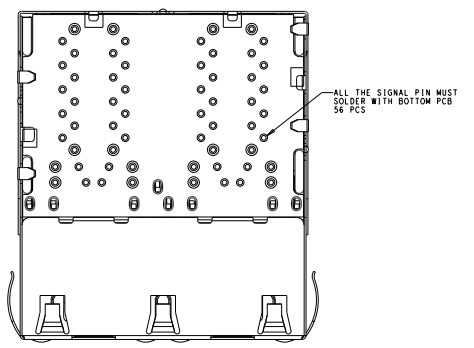
RECOMMENDED PANEL CUT-OUT



DETAIL G SCALE 8:1



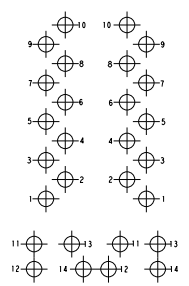
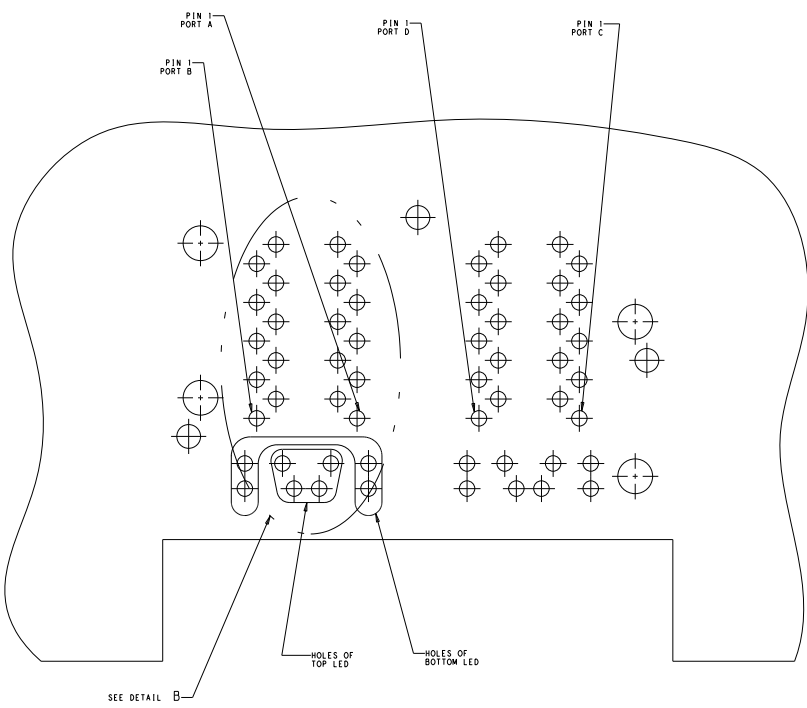
PIN ON BOTTOM LED'S PIN ON MOD JACK AND BOTTOM PCB ARE STUB PINS WITH LENGTH SIMILAR TO PCB STANDOFF HEIGHT PRECAUTIONS SHOULD BE TAKEN IN PCB DESIGN TO GUARD AGAINST SHORTING TRACES TO STUB PINS



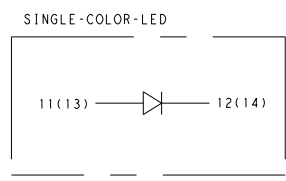
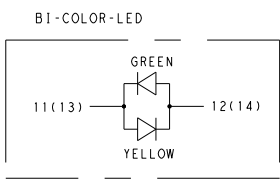
ALL THE SIGNAL PIN MUST SOLDER WITH BOTTOM PCB 56 PCS

THIS DRAWING IS A CONTROLLED DOCUMENT		REV: RICHARD.GLO (000000)	DATE: 2/20/2007	REV: RICHARD.GLO (000000)	DATE: 2/20/2007
DESIGNED BY: RICHARD.GLO	DESIGNED DATE: 2/20/2007	DESIGNED BY: RICHARD.GLO	DESIGNED DATE: 2/20/2007	DESIGNED BY: RICHARD.GLO	DESIGNED DATE: 2/20/2007
CHECKED BY: RICHARD.GLO	CHECKED DATE: 2/20/2007	CHECKED BY: RICHARD.GLO	CHECKED DATE: 2/20/2007	CHECKED BY: RICHARD.GLO	CHECKED DATE: 2/20/2007
APPROVED BY: RICHARD.GLO	APPROVED DATE: 2/20/2007	APPROVED BY: RICHARD.GLO	APPROVED DATE: 2/20/2007	APPROVED BY: RICHARD.GLO	APPROVED DATE: 2/20/2007
DATE: 2/20/2007	TIME: 10:00 AM	DATE: 2/20/2007	TIME: 10:00 AM	DATE: 2/20/2007	TIME: 10:00 AM
PROJECT: 100779	DESCRIPTION: 2X2 MAGASITM MODULAR JACK, OFFSET SHIELDED, WAVE PANEL GROUND TABS	PROJECT: 100779	DESCRIPTION: 2X2 MAGASITM MODULAR JACK, OFFSET SHIELDED, WAVE PANEL GROUND TABS	PROJECT: 100779	DESCRIPTION: 2X2 MAGASITM MODULAR JACK, OFFSET SHIELDED, WAVE PANEL GROUND TABS
DRAWN BY: RICHARD.GLO	DRAWN DATE: 2/20/2007	DRAWN BY: RICHARD.GLO	DRAWN DATE: 2/20/2007	DRAWN BY: RICHARD.GLO	DRAWN DATE: 2/20/2007
CUSTOMER DRAWING	SCALE: 5:1	SHEET: 3	TOTAL SHEETS: 4	REV: 0	

REVOLUTIONS		DATE	BY	TYPE
1	SEE SHEET 1	-	-	-



LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE



THIS DRAWING IS A CONTROLLED DOCUMENT.		REV: 1	DATE: 08/2007	BY: RICHARD GIL	FOR: 212 MAGASITM MODULAR JACK, OFFSET
DRAWN BY: FRANK LIU		DATE: 08/2007	BY: FRANK LIU	FOR: 212 MAGASITM MODULAR JACK, OFFSET	212 MAGASITM MODULAR JACK, OFFSET
CHECKED BY: FRANK LIU		DATE: 08/2007	BY: FRANK LIU	FOR: 212 MAGASITM MODULAR JACK, OFFSET	212 MAGASITM MODULAR JACK, OFFSET
APPROVED BY: FRANK LIU		DATE: 08/2007	BY: FRANK LIU	FOR: 212 MAGASITM MODULAR JACK, OFFSET	212 MAGASITM MODULAR JACK, OFFSET
MATERIAL: -		REVISION: A	DATE: 08/2007	BY: FRANK LIU	FOR: 212 MAGASITM MODULAR JACK, OFFSET
CUSTOMER DRAWING		SCALE: 8:1	SHEET: 4	OF: 4	NO: D